



EFR32ZG14 Radio Board

SubGHz 13 dBm, VDCDC to PAVDD

Board Function	Page
Title Page	1
RF, Antenna and Power	2
I/O Port Connections	3
WSTK Connectors & Board ID	4

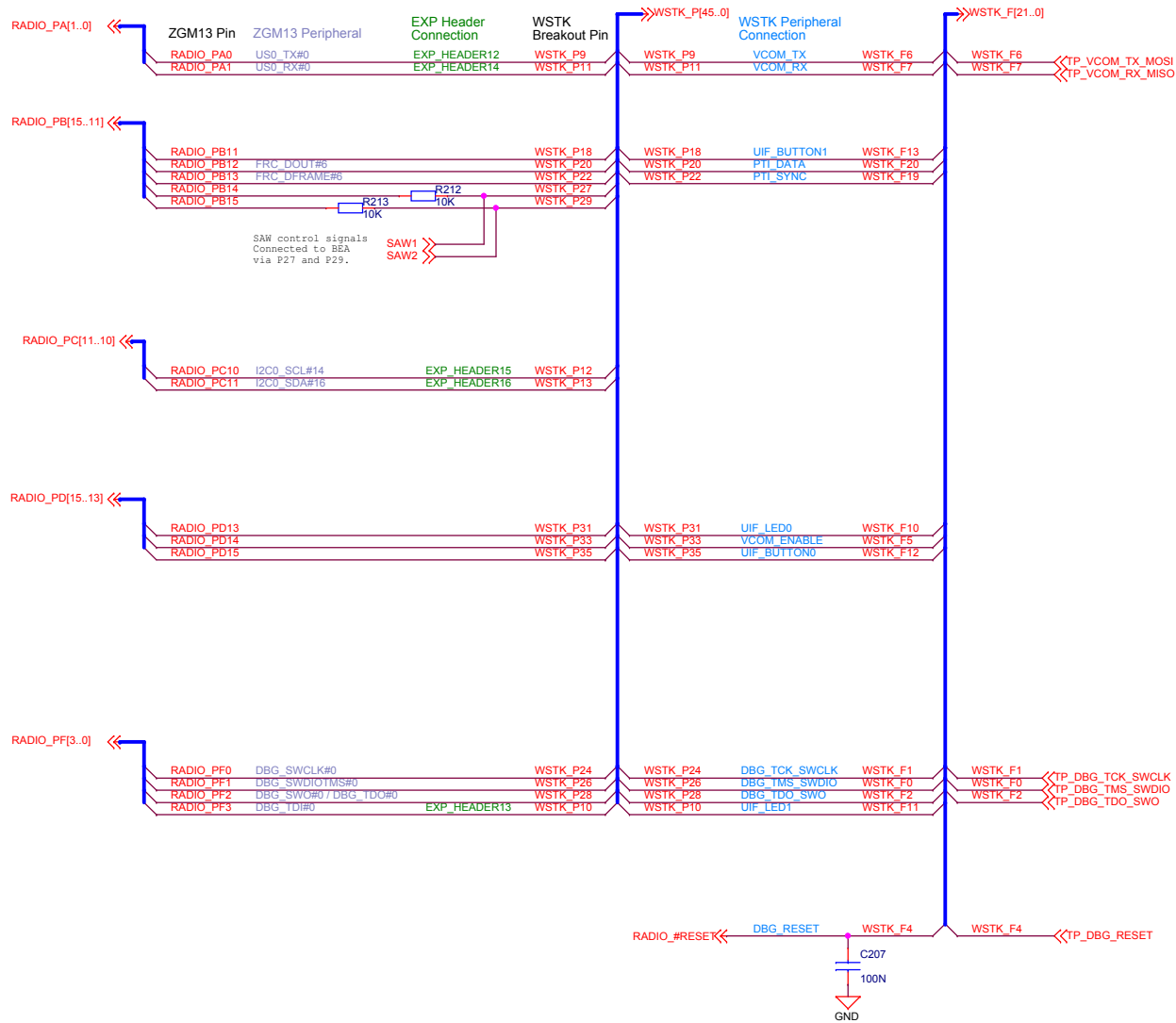
Revision History	
Rev.	Description
A00	Changed R1, R2 and C21 to 56pF. Initial release revision.
A01	Release for build with recommended PCB stackup.
A02	Fixing S3 symbol and conn. Updated pinmap, filter comps., ant. match., L100.
A03	RF output connected to the SMA.
A04	PCB stackup/material update.

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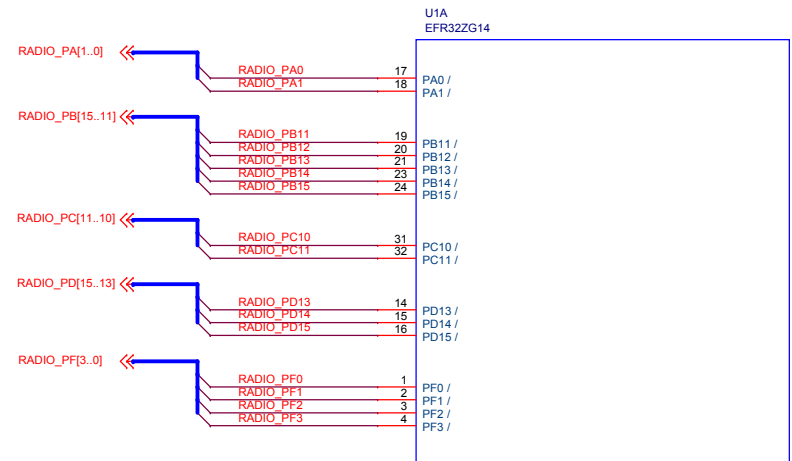


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Size A3		BOM Doc No: BRD4201A			Revision A04
Design Created Date: Monday, April 02, 2012		Sheet Created Date Monday, April 02, 2012		Sheet Modified Date Monday, September 24, 2018	
				Sheet 1 of 4	

Pin Mapping

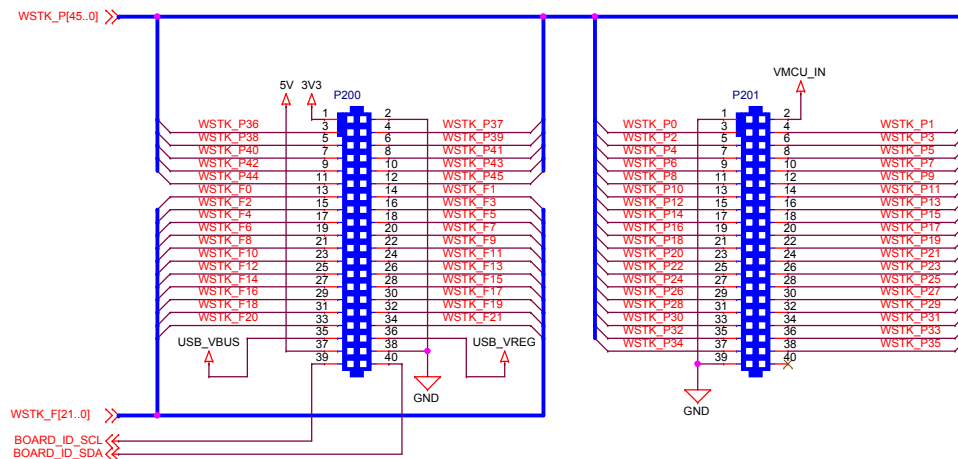


I/O Port Pins

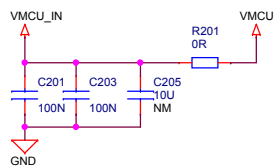


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		EFR32ZG14 Radio Board	
Designed: TAB		Page Title	
Size A3		I/O Port Connections	
BOM Doc No:		Document number	Revision
Design Created Date: Monday, April 02, 2012		BRD4201A	A04
Sheet Created Date: Wednesday, April 11, 2012		Sheet Modified Date: Monday, September 24, 2018	Sheet 3 of 4

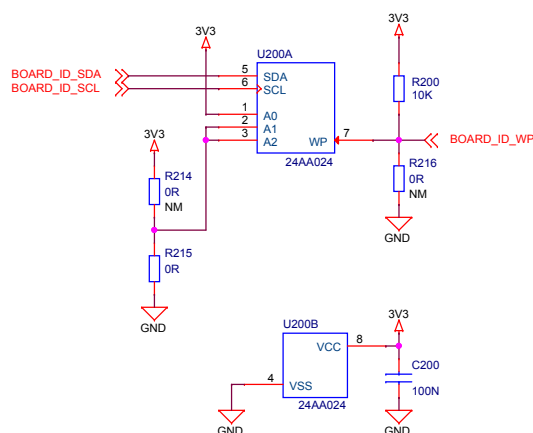
WSTK Connectors



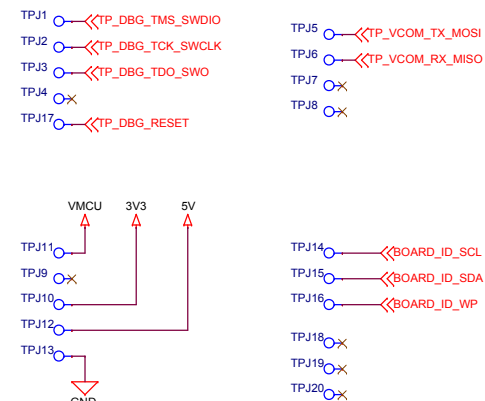
WSTK Power Decoupling



Board Identification



Test Points



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		EFR32ZG14 Radio Board	
Designed: TAB		Page Title	
Size A3		WSTK Connectors and Board ID	
BOM Doc No:		Document number	Revision
Design Created Date: Monday, April 02, 2012		BRD4201A	A04
Sheet Created Date: Tuesday, April 03, 2012		Sheet Modified Date: Monday, September 24, 2018	Sheet 4 of 4