



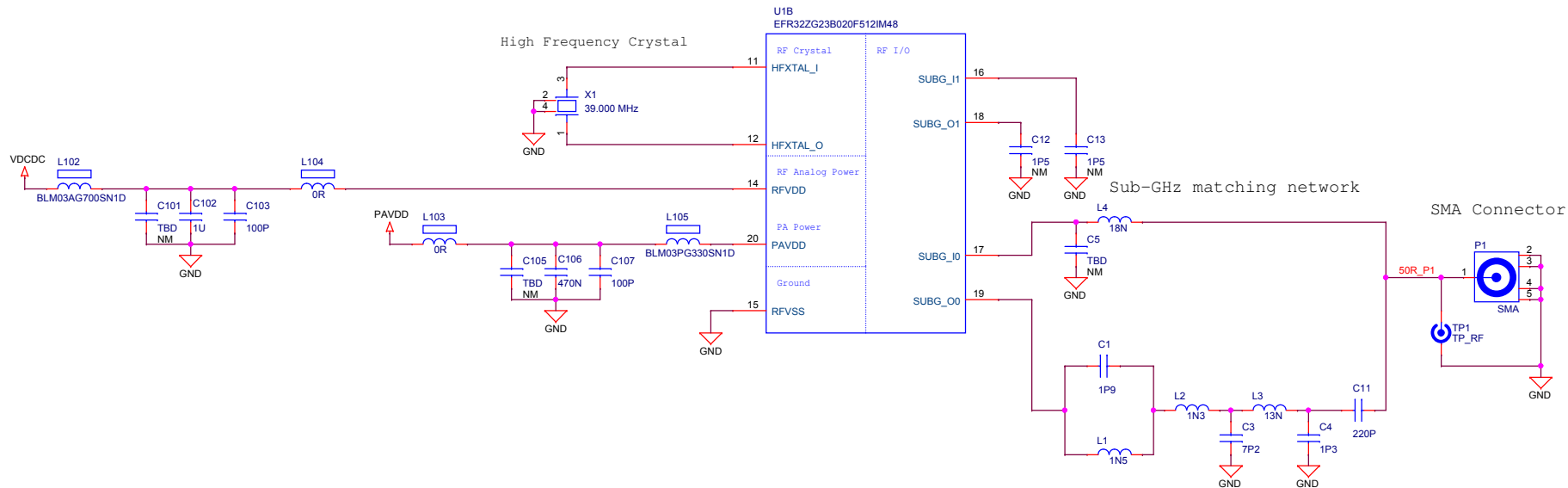
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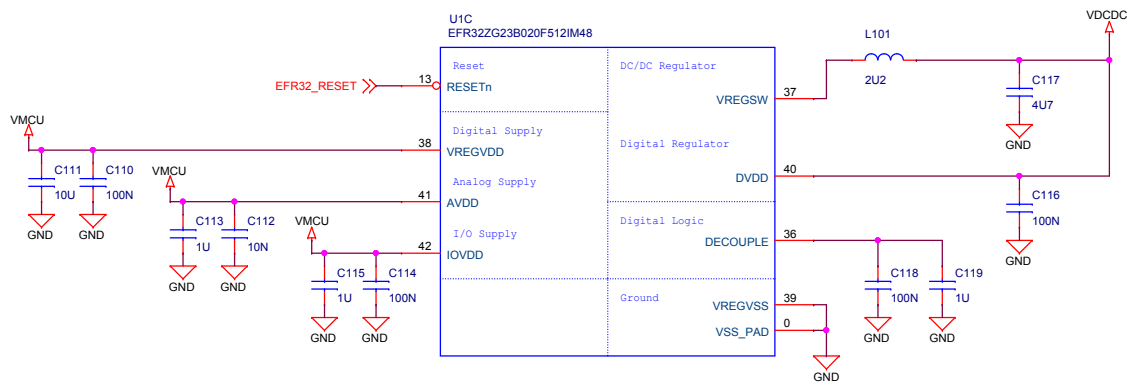
xG23B Radio Board	
868-915 MHz 20 dBm	
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Revision History	
Rev.	Description
A00	Initial production release.

Radio Interface

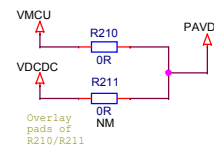


Power & Decoupling

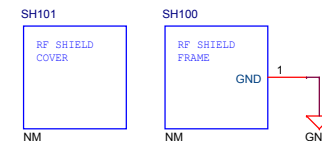


PAVDD Configuration

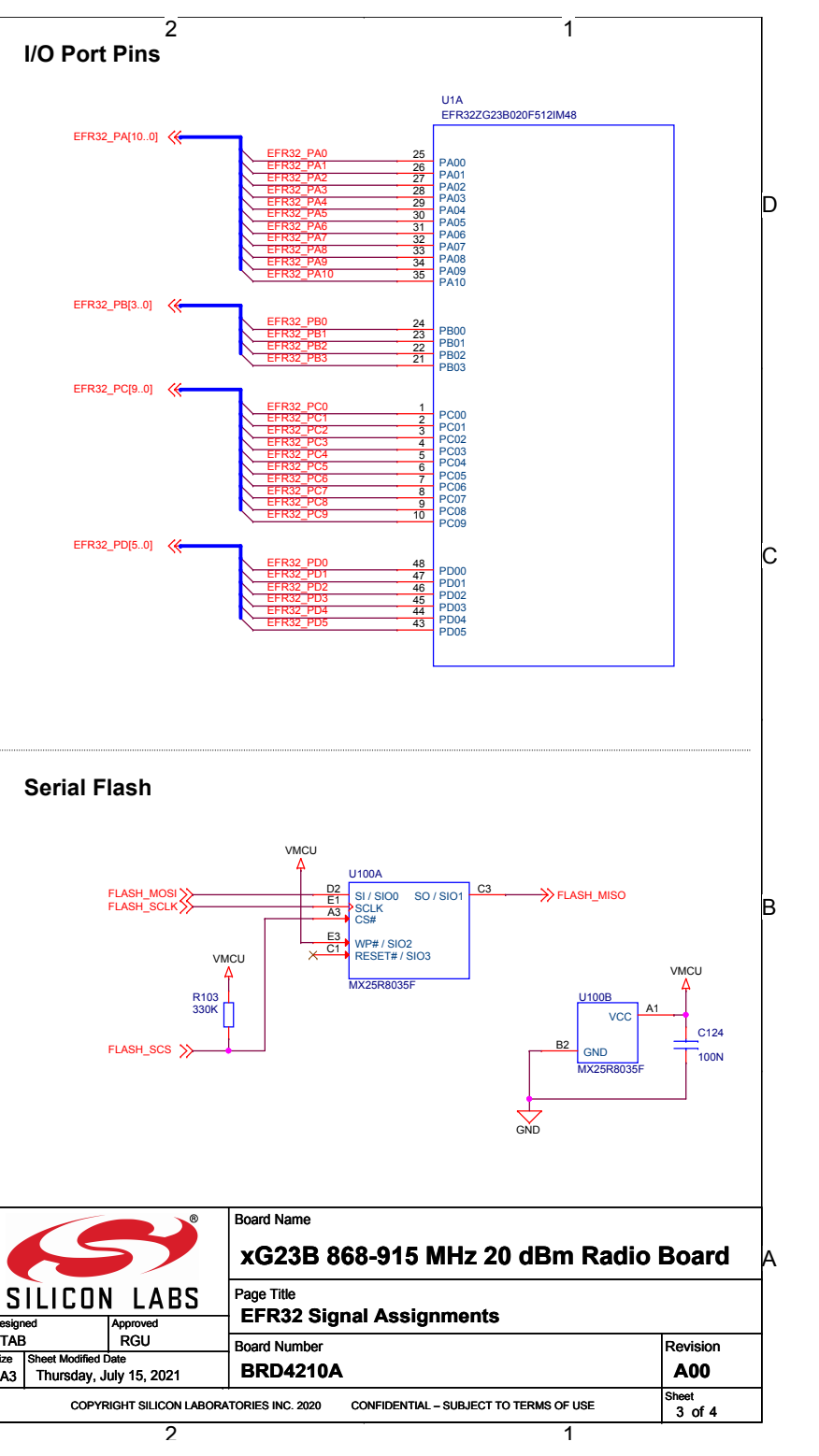
	Power Config 1 VMCU to PAVDD	Power Config 2 DCDC to PAVDD
R210	Mount	Not mount
R211	Not mount	Mount



RF Shielding




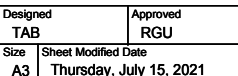
		Board Name	
		xG23B 868-915 MHz 20 dBm Radio Board	
Designed TAB		Page Title	
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Serial Flash

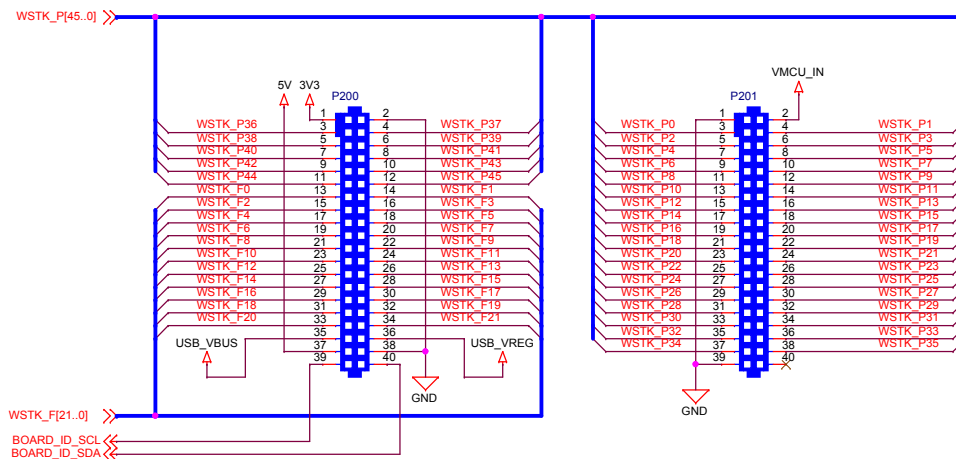
The schematic diagram illustrates the serial flash configuration for the xG23B 868-915 MHz 20 dBm Radio Board. It features two MX25R8035F flash chips, U100A and U100B. U100A is connected to the VMCU and FLASH_MOSI/FLASH_SCLK. U100B is connected to the VMCU and FLASH_MISO. Both chips have their VCC pins connected to a 100nF capacitor and their GND pins connected to ground. A 330K resistor R103 is connected between VMCU and FLASH_SCS.

 SILICON LABS		Board Name xG23B 868-915 MHz 20 dBm Radio Board	
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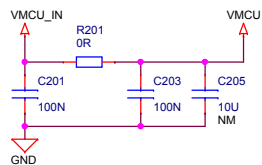
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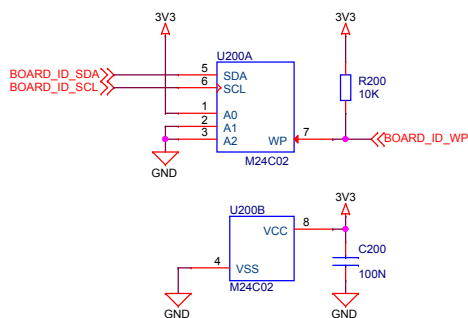
WSTK Connectors



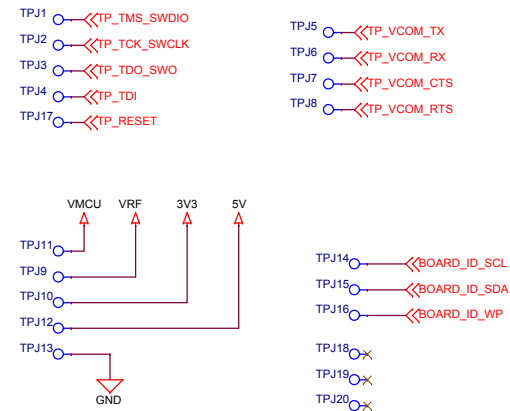
WSTK Power Decoupling




Board Identification



Test Points



		Board Name	
		xG23B 868-915 MHz 20 dBm Radio Board	
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