



## EFR32 Flex Gecko Radio Board

2.4 GHZ / 915 MHZ 19 dBm, VMCU to PAVDD


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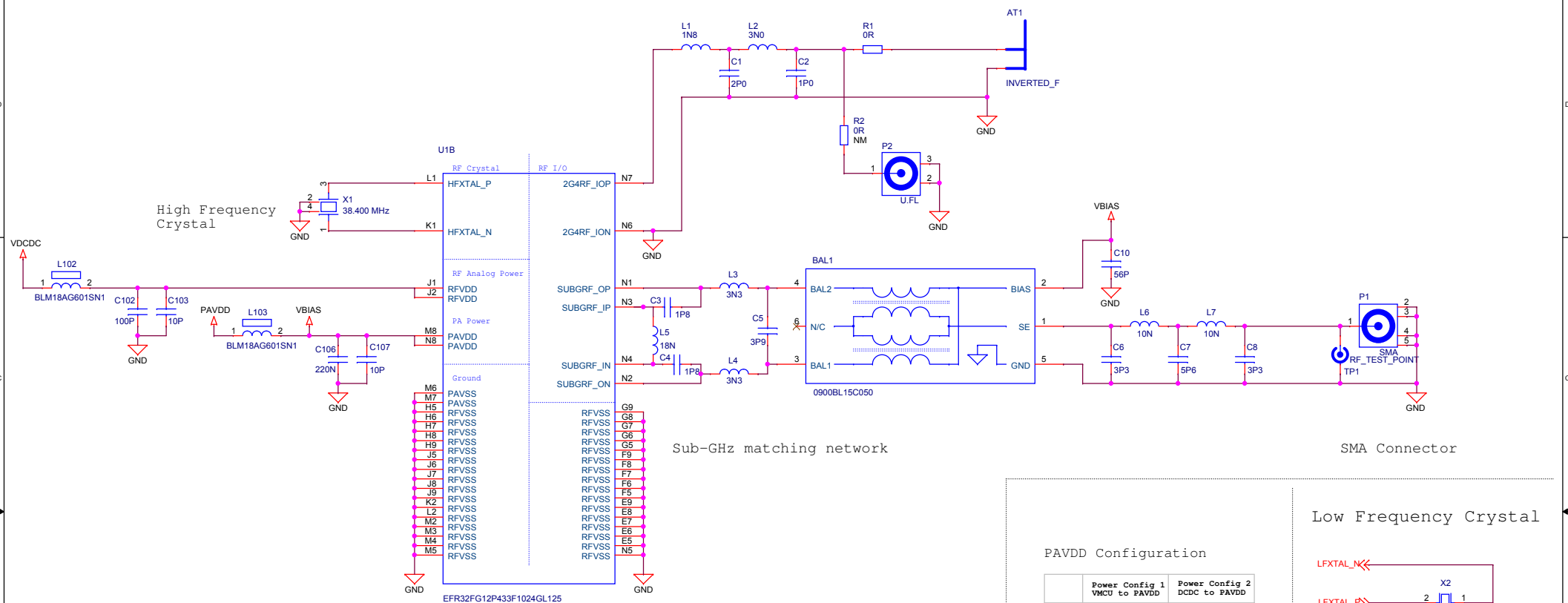
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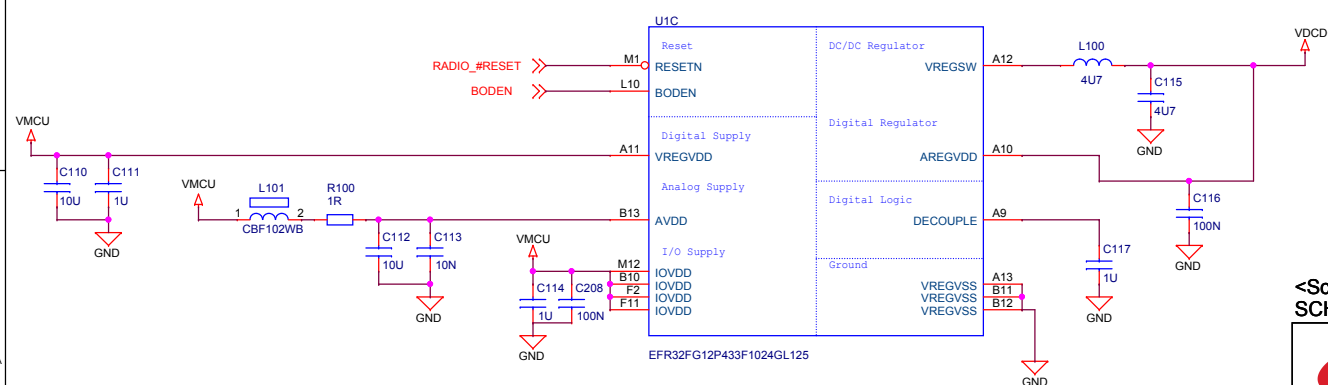
<Schematic Path>  
SCHEMATIC1

 <b>SILICON LABS</b>		Schematic Title	
		<b>EFR32FG12 Dual Band 2400/915 MHz Radio Bo</b>	
Design: TAB		Approved: <OrgAddr2>	
Size A3	BOM Doc No: <Cage Code>	Document number <b>BRD4253A</b>	
Design Created Date: Monday, April 02, 2012		Sheet Created Date Monday, April 02, 2012	Sheet Modified Date Thursday, November 17, 2016
			Revision <b>A02</b>
			Sheet 1 of 4

## Antenna & Radio Interface

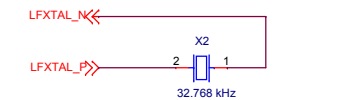
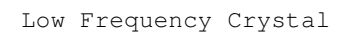


## Power & Decoupling

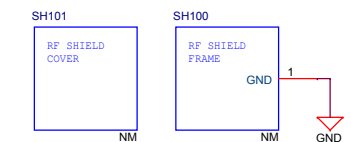


### PAVDD Configuration

	Power Config 1 VMCU to PAVDD	Power Config DCDC to PAVDD
R210	Mount	Not mount
R211	Not mount	Mount



## RF Shielding



<Schematic Path>  
SCHEMATIC1



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## EFR32FG12 Dual Band 2400/915 MHz Radio Boar

Page Title

	<b>RF, Antenna &amp; Power</b>
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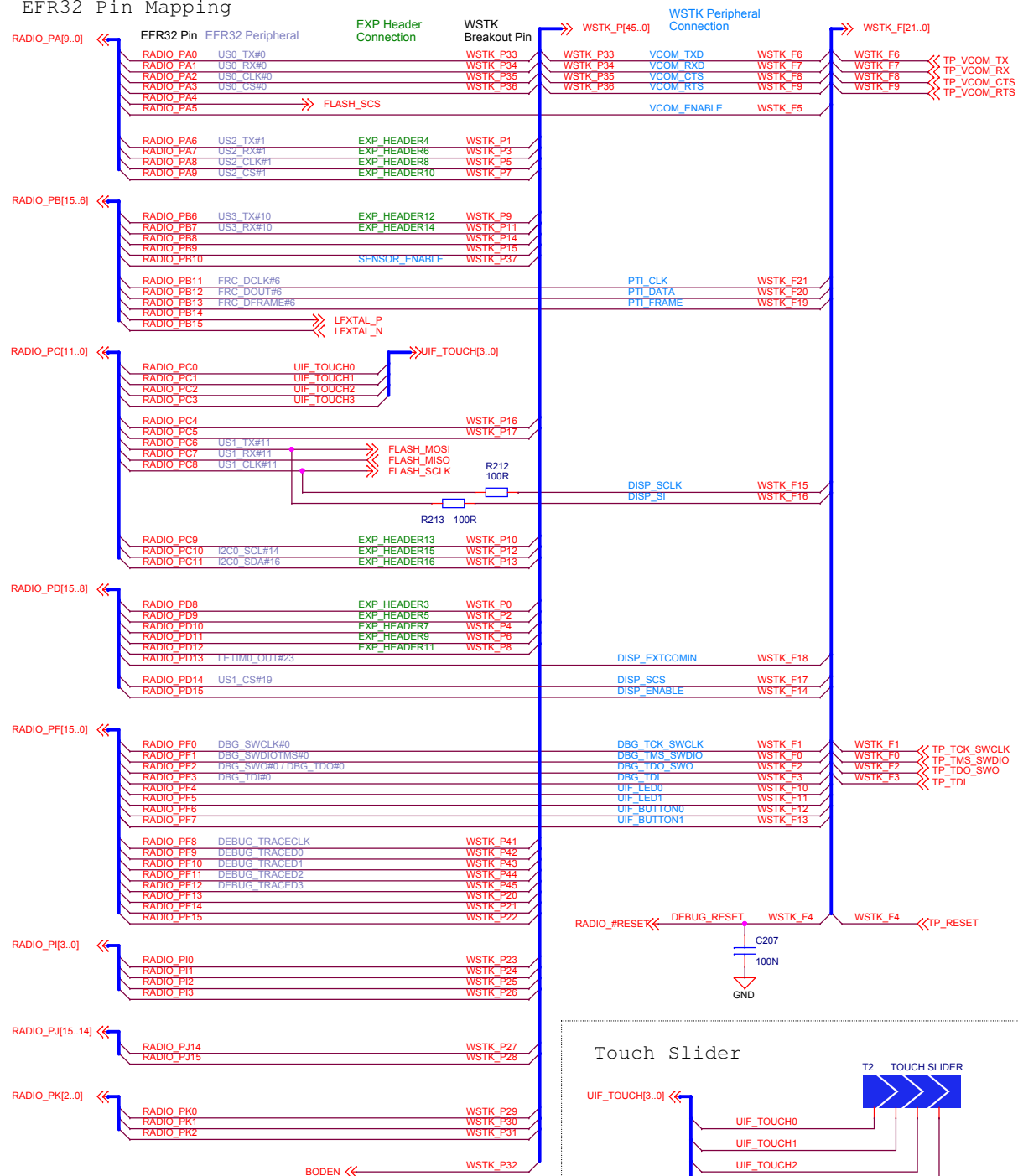
Document number  
**BRD4253A**

Sheet Created Date  
Monday, April 02, 2012

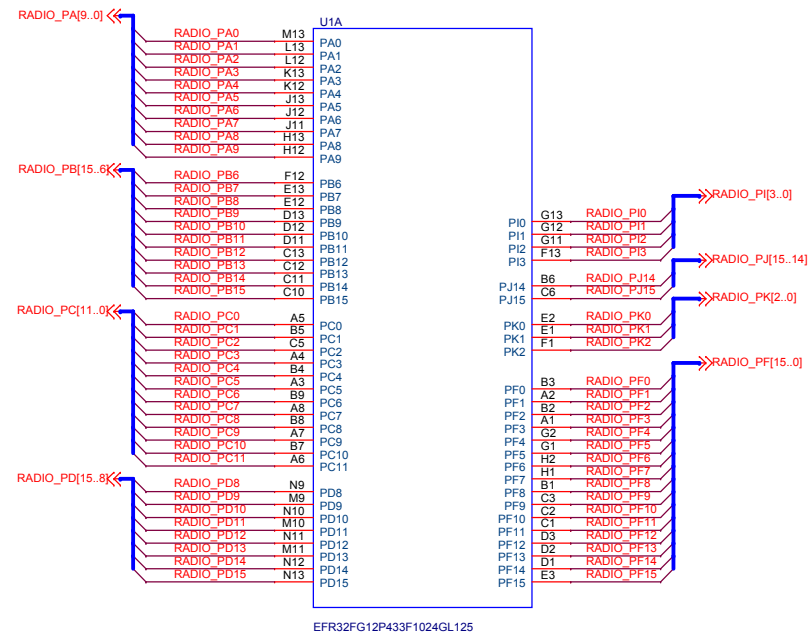
Revision
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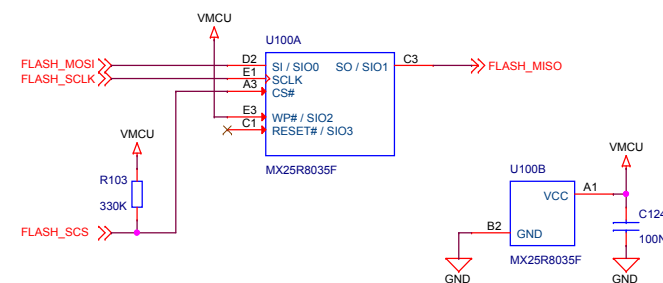
## EFR32 Pin Mapping



## EFR32 I/O Port Pins



## Serial Flash

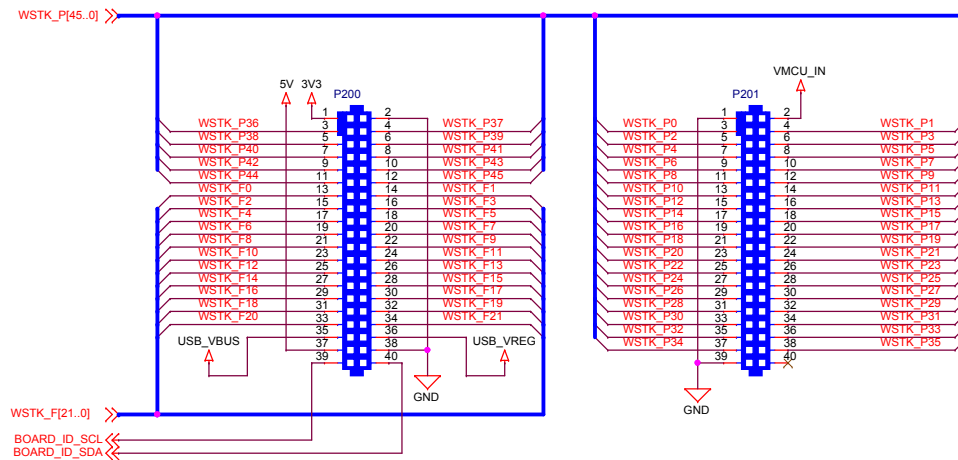


<Schematic Path>  
SCHEMATIC1

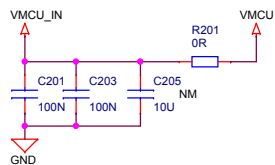


Designed: TAB		Approved: <OrgAddr>		<b>EFR32 I/O Port Connections</b>	
Size A3		BOM Doc No: <Cage Code>		Document number <b>BRD4253A</b>	
Design Created Date: Monday, April 02, 2012		Sheet Created Date Wednesday, April 11, 2012		Revision A02	
		Sheet Modified Date Thursday, November 17, 2016		Sheet 3 of 4	

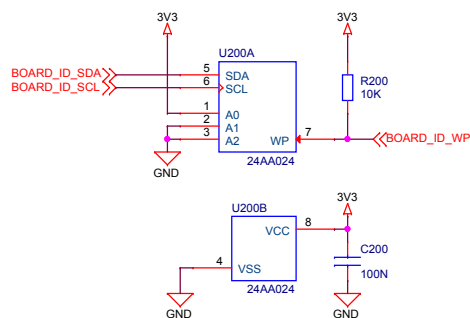
## WSTK Connectors



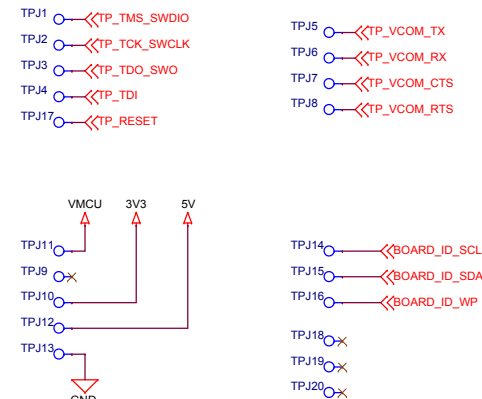
## WSTK Power Decoupling




## Board Identification



## Test Points



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		Schematic Title	
		EFR32FG12 Dual Band 2400/915 MHz Radio Boa	
Designed:		Page Title	
TAB		WSTK Connectors & Board ID	
Size		Document number	
A3		BRD4253A	
BOM Doc No:		Revision	
<Cage Code>		A02	
Design Created Date:		Sheet Created Date	
Monday, April 02, 2012		Tuesday, April 03, 2012	
		Sheet Modified Date	
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