



EFR32 Flex Gecko Radio Board
2.4 GHz / 915 MHz 19 dBm, VMCU to PAVDD


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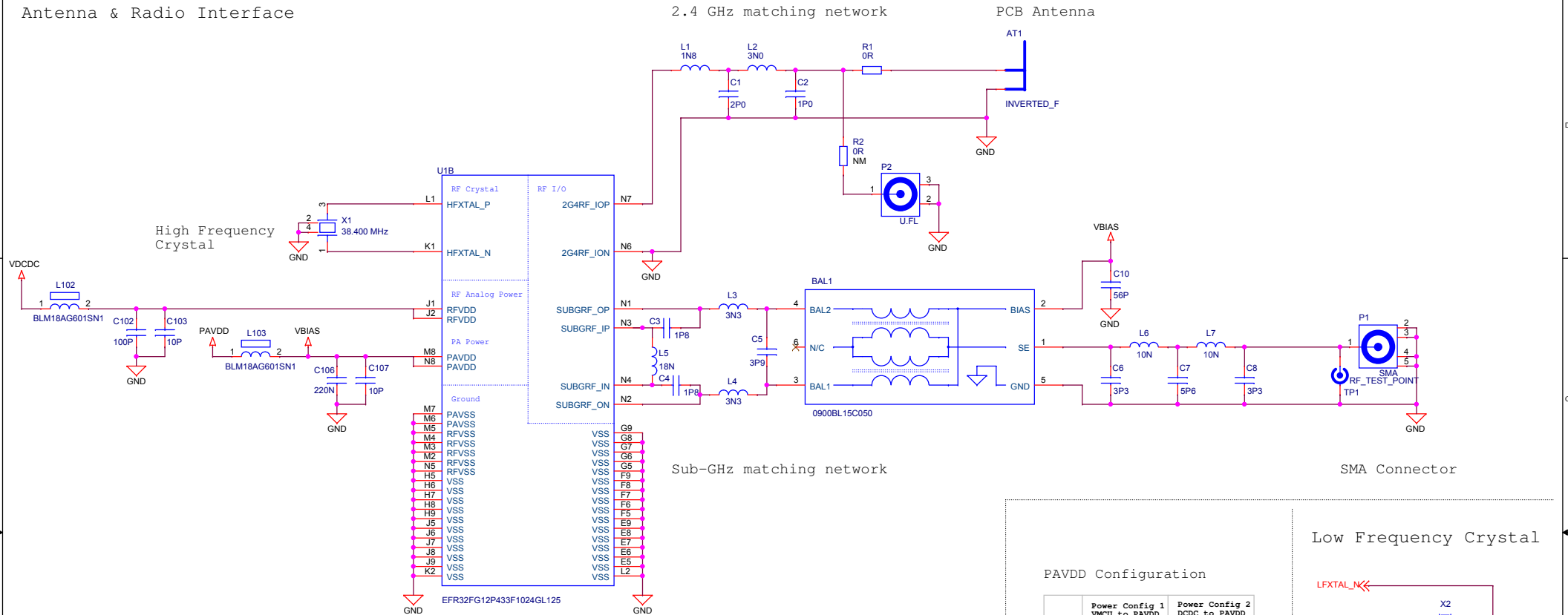


Revision History	
Rev.	Description
A00	Initial version.
A01	Fixing EFR part number.
A02	Updated EFR32 chip revision and 32 kHz crystal part number.
A03	Updated EFR32 to Rev. C. Fixed 32 kHz crystal P/N. Updated PCB rev.

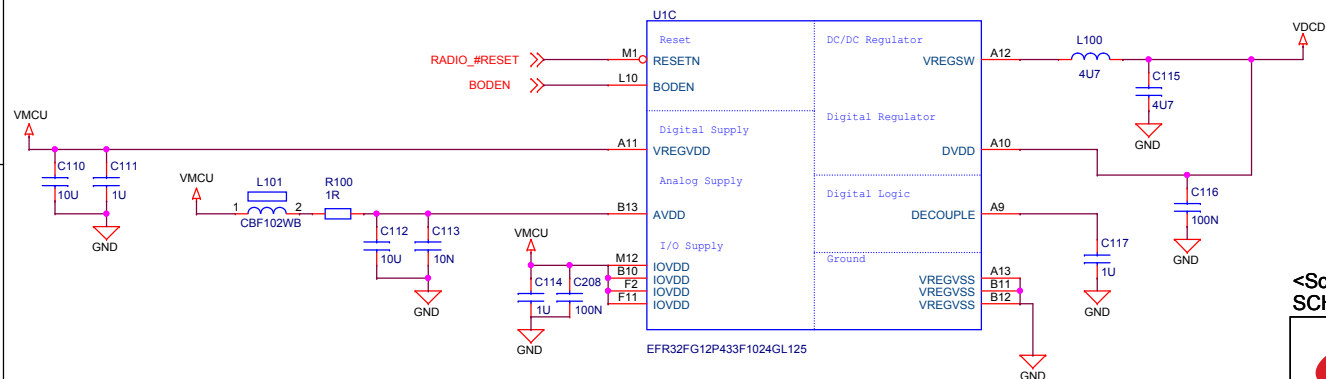
<Schematic Path>
SCHEMATIC1

 SILICON LABS		Schematic Title	
		EFR32FG12 Dual Band 2400/915 MHz Radio Bo	
Designed: TAB	Approved: <OrgAddr2>	Page Title	
Size A3		Title Page	
BOM Doc No: <Cage Code>		Document number	Revision
Design Created Date: Monday, April 02, 2012		BRD4253A	A03
Sheet Created Date Monday, April 02, 2012		Sheet Modified Date Friday, October 13, 2017	Sheet 1 of 4

Antenna & Radio Interface



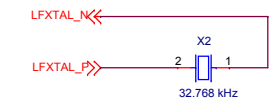
Power & Decoupling



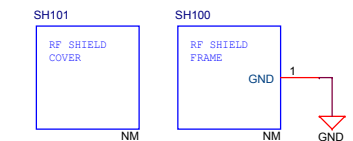
PAVDD Configuration

	Power Config 1 VMCU to PAVDD	Power Config 2 DCDC to PAVDD
R210	Mount	Not mount
R211	Not mount	Mount

Low Frequency Crystal



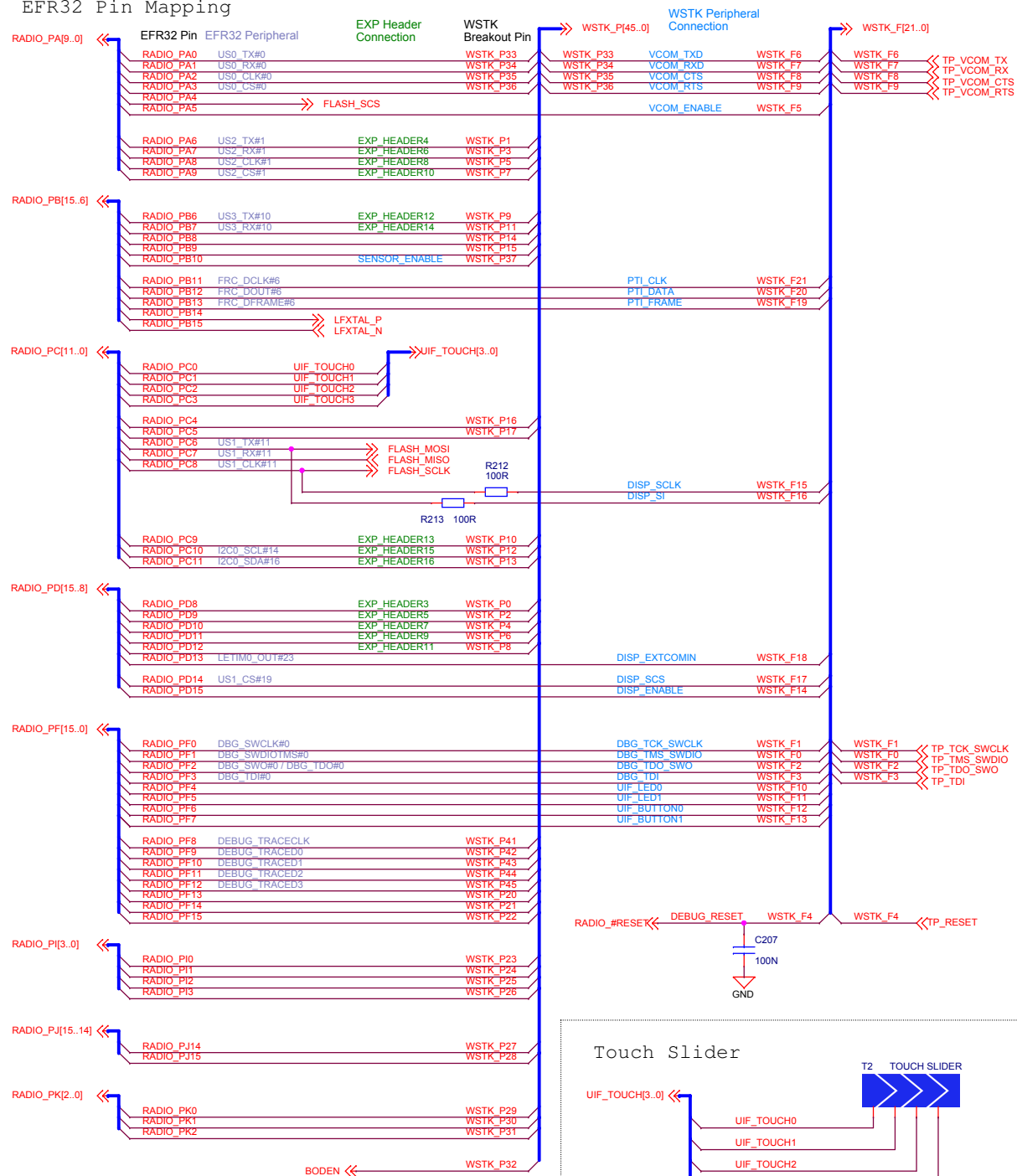
RF Shielding



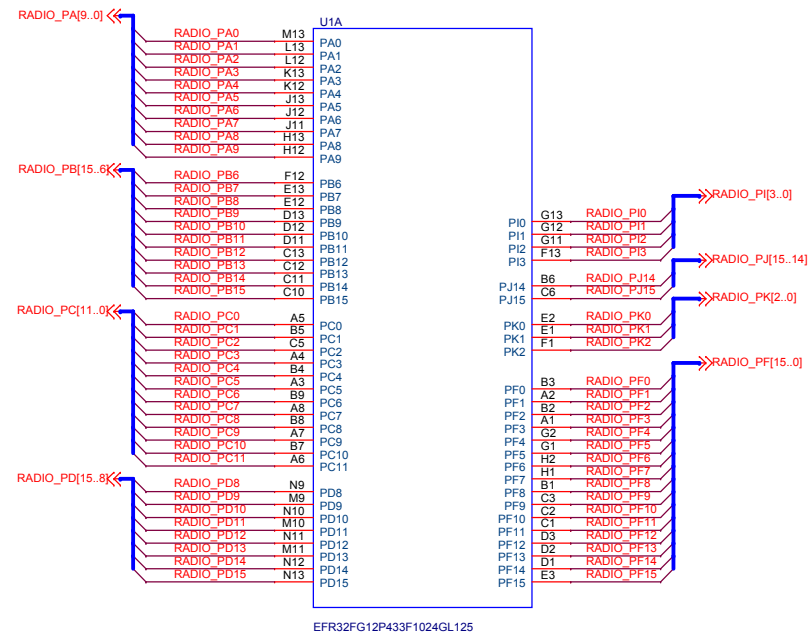
<Schematic Path>
SCHEMATIC1

		Schematic Title	
		EFR32FG12 Dual Band 2400/915 MHz Radio Boa	
Designed: TAB		Page Title	
Size A3		RF, Antenna & Power	
BOM Doc No: <Cage Code>		Document number	
Design Created Date: Monday, April 02, 2012		BRD4253A	
Approved: <OrgAddr2>		Revision	
Sheet Created Date: Monday, April 02, 2012		A03	
Sheet Modified Date: Friday, October 13, 2017		Sheet	
		2 of 4	

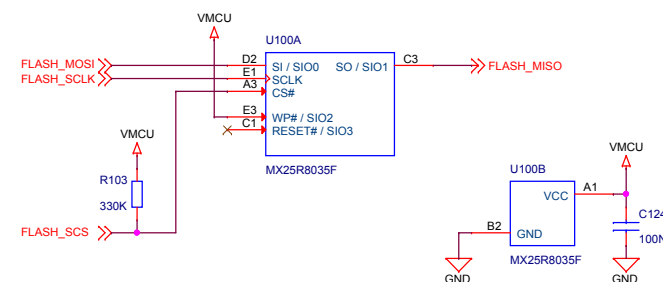
EFR32 Pin Mapping



EFR32 I/O Port Pins



Serial Flash



<Schematic Path>
SCHEMATIC1



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Schematic Title

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EFR32 I/O Port Connections

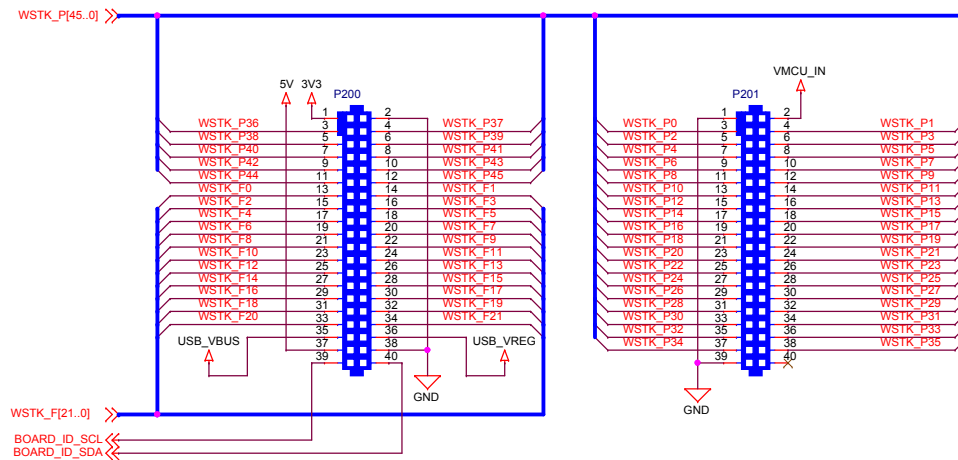
Document number
BRD4253A

Sheet Created Date
Wednesday, April 11, 2012

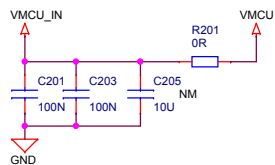
	Revision
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		A03
Sheet Modified Date	Friday, October 13, 2017	Sheet 3 of 3

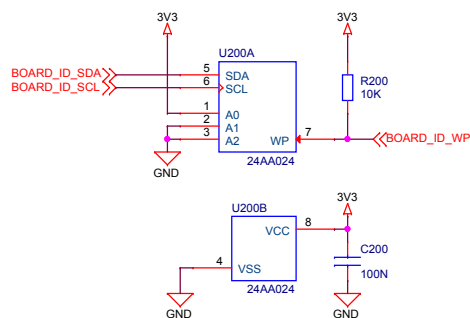
WSTK Connectors



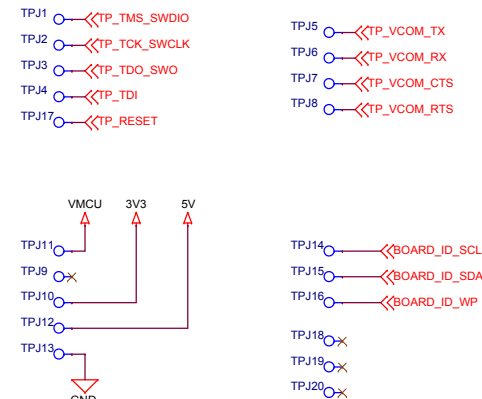
WSTK Power Decoupling




Board Identification



Test Points



<Schematic Path>
SCHEMATIC1

		Schematic Title	
		EFR32FG12 Dual Band 2400/915 MHz Radio Boa	
Designed:		Page Title	
TAB		WSTK Connectors & Board ID	
Size		Document number	
A3		BRD4253A	
BOM Doc No:		Revision	
<Cage Code>		A03	
Design Created Date:		Sheet Created Date	
Monday, April 02, 2012		Tuesday, April 03, 2012	
		Sheet Modified Date	
		Friday, October 13, 2017	
		Sheet	
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