



EFR32FG13 Dual Band Radio Board
2.4 GHz / 868 MHz 10 dBm, VDCDC to PAVDD

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Revision History	
Rev.	Description
A00	Initial revision.

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SCHEMATIC1

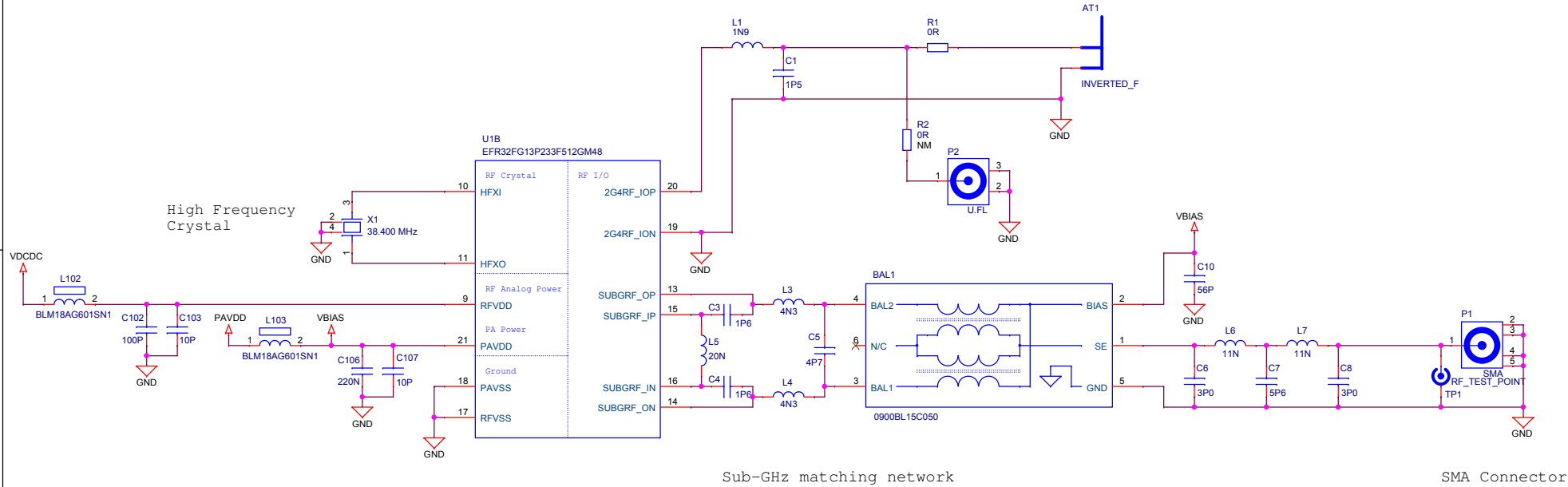
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Designed: JSH		Page Title	
Approved: JNO		Title Page	
Size: A3	BOM Doc No: <Cage Code>	Document number	Revision
Design Created Date: Monday, April 02, 2012		BRD4256A	A00
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Antenna & Radio Interface

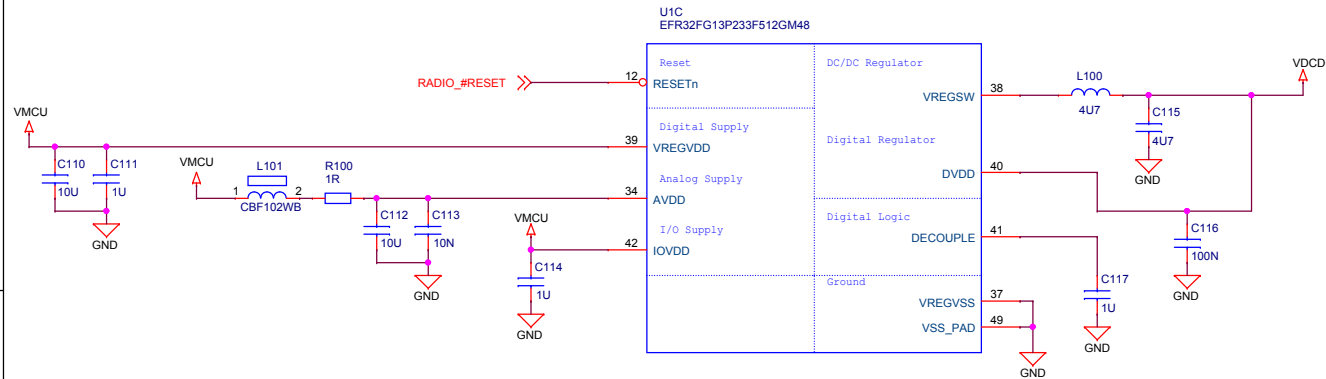
2.4 GHz matching network

PCB Antenna

High Frequency Crystal

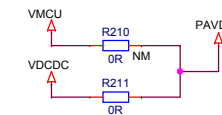


Power & Decoupling

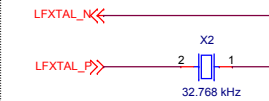


PAVDD Configuration

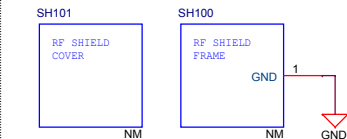
	Power Config 1 VMCU to PAVDD	Power Config 2 DCDC to PAVDD
R210	Mount	Not mount
R211	Not mount	Mount



Low Frequency Crystal



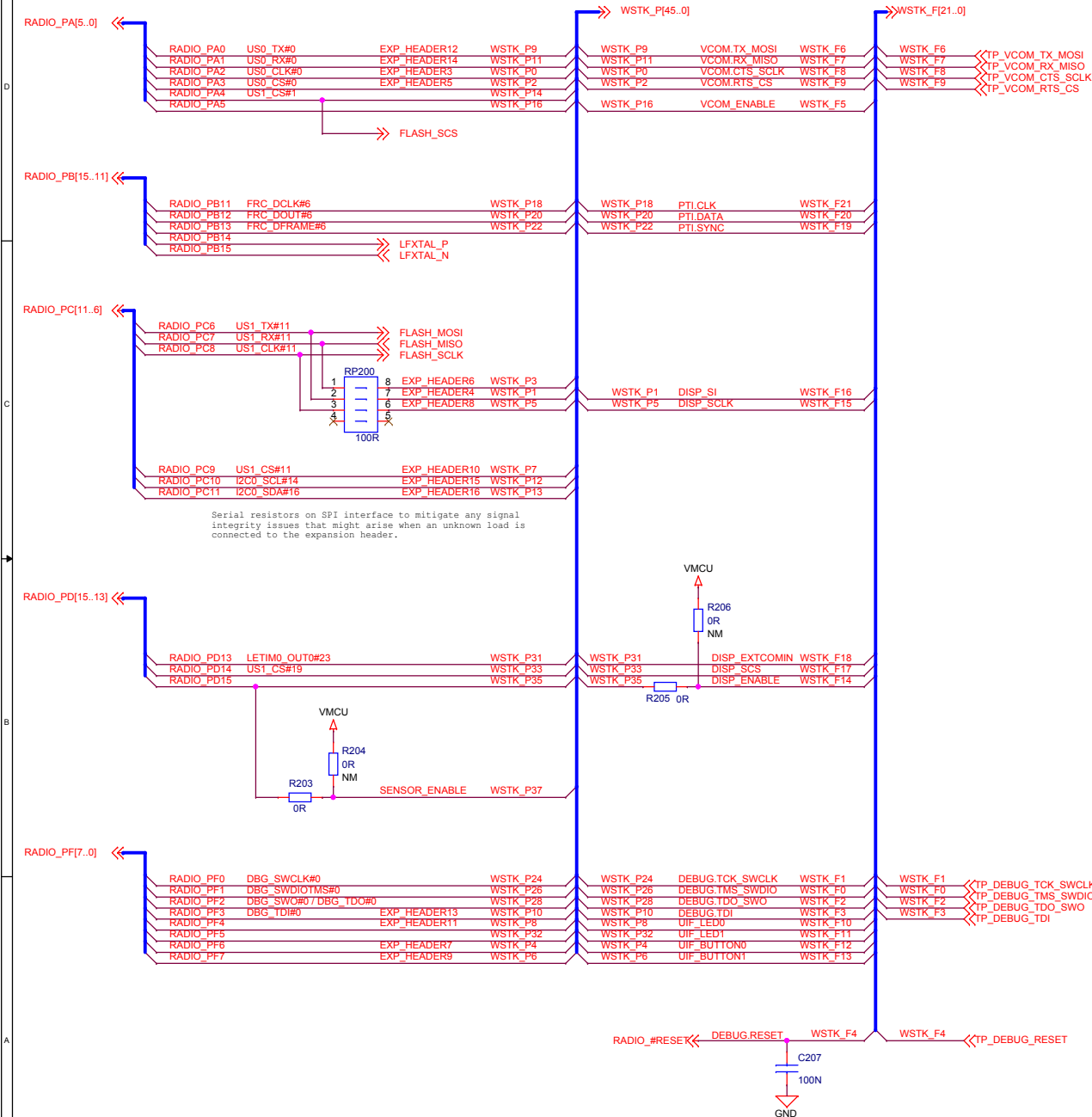
RF Shielding



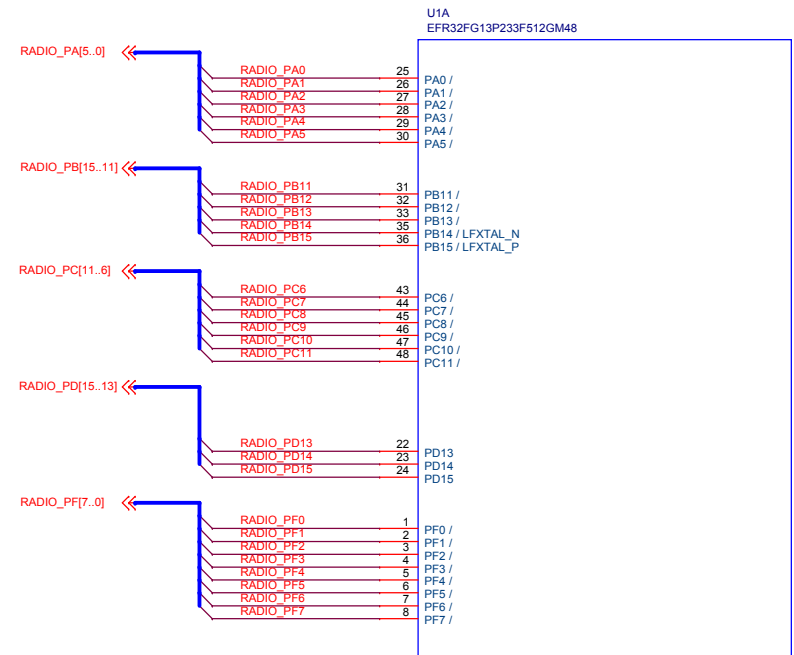
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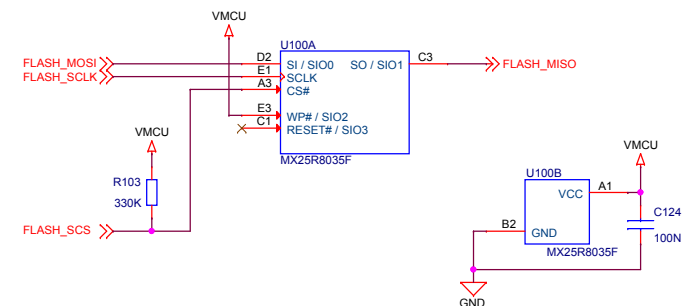
EFR32 Pin Mapping



EFR32 I/O Port Pins



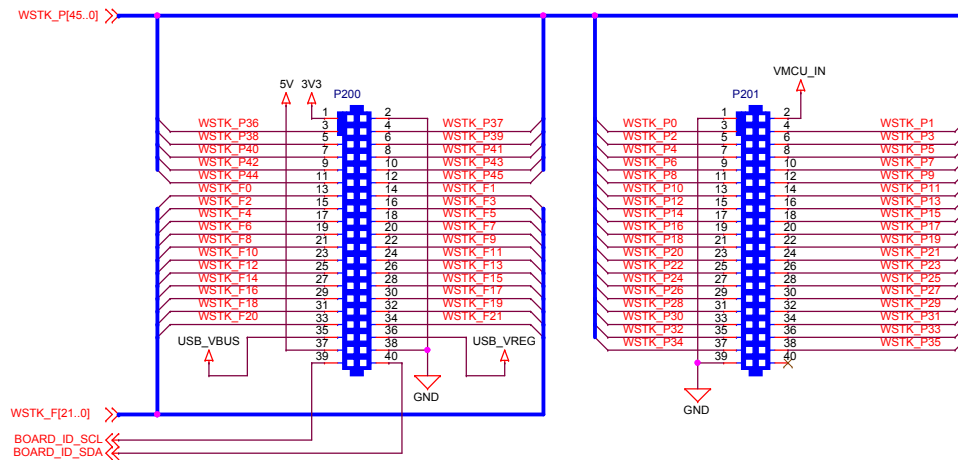
Serial Flash



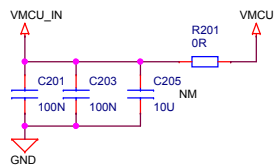
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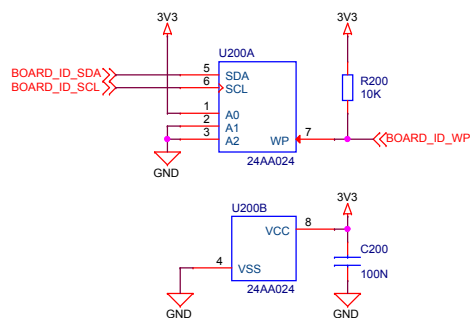
WSTK Connectors



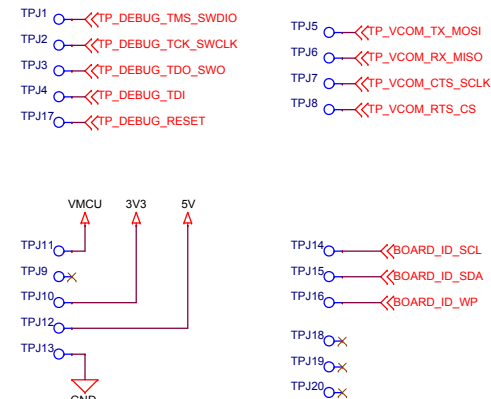
WSTK Power Decoupling



Board Identification



Test Points



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