



EFR32FG13 Dual Band Radio Board
2.4 GHz / 868 MHz 10 dBm, VDCDC to PAVDD

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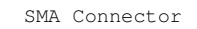


Revision History	
Rev.	Description
A00	Initial revision.
A01	Updated EFR32 revision to Rev. C. Fixing 32 kHz crystal P/N.
A02	Fixing P/N for C5.

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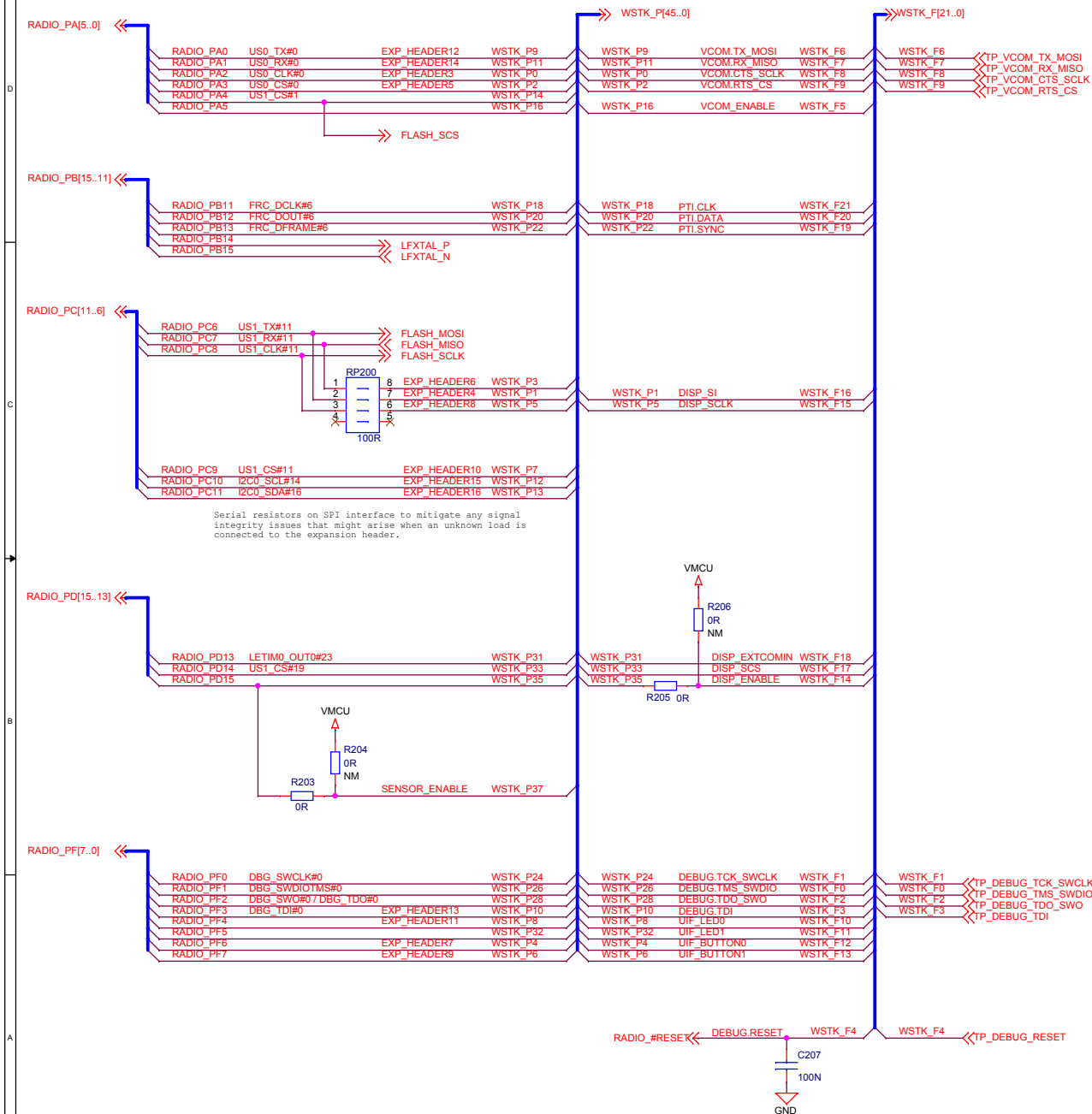
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Approved: JNO		Title Page	
Size: A3	BOM Doc No: <Cage Code>	Document number	Revision
Design Created Date: Monday, April 02, 2012		BRD4256A	A02
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PCB Antenna

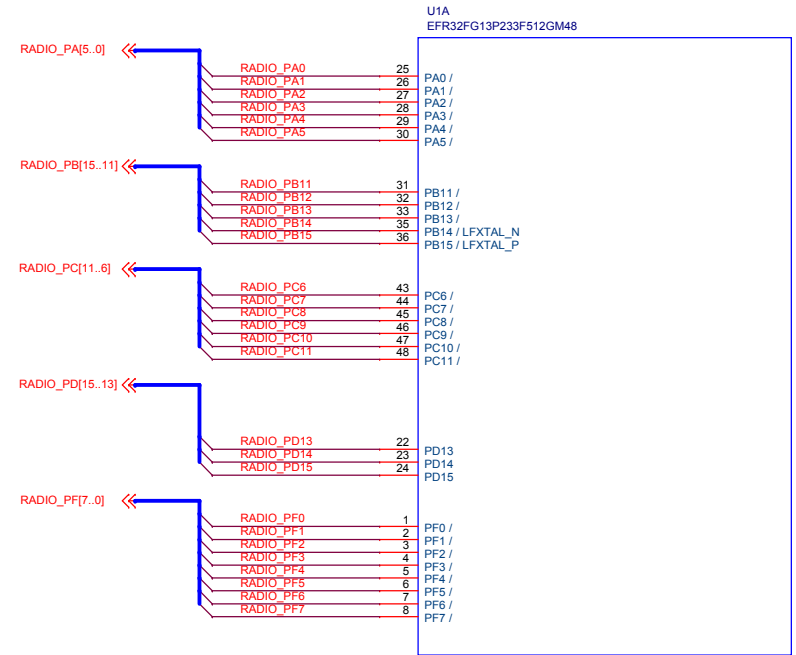


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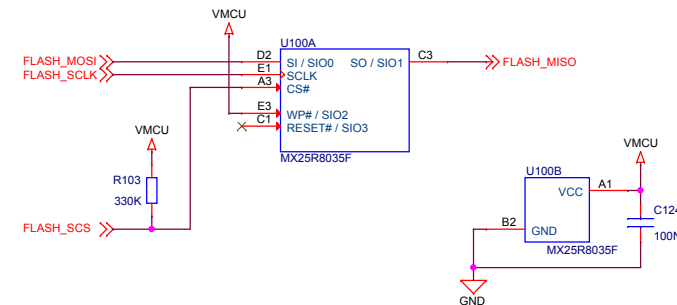
EFR32 Pin Mapping



EFR32 I/O Port Pins



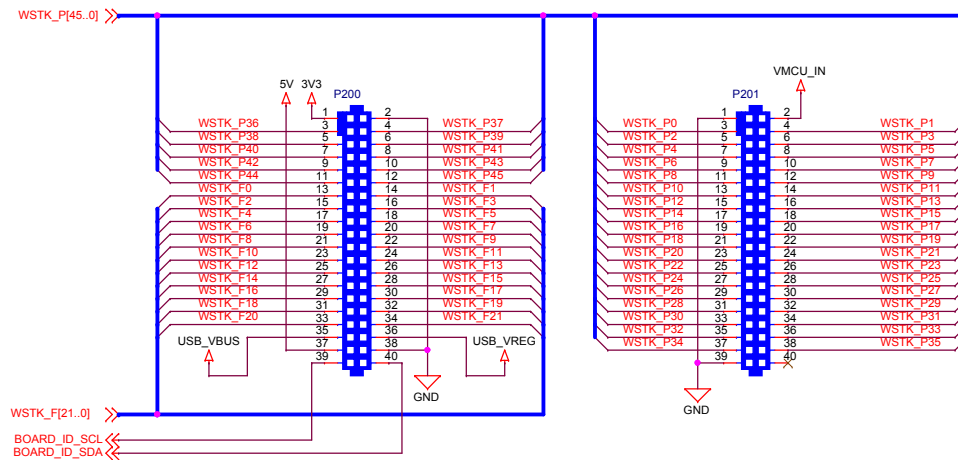
Serial Flash



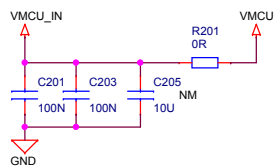
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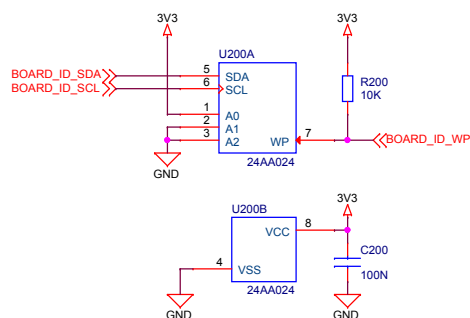
WSTK Connectors



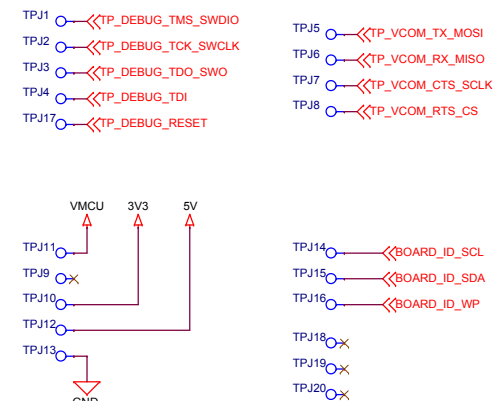
WSTK Power Decoupling



Board Identification



Test Points



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