EFR32FG13 Dual Band Radio Board

2.4 GHz / 868 MHz 10 dBm, VDCDC to PAVDD

Board Function	Page
Title Page	1
RF, Antenna & Power	2
EFR32 Signal Assignments	3
WSTK Connectors & Board ID	4

Revision History

Rev.	Description
A00	Initial revision.
A01	Updated EFR32 revision to Rev. C. Fixing 32 kHz crystal P/N.

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PCB1

PCB4256A

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Design Created Date: Monday, April 02, 2012		Document number: BRD4256A	Revision: A01
		Sheet Created Date: Monday, April 02, 2012	Sheet Modified Date: Friday, October 20, 2017
			Sheet: 1 of 4

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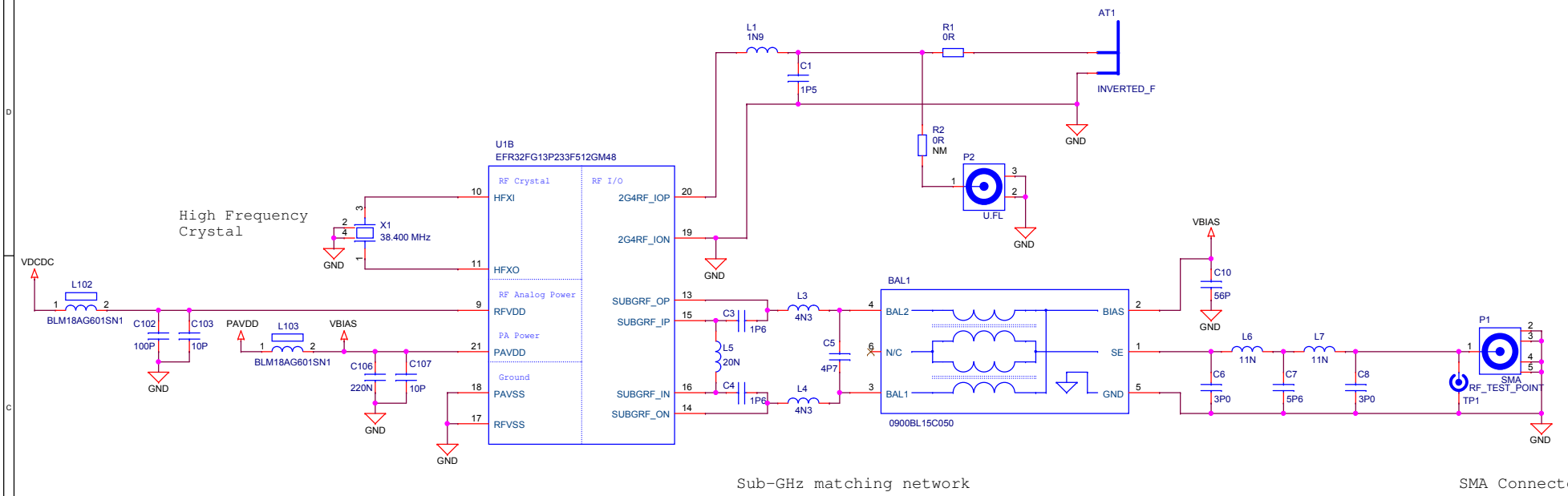
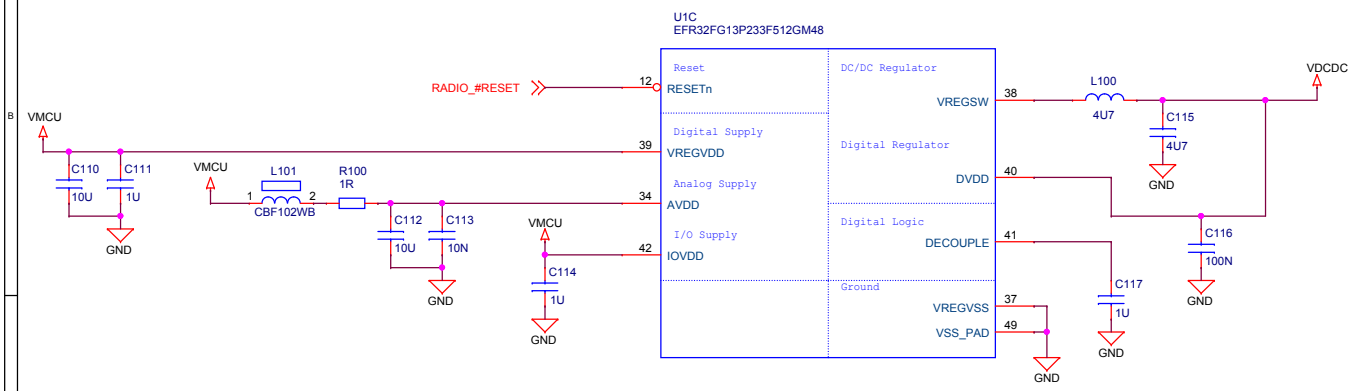
Antenna & Radio Interface

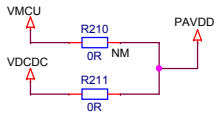


Power & Decoupling

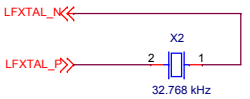


PAVDD Configuration

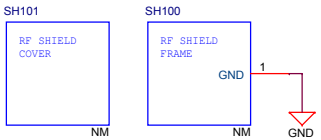
	Power Config 1 VMCU to PAVDD	Power Config 2 DCDC to PAVDD
R210	Mount	Not mount
R211	Not mount	Mount



Low Frequency Crystal



RF Shielding



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Page Title: **RF, Antenna & Power**

Document number: BRD4256A	Revision: A01
Sheet Created Date: Monday, April 02, 2012	Sheet Modified Date: Friday, October 20, 2017
Sheet: 2 of 4	

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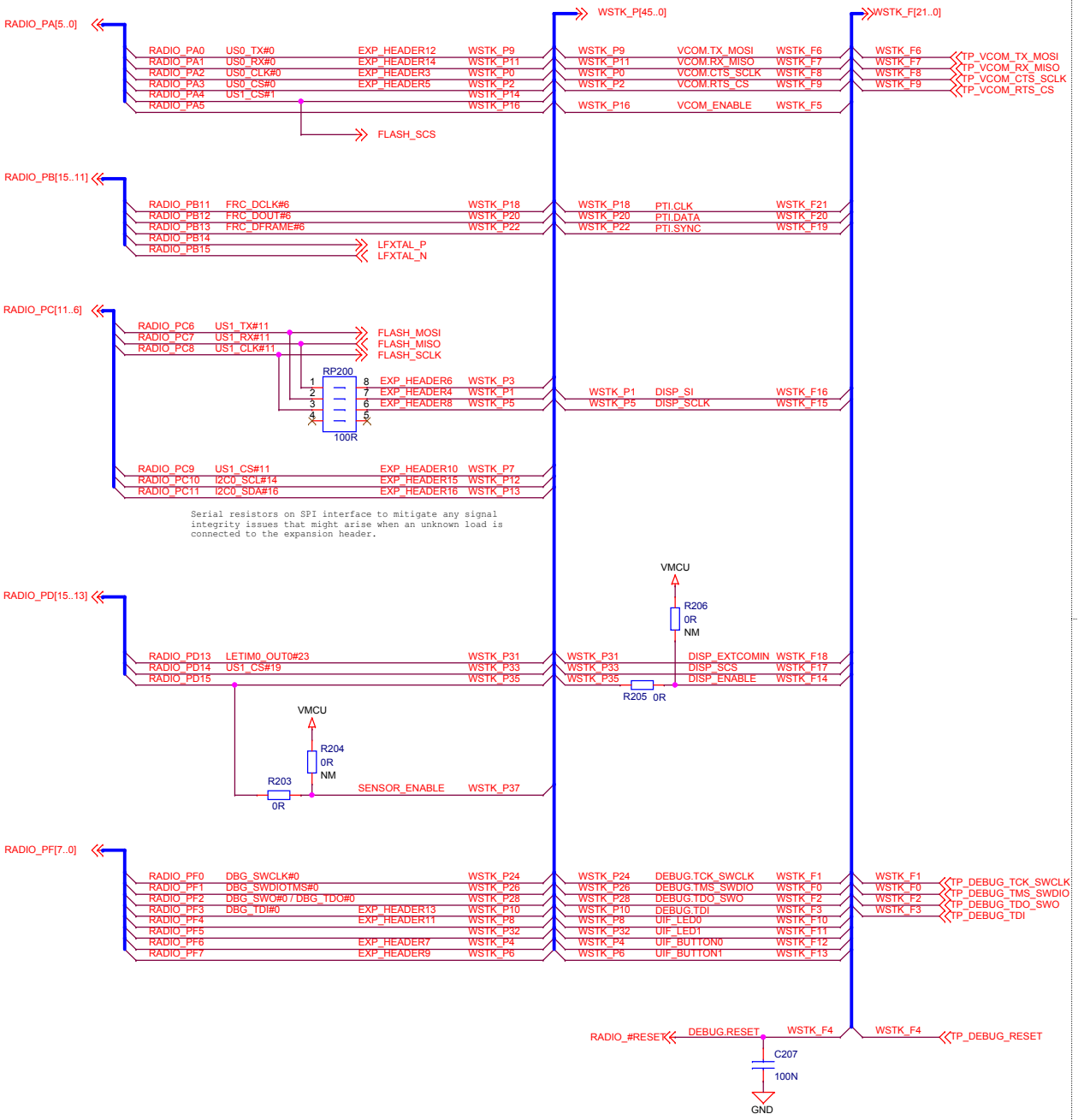
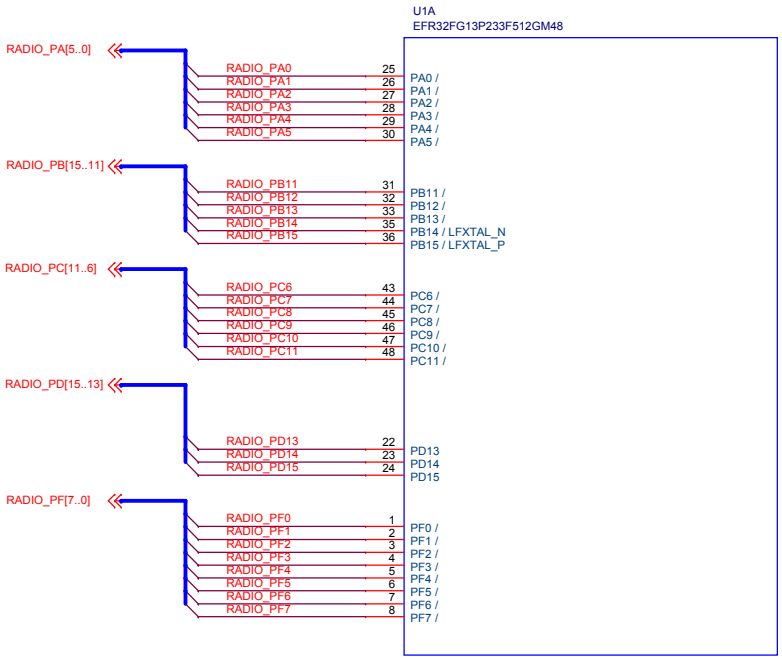
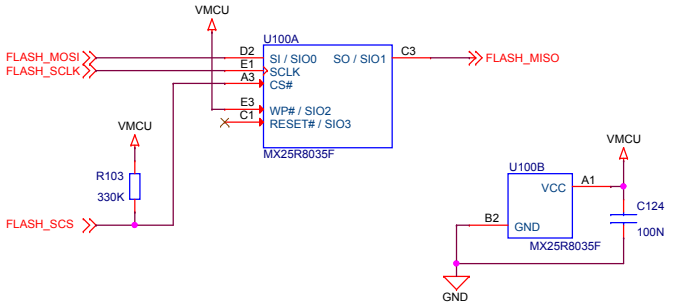
EFR32 Pin Mapping



EFR32 I/O Port Pins



Serial Flash



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Schematic Title
EFR32FG13 Dual Band 2400/868 MHz Radio Boa

Page Title
EFR32 I/O Port Connections

Document number: **BRD4256A**	Revision: **A01**
Sheet Created Date: Wednesday, April 11, 2012	Sheet Modified Date: Friday, October 20, 2017
	Sheet: 3 of 4

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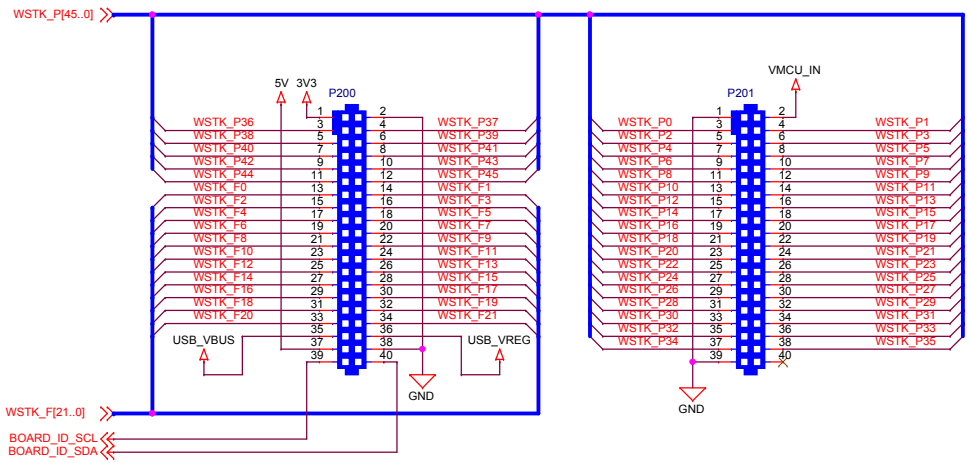
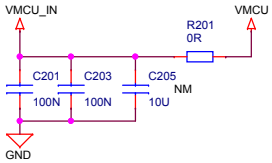
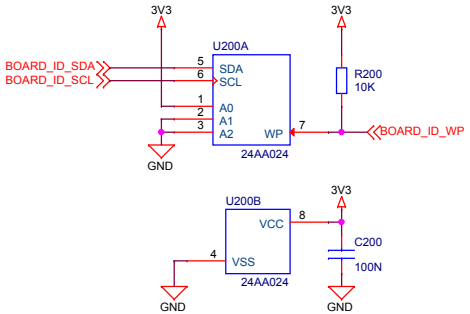
WSTK Connectors



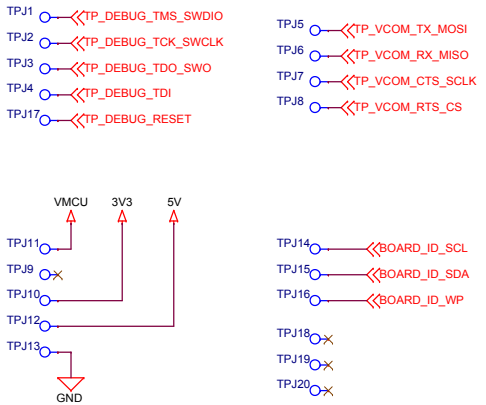
WSTK Power Decoupling



Board Identification



Test Points



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			Sheet: 4 of 4

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