



EFR32FG14 Flex Gecko Radio Board
2.4 GHz / 434 MHz 10 dBm, VDCDC to PAVDD


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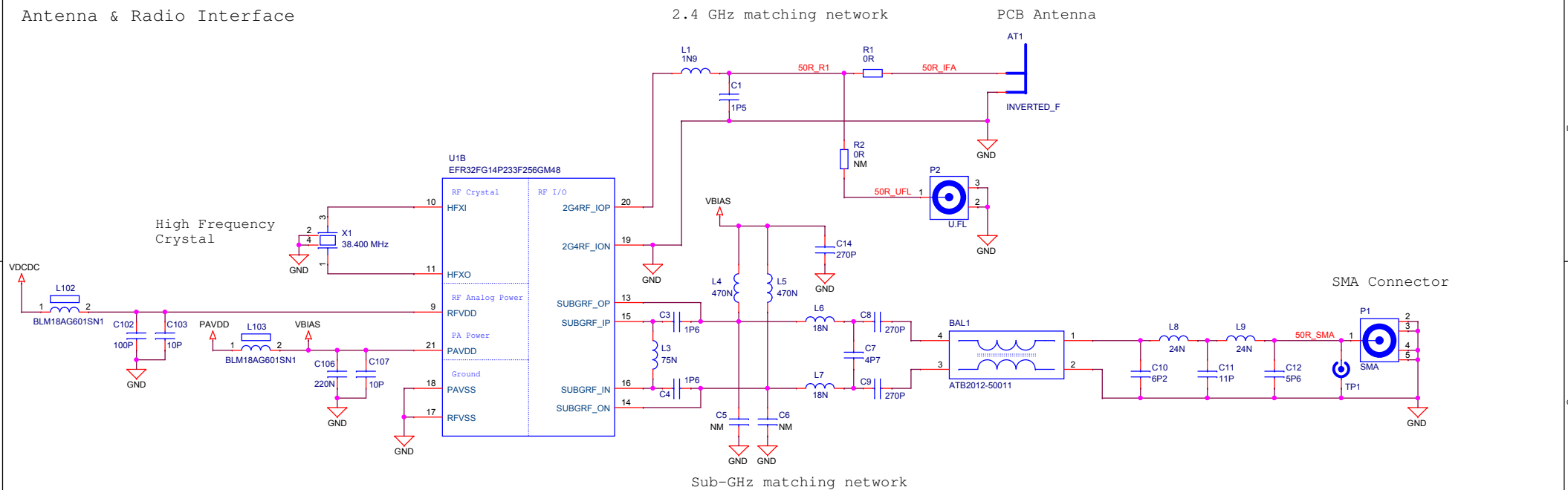


| Revision History | |
|------------------|----------------------------------|
| Rev. | Description |
| A00 | Initial revision. |
| A01 | Updating EFR revision to Rev. B. |
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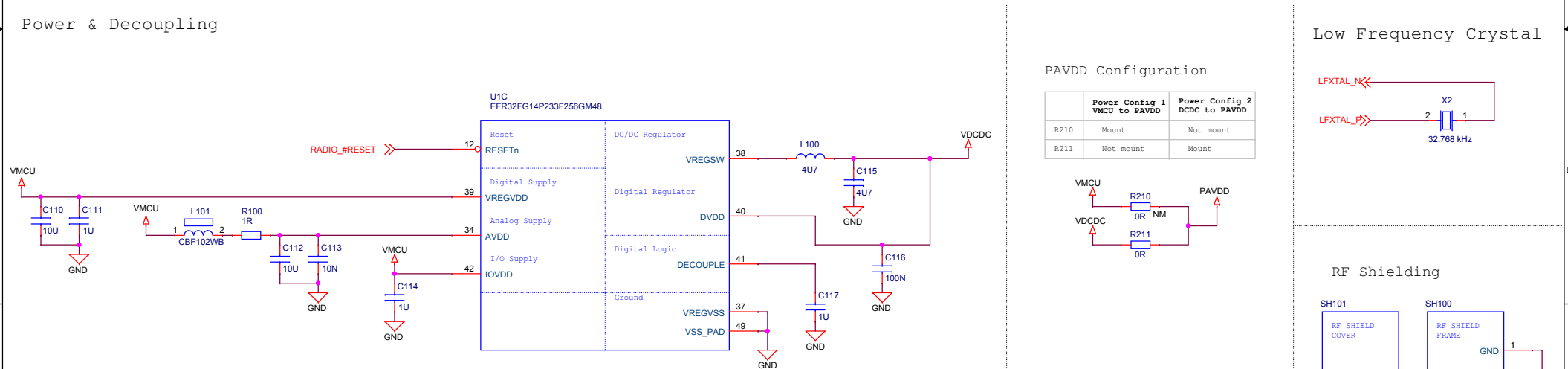
<Schematic Path>
SCHEMATIC1

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|  SILICON LABS | | Schematic Title | |
| | | EFR32FG14 2.4 GHz / 434 MHz 10 dBm Radio B | |
| Designed: JSH | | Page Title | |
| Approved: JNO | | Title Page | |
| Size: A3 | BOM Doc No: <Cage Code> | Document number | Revision |
| Design Created Date: Monday, April 02, 2012 | | BRD4259A | A01 |
| | | Sheet Created Date: Monday, April 02, 2012 | Sheet Modified Date: Tuesday, August 29, 2017 |
| | | | Sheet 1 of 4 |

Antenna & Radio Interface

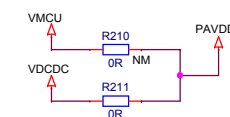


Power & Decoupling

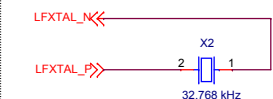


PAVDD Configuration

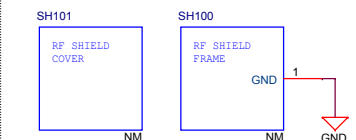
| | Power Config 1 VMCU to PAVDD | Power Config 2 DCDC to PAVDD |
|------|---------------------------------|---------------------------------|
| R210 | Mount | Not mount |
| R211 | Not mount | Mount |



Low Frequency Crystal



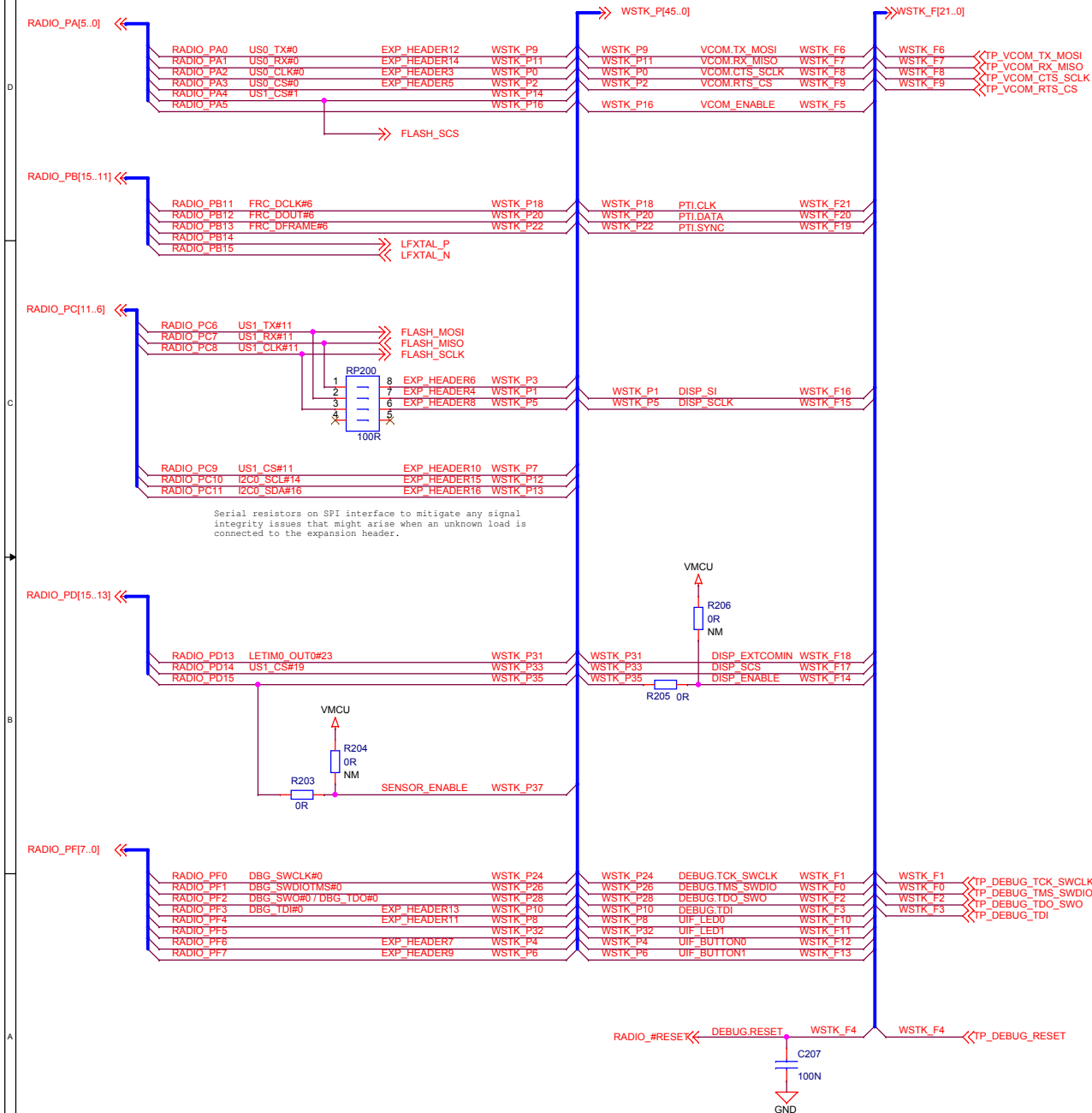
RF Shielding



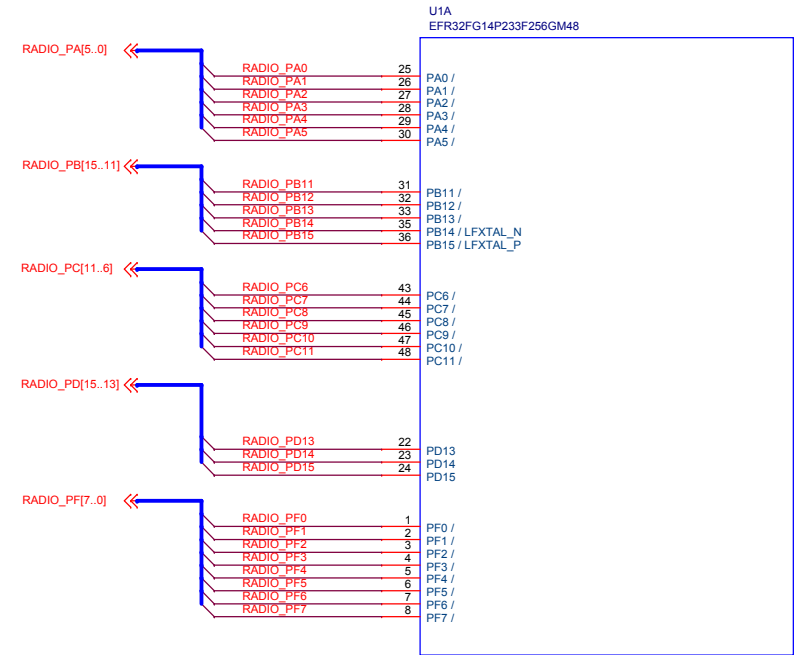
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| | | Schematic Title | |
| | | EFR32FG14 2.4 GHz / 434 MHz 10 dBm Radio Bc | |
| Designed: JSH | | Page Title | |
| Size A3 | | RF, Antenna & Power | |
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| Design Created Date: Monday, April 02, 2012 | | BRD4259A | |
| Sheet Created Date: Monday, April 02, 2012 | | Revision | |
| Sheet Modified Date: Tuesday, August 29, 2017 | | A01 | |
| Sheet 2 of 4 | | | |

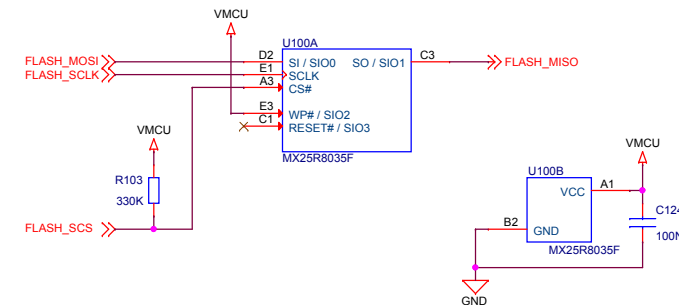
EFR32 Pin Mapping




EFR32 I/O Port Pins



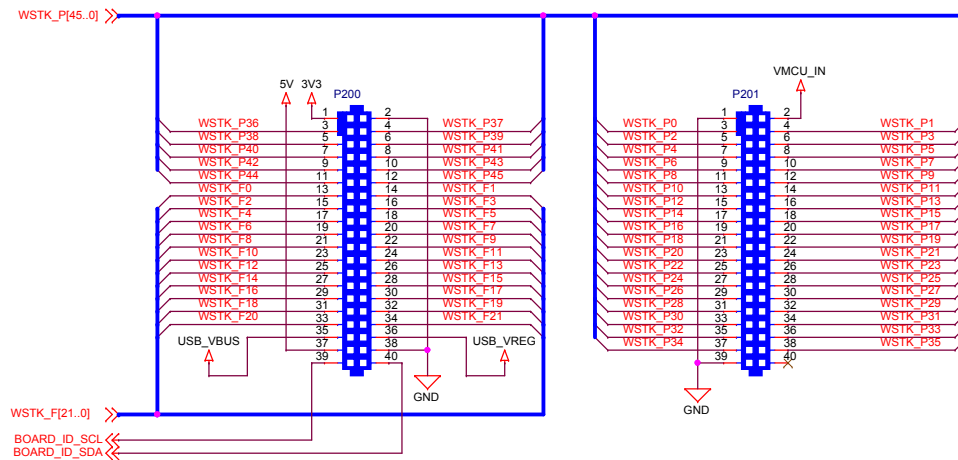
Serial Flash



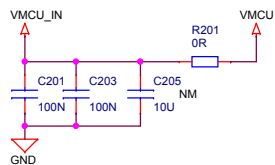
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| | | EFR32FG14 2.4 GHz / 434 MHz 10 dBm Radio Bc | |
| Designed: JSH | | Page Title | |
| Size: A3 | | EFR32 I/O Port Connections | |
| BOM Doc No: <Cage Code> | | Document number | |
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| Sheet Created Date: Wednesday, April 11, 2012 | | A01 | |
| Sheet Modified Date: Tuesday, August 29, 2017 | | Sheet 3 of 4 | |

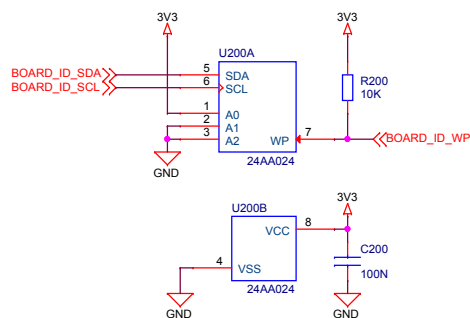
WSTK Connectors



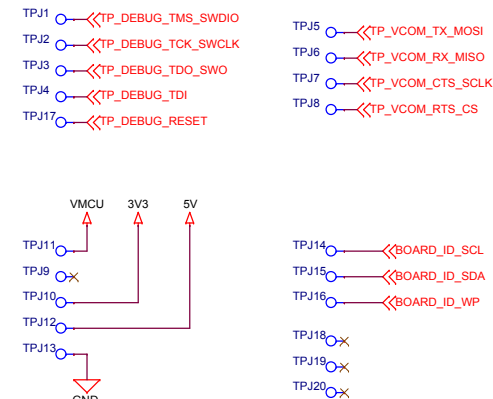
WSTK Power Decoupling




Board Identification



Test Points



<Schematic Path>
SCHEMATIC1

| | | | |
|--|------------------|--|-----------------|
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