



EFR32FG25 Radio Board

915 MHz +16 dBm

Board Function

Page

Title Page

1

RF & Power

2

Board Power

3

EFR32 Signal Assignments

4

WSTK Connectors & Board ID

5



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Revision History

Rev. Description

A00 Initial release.

A01 Updated X1 OPN. Fixed U8/U9 and P900 footprints.

A02 Upd. matching. Added R225.

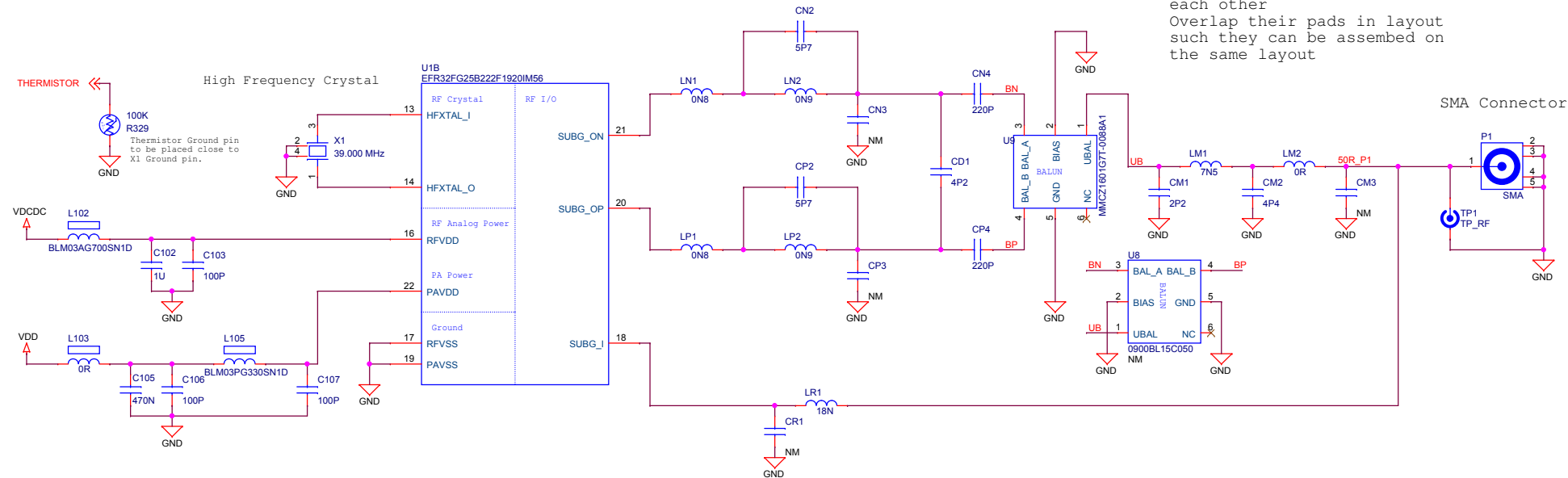
A03 Release with U1 not mounted.

A04 Release with U1 mounted. Updated U1 OPN.

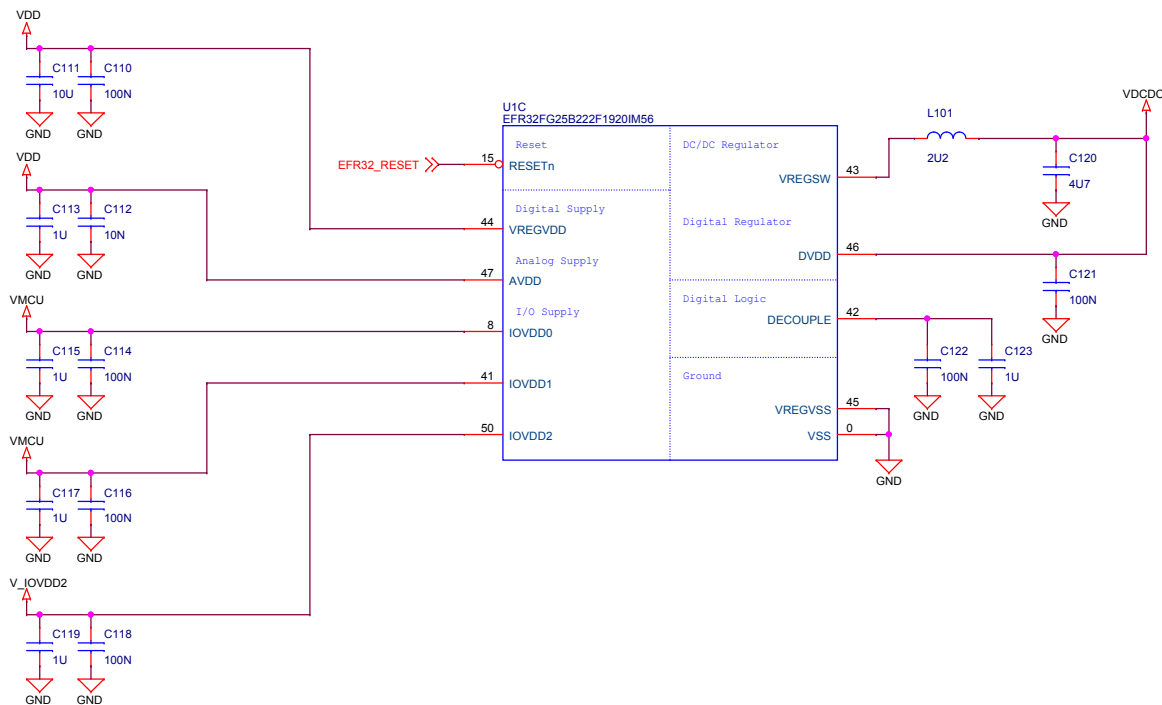
A05 Updated markings to 16dBm. Rearranged filter. Fixed P900 footprint.

		Board Name	
SILICON LABS		FG25 915 MHz 16 dBm Radio Board	
Designed TAB		Approved RGU	
Size A3	Sheet Modified Date Thursday, August 18, 2022		Revision A05
Board Number BRD4270B			Sheet 1 of 5
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Radio Interface

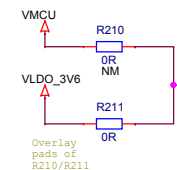


Power & Decoupling

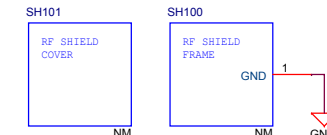


VDD Configuration

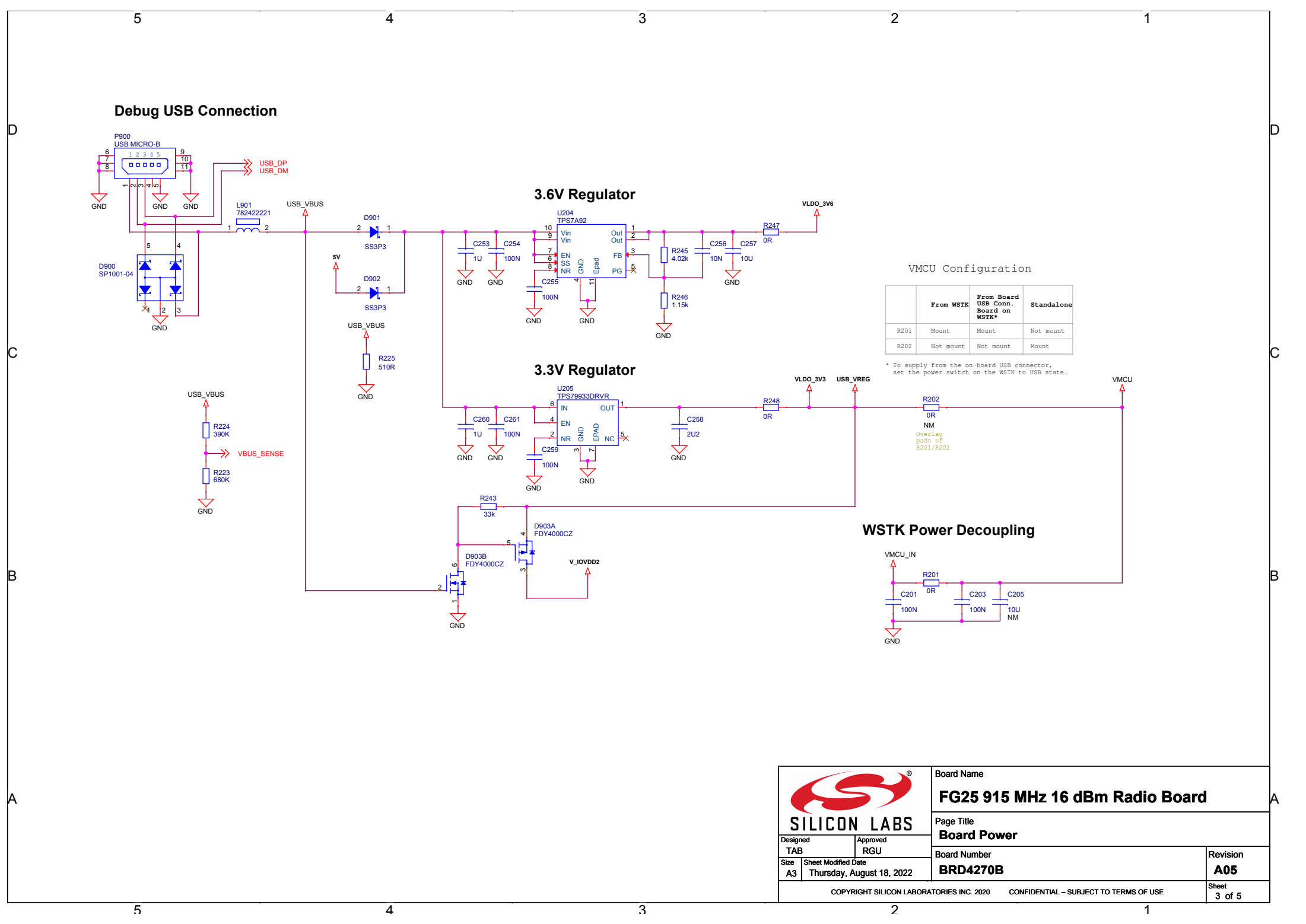
	Power Config 1 VMCU to VDD	Power Config 2 VLDO_3V6 to VDD
R210	Mount	Not mount
R211	Not mount	Mount



RF Shielding



		Board Name	
		FG25 915 MHz 16 dBm Radio Board	
Designed TAB		Page Title	
Size A3		RF & Power	
Sheet Modified Date		Board Number	
Thursday, August 18, 2022		BRD4270B	
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		Revision	
		A05	
		Sheet 2 of 5	



Debug USB Connection

3.6V Regulator

3.3V Regulator

VMCU Configuration

	From WSTK	From Board USB Conn. Board on WSTK*	Standalone
R201	Mount	Mount	Not mount
R202	Not mount	Not mount	Mount

* To supply from the on-board USB connector, set the power switch on the WSTK to USB state.

WSTK Power Decoupling



SILICON LABS

Board Name
FG25 915 MHz 16 dBm Radio Board

Page Title
Board Power

Board Number
BRD4270B

Revision
A05

Designed
TAB

Approved
RGU

Size
A3

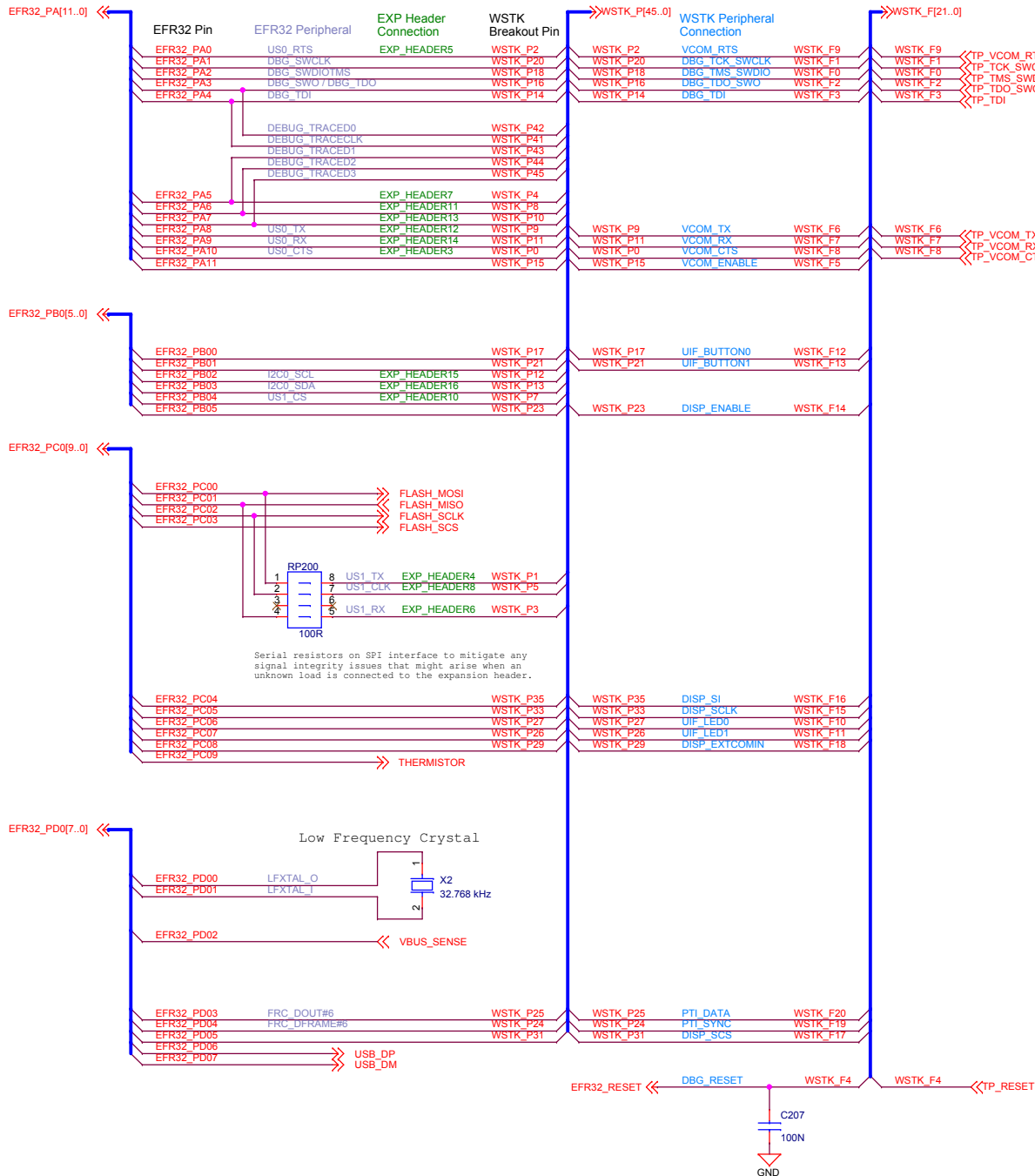
Sheet Modified Date
Thursday, August 18, 2022

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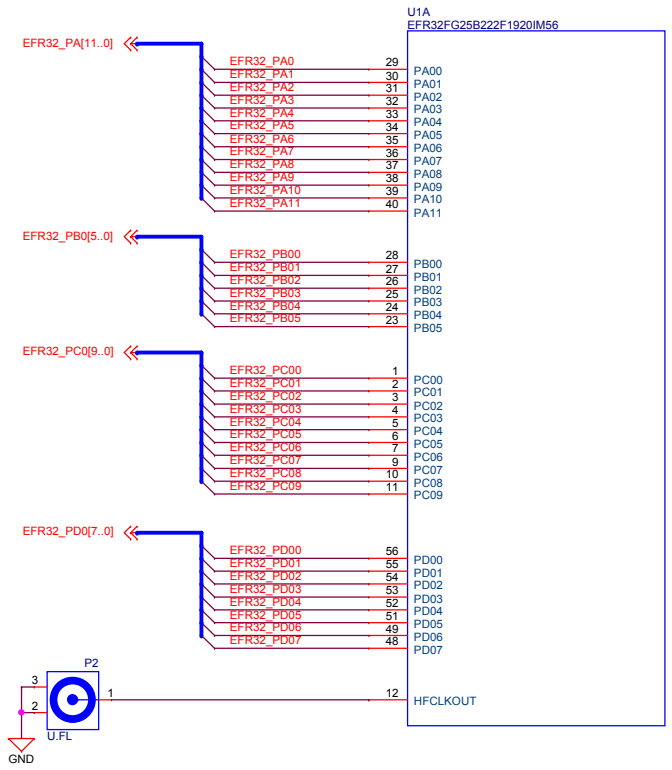
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Sheet
3 of 5

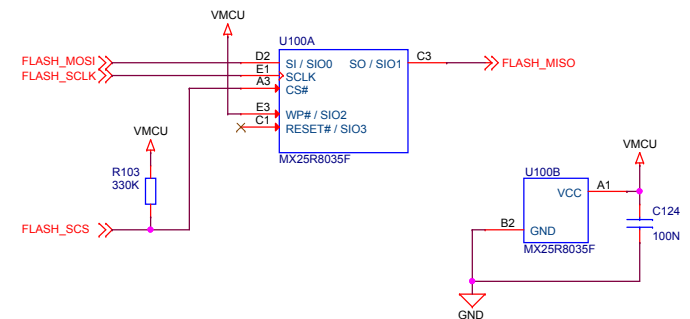
Pin Mapping



I/O Port Pins

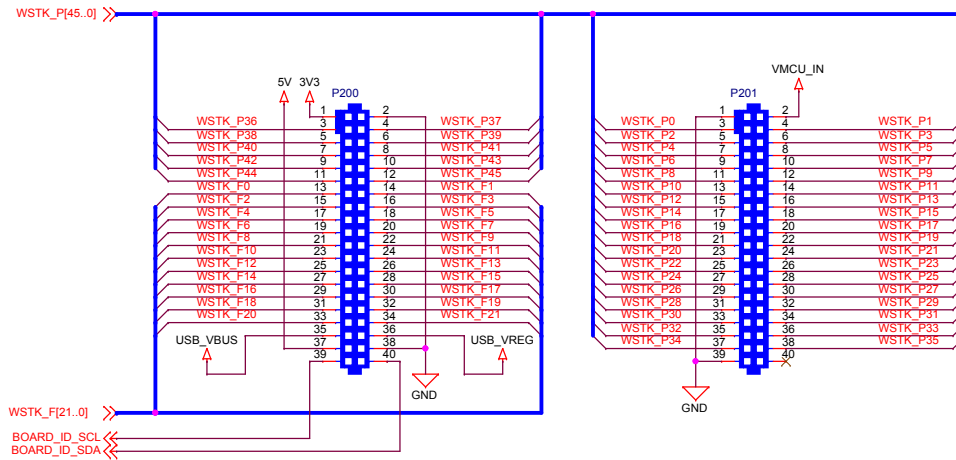


Serial Flash

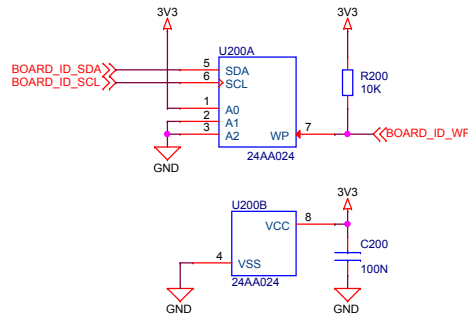


		Board Name	
		FG25 915 MHz 16 dBm Radio Board	
Designed TAB		Page Title	
Approved RGU		EFR32 Signal Assignments	
Size A3		Board Number	
Sheet Modified Date		BRD4270B	
Thursday, August 18, 2022		Revision	
		A05	
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		Sheet 4 of 5	

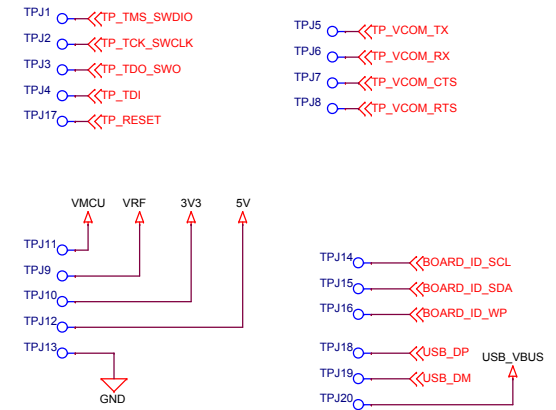
WSTK Connectors



Board Identification



Test Points



		Board Name	
		FG25 915 MHz 16 dBm Radio Board	
Designed TAB		Page Title	
Size A3		WSTK Connectors & Board ID	
Sheet Modified Date Thursday, August 18, 2022		Board Number	Revision
		BRD4270B	A05
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Sheet 5 of 5			