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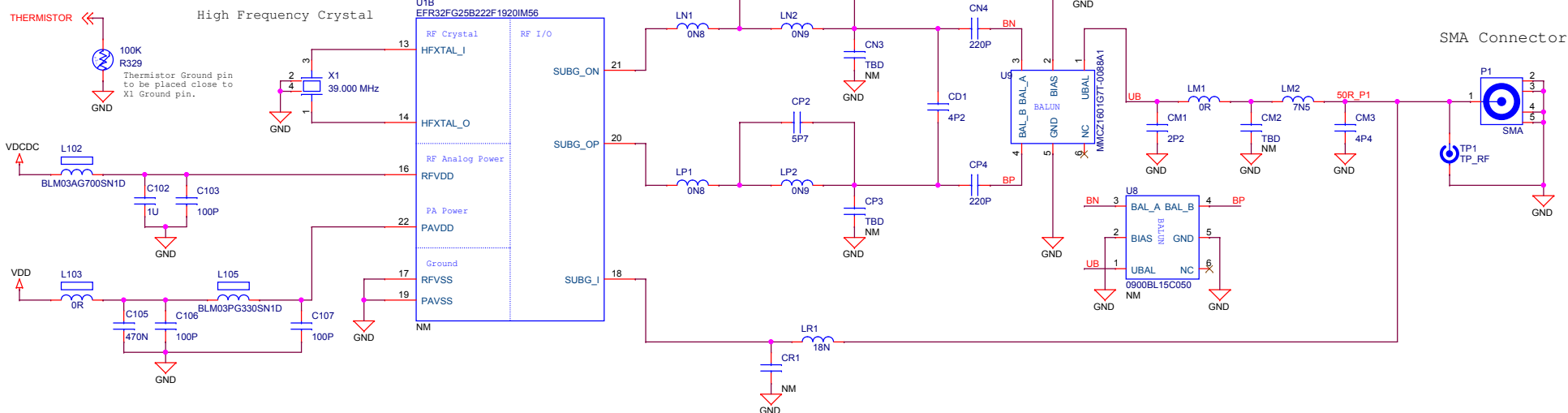


EFR32FG25 Radio Board 915 MHz +14 dBm	
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Revision History	
Rev.	Description
A00	Initial release.
A01	Updated X1 OPN. Fixed U8/U9 and P900 footprints.
A02	Upd. matching. Added R225.
A03	Release with U1 not mounted.

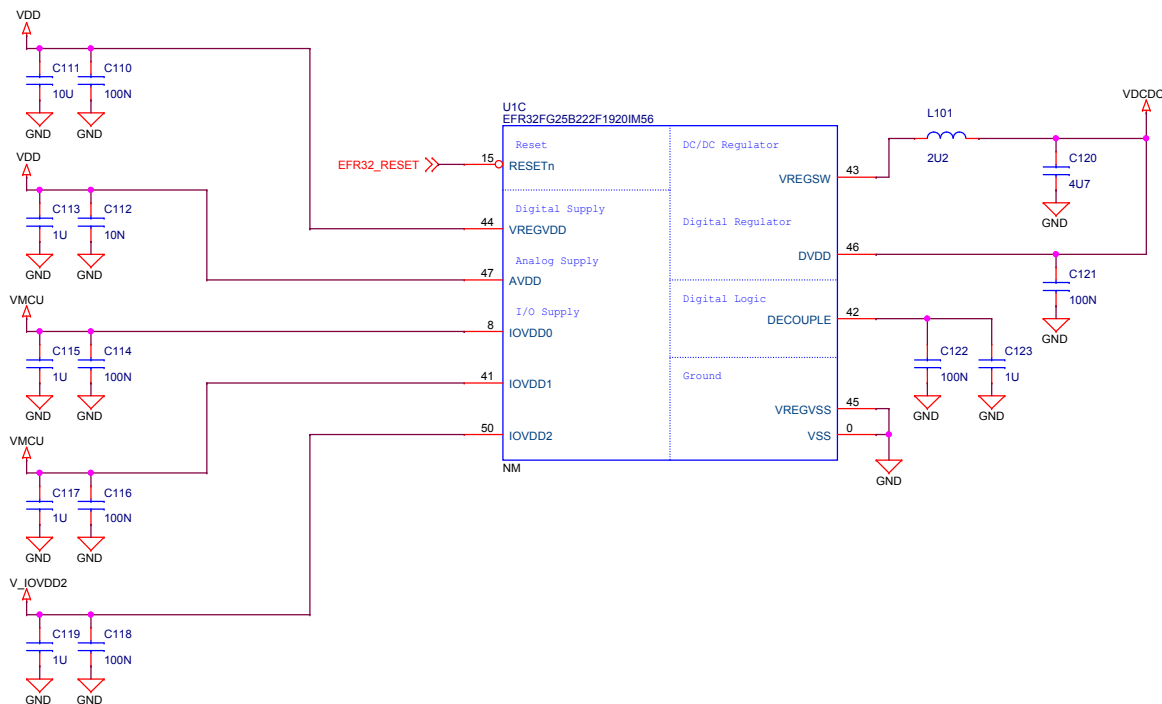
 SILICON LABS		Board Name FG25 915 MHz 14 dBm Radio Board	
		Page Title Title Page	
Designed TAB	Approved RGU	Board Number BRD4270B	Revision A03
Size A3	Sheet Modified Date Tuesday, June 07, 2022	Sheet 1 of 5	
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Radio Interface



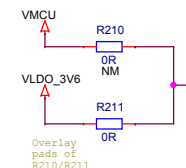
U8 and U9 are substitutes of each other
Overlap their pads in layout such they can be assembled on the same layout

Power & Decoupling



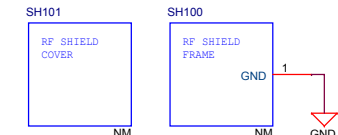
VDD Configuration

	Power Config 1 VMCU to VDD	Power Config 2 VLDO_3V6 to VDD
R210	Mount	Not mount
R211	Not mount	Mount



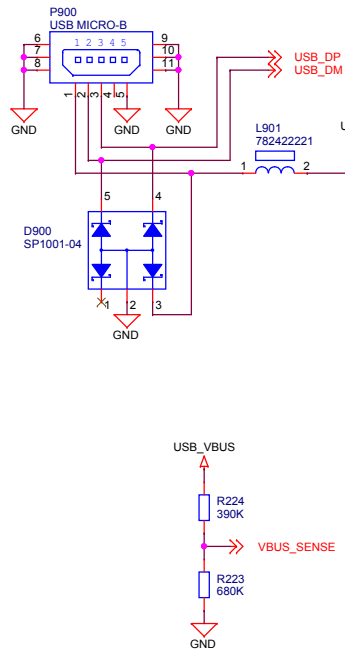
Overlay pads of R210/R211

RF Shielding

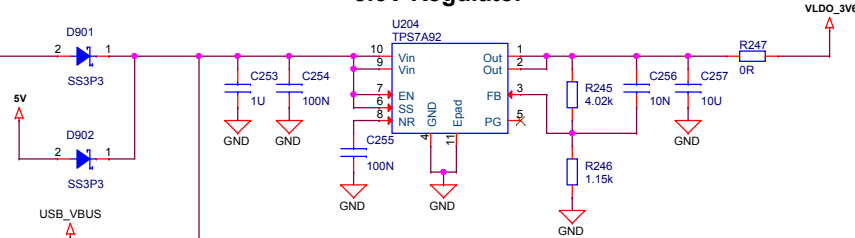


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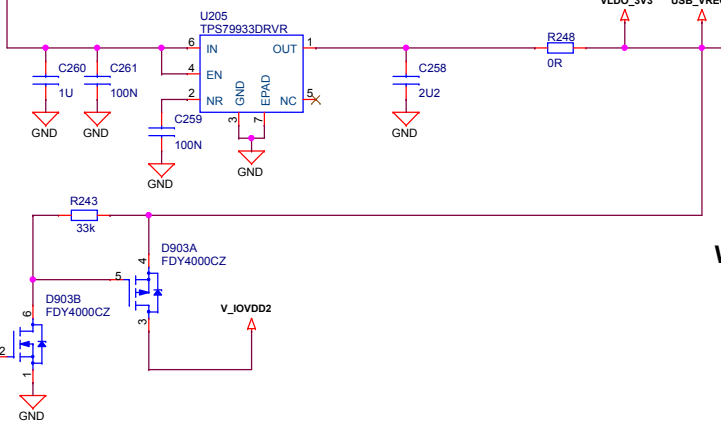
Debug USB Connection



3.6V Regulator



3.3V Regulator

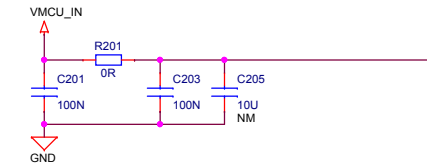



VMCU Configuration

	From WSTK	From Board USB Conn. Board on WSTK*	Standalone
R201	Mount	Mount	Not mount
R202	Not mount	Not mount	Mount

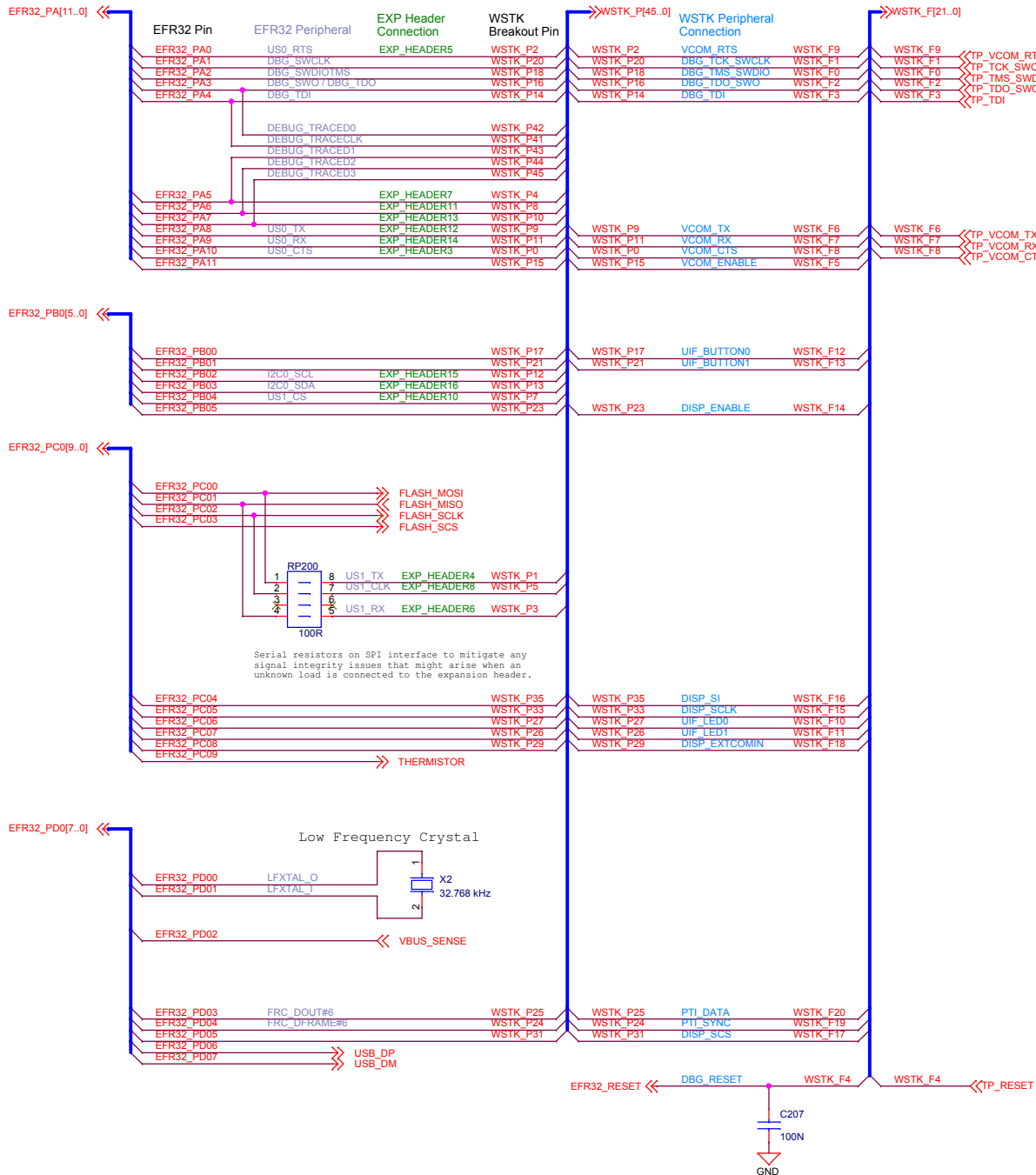
* To supply from the on-board USB connector, set the power switch on the WSTK to USB state.

WSTK Power Decoupling

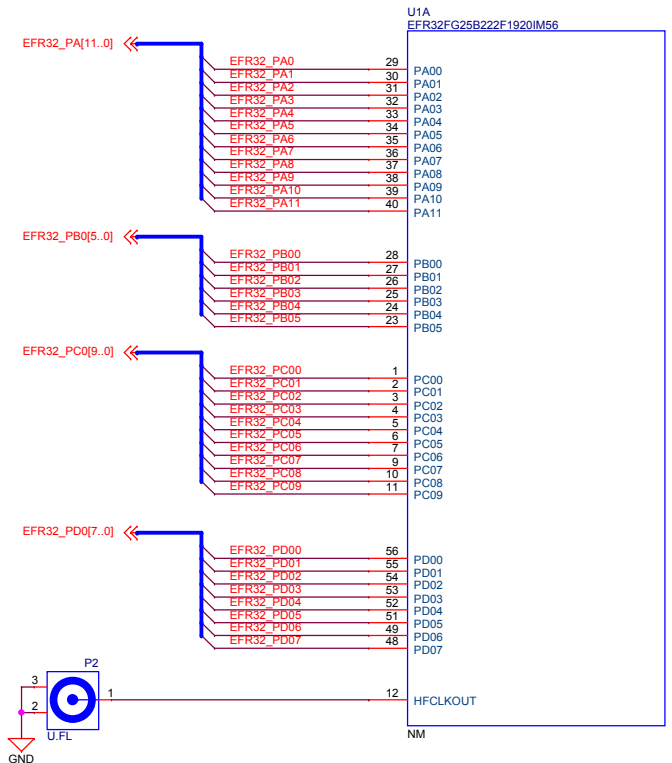


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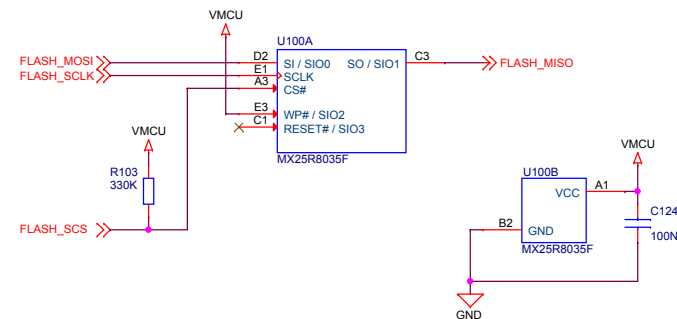
Pin Mapping



I/O Port Pins

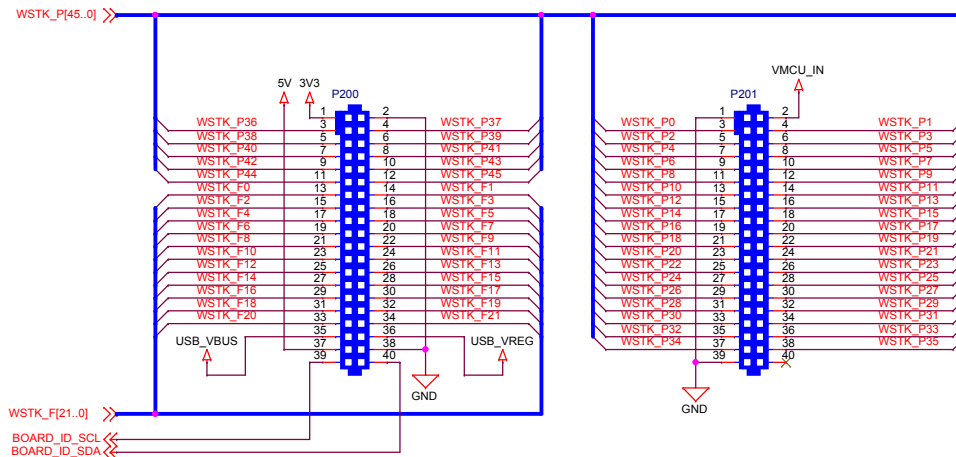


Serial Flash

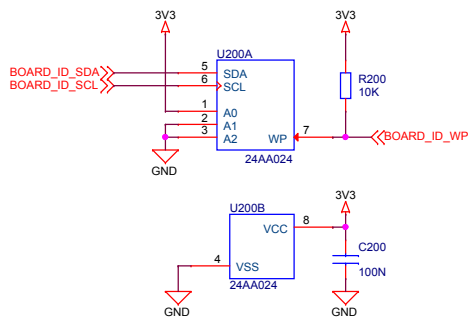


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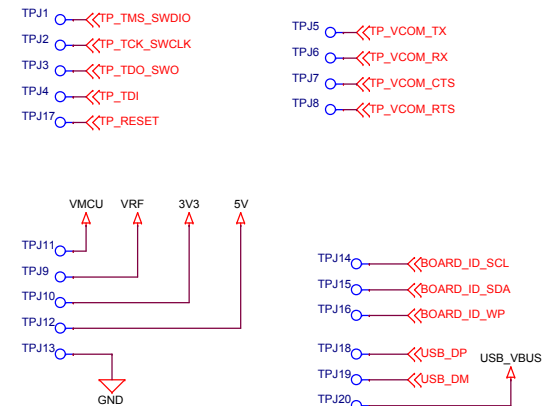
WSTK Connectors




Board Identification



Test Points



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