



EFR32FG25 Radio Board

868 MHz +14 dBm

Board Function

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RF & Power

2

Board Power

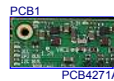
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EFR32 Signal Assignments

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WSTK Connectors & Board ID

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Revision History

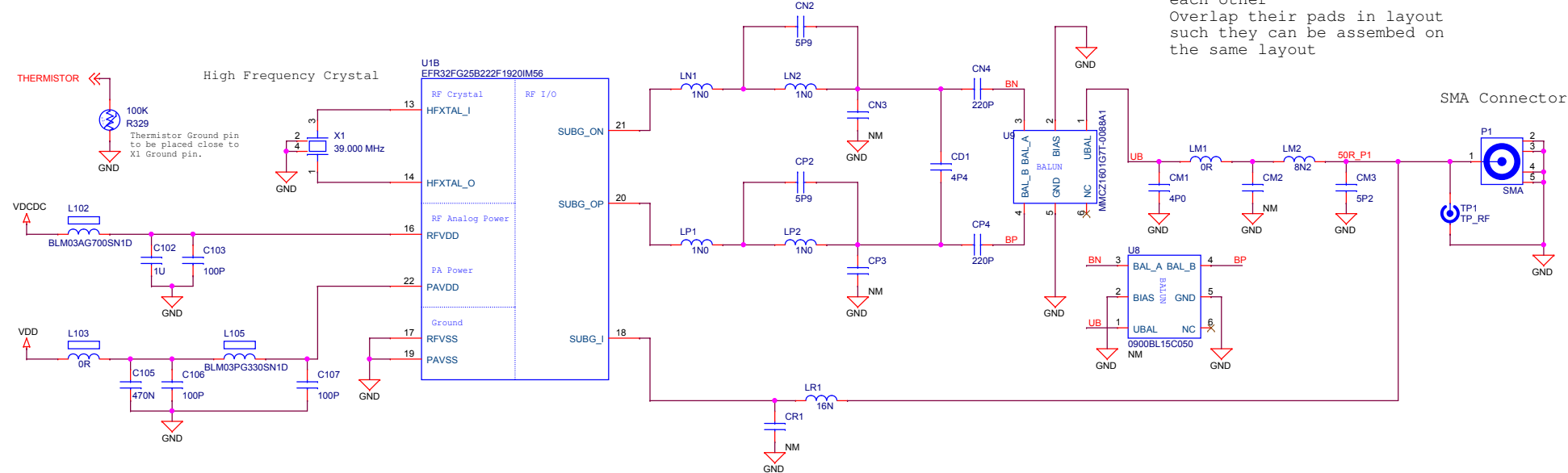
Rev. Description

A00 Initial release.

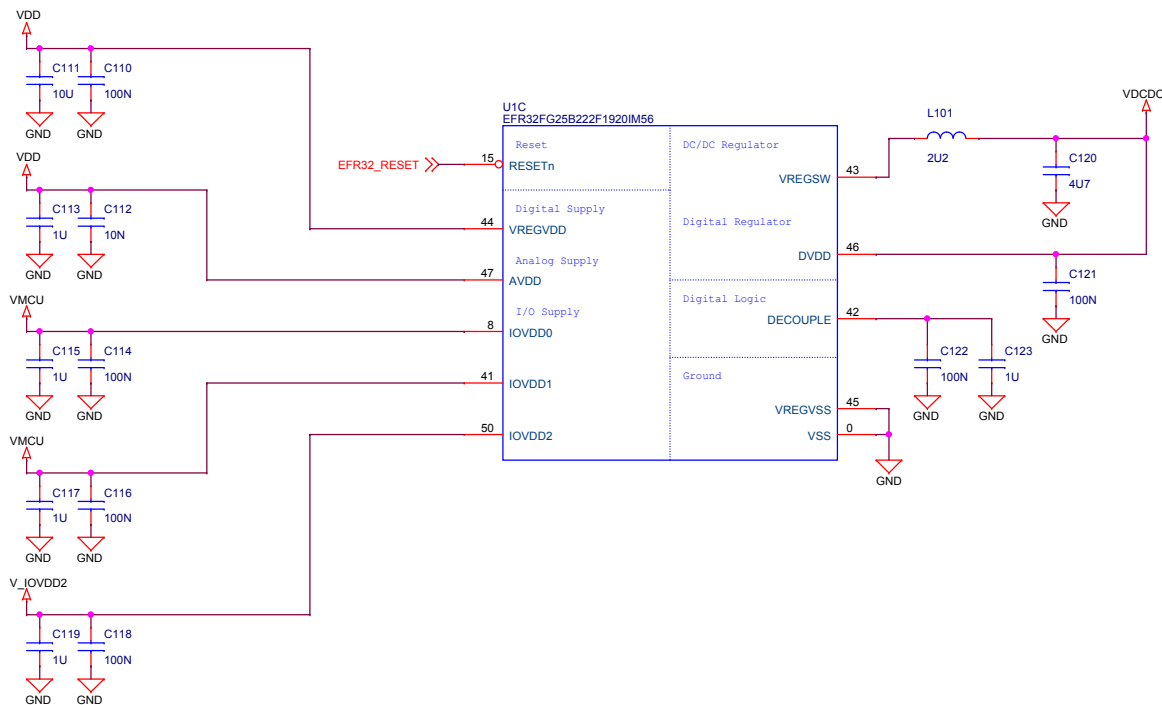
A01 Upd. match. nw., X1, C112, C256. Fixed P900 footprint. Thermistor, VBUS_Sense added.

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Radio Interface

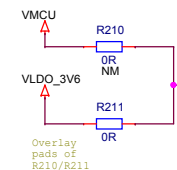


Power & Decoupling

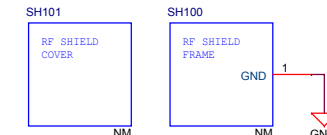


VDD Configuration

	Power Config 1 VMCU to VDD	Power Config 2 VLDO_3V6 to VDD
R210	Mount	Not mount
R211	Not mount	Mount

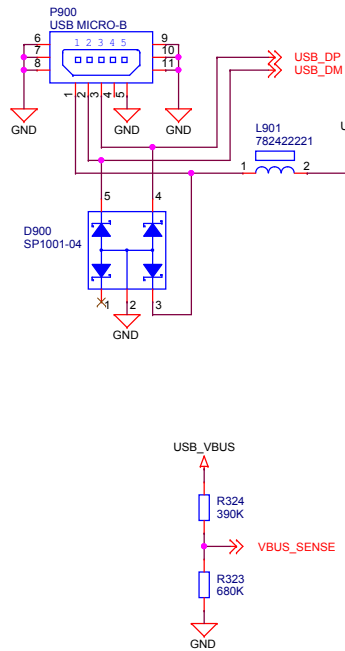


RF Shielding

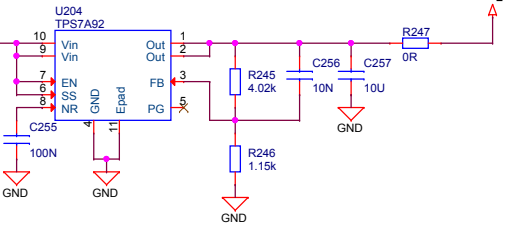


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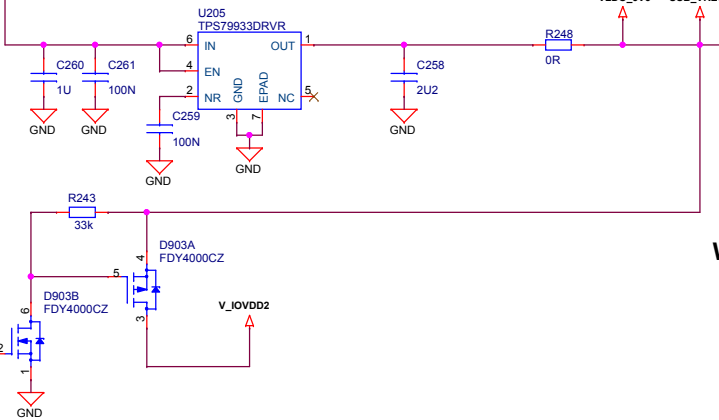
Debug USB Connection



3.6V Regulator



3.3V Regulator

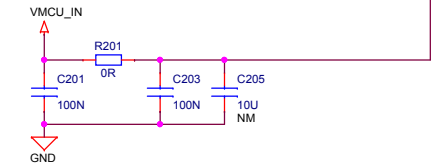



VMCU Configuration

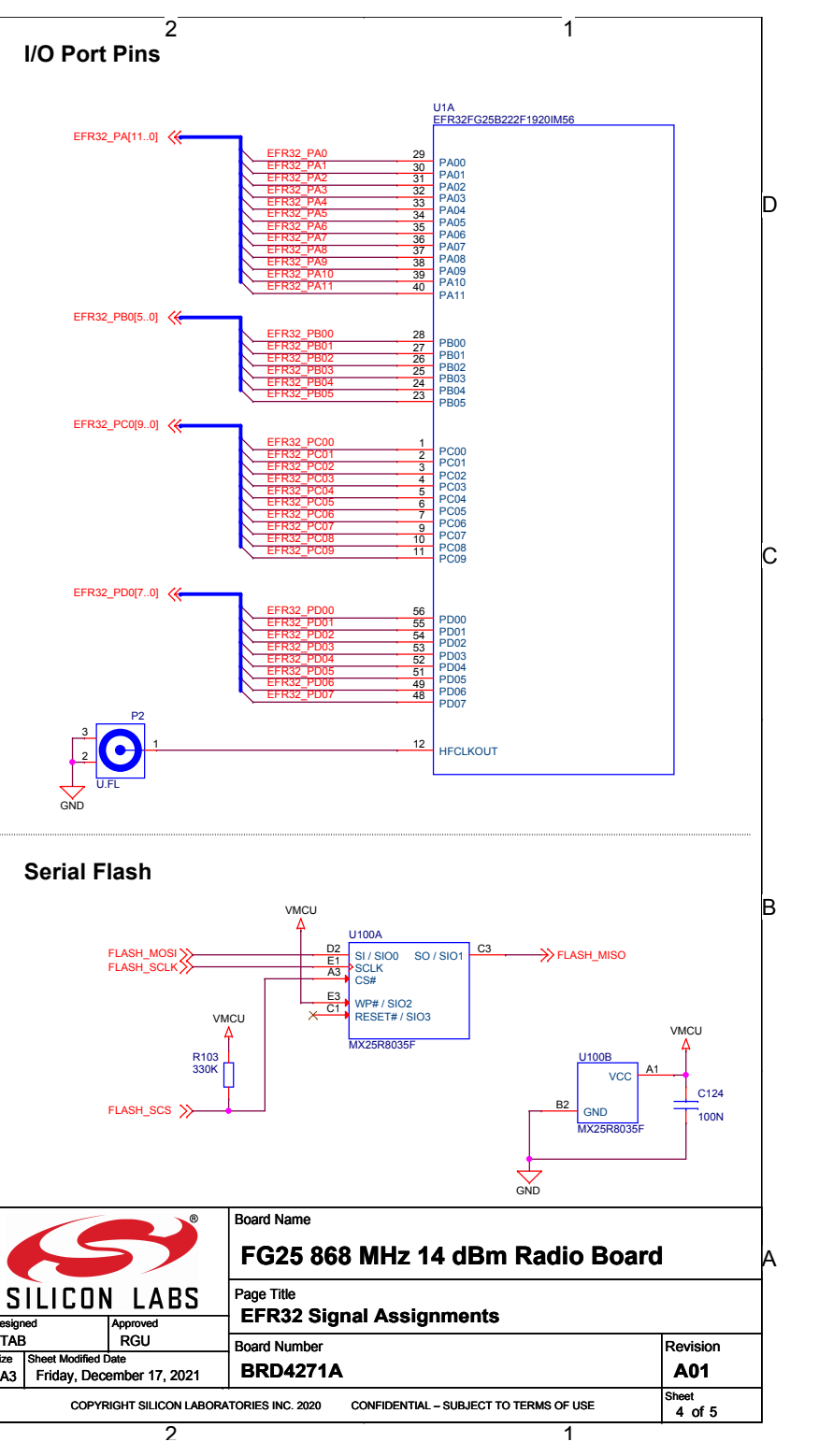
	From WSTK	From Board USB Conn. Board on WSTK*	Standalone
R201	Mount	Mount	Not mount
R202	Not mount	Not mount	Mount

* To supply from the on-board USB connector, set the power switch on the WSTK to USB state.


WSTK Power Decoupling



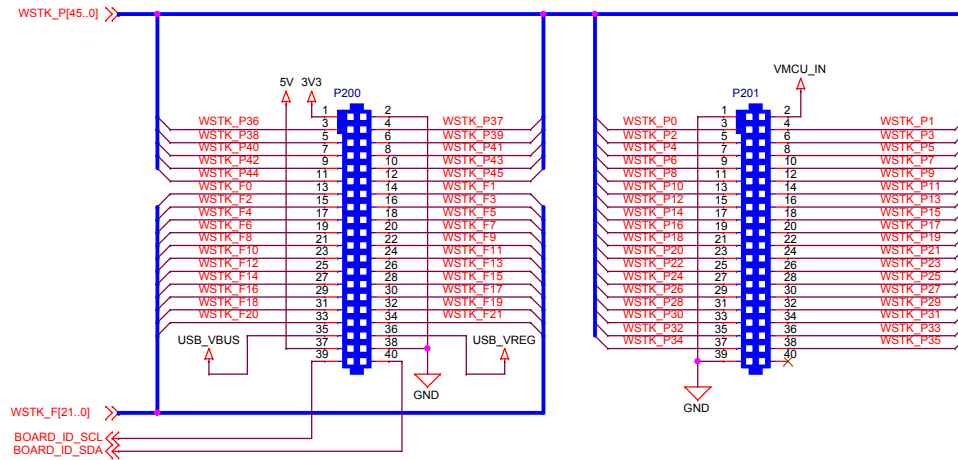
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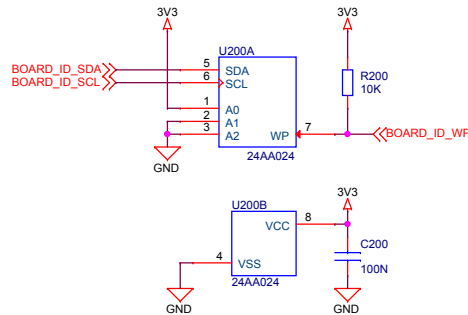
The schematic diagram illustrates the connection of the MX25R8035F flash memory. It features two components, U100A and U100B, both labeled MX25R8035F. U100A is connected to the FLASH_MOSI, FLASH_SCLK, and FLASH_MISO signals. U100B is connected to VCC and GND. The circuit includes a 330K resistor (R103) connected to VMCU and a 100nF capacitor (C124) connected to VMCU. The diagram is labeled B.

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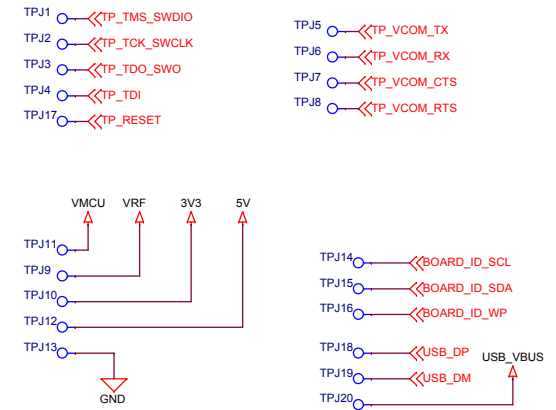
WSTK Connectors




Board Identification



Test Points



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