



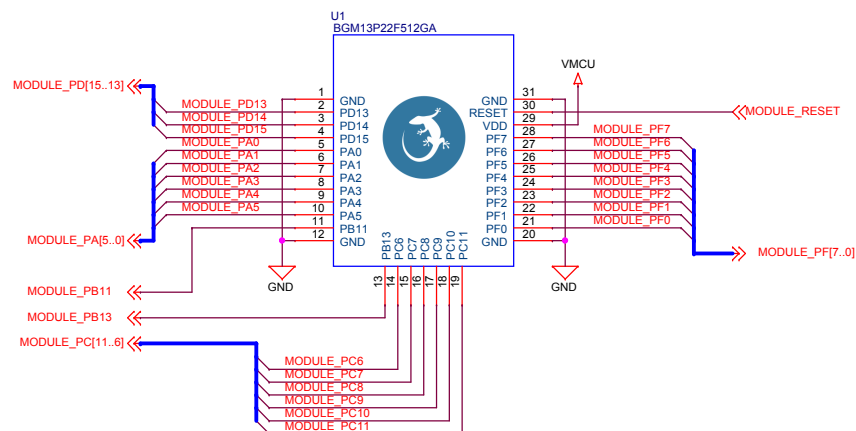
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SCHEMATIC1

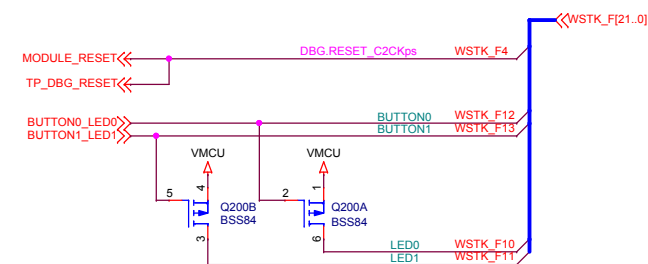
 SILICON LABS		Schematic Title BGM13P WSTK Radio Board	
Designed: DDB		Page Title Title Page	
Size A3		Document number BRD4306A	Revision A01
BOM Doc No: <Cage Code>		Sheet Created Date Monday, April 02, 2012	Sheet Modified Date Tuesday, November 21, 2017
Design Created Date: Monday, April 02, 2012		Sheet 1 of 3	

Module Footprint



Additional Connections

Reset input from mainboard

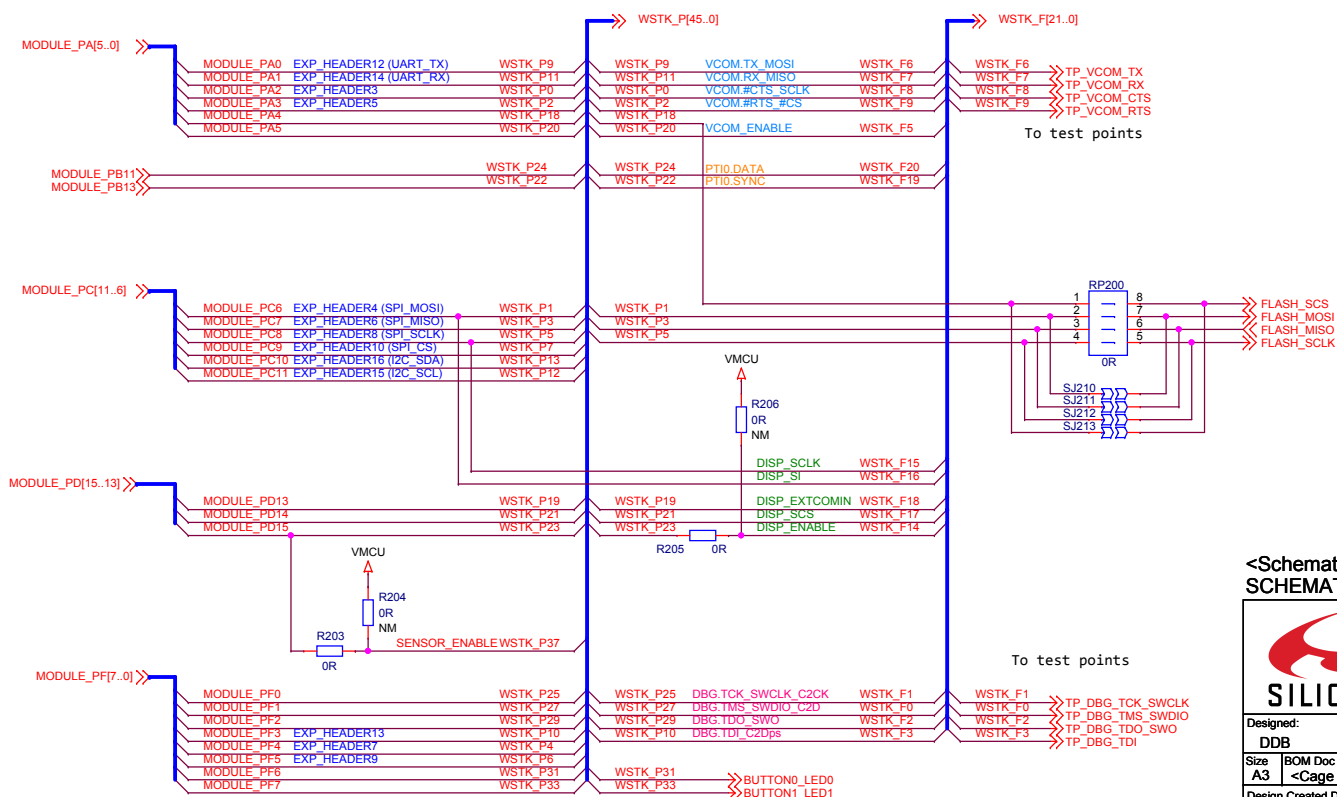


Buttons and leds connected to the same I/O pins:
Output: 0 = LED ON, 1 = LED OFF
Input: 0 = BUTTON PRESSED, 1 = BUTTON RELEASED

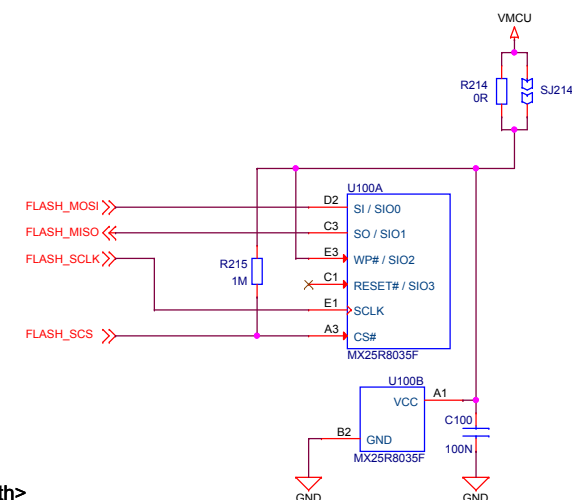
Pin Assignments

Breakout connections

Functional Connections



Serial Flash



<Schematic Path>
SCHEMATIC1



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Monday, April 02, 2012

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Schematic Title

BGM13P WSTK Radio Board

Page Title

	Module
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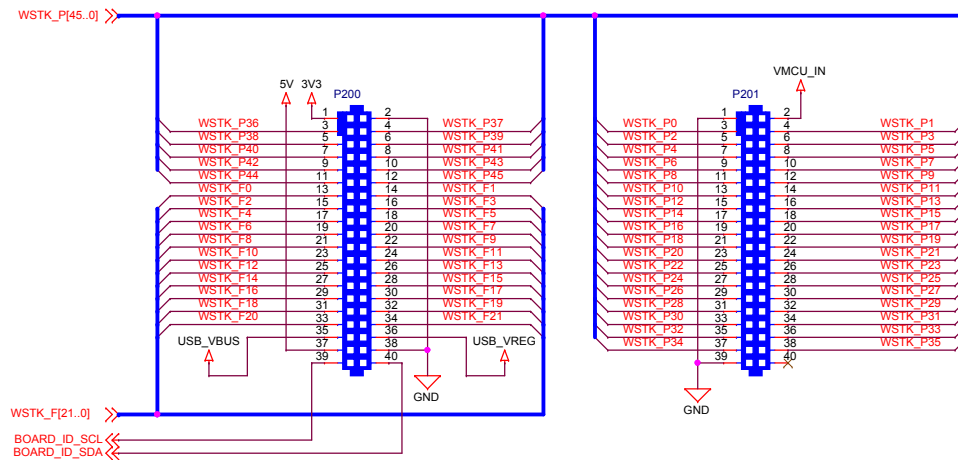
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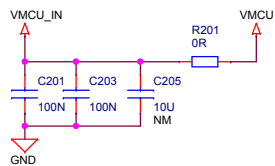
Revision
A01

Sheet Modified Date Tuesday, November 21, 2017	Sheet 2 of
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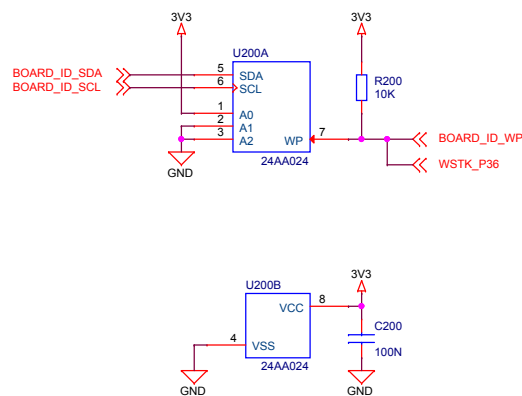
WSTK Connectors



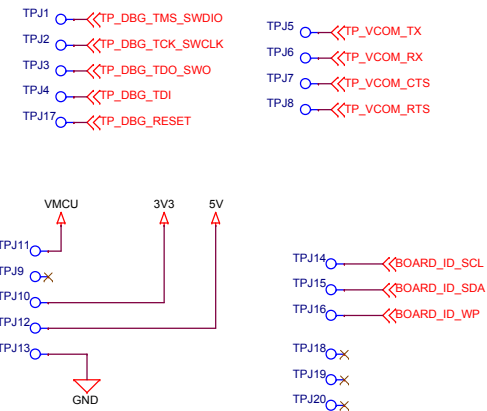
WSTK Power Decoupling



Board Identification



Test Points



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SCHEMATIC1

		Schematic Title	
		BGM13P WSTK Radio Board	
Designed: DDB		Page Title	
Approved: JNO		WSTK Connectors & Board ID	
Size: A3	BOM Doc No: <Cage Code>	Document number	Revision
		BRD4306A	A01
Design Created Date: Monday, April 02, 2012		Sheet Created Date: Tuesday, April 03, 2012	Sheet Modified Date: Tuesday, November 21, 2017
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