



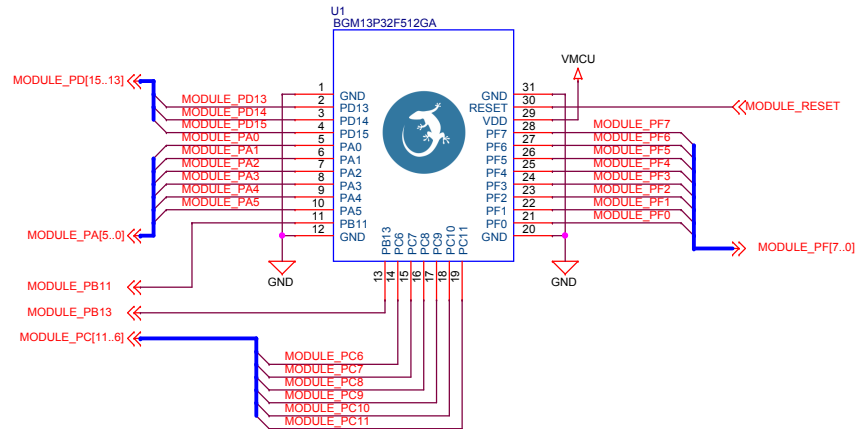
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SCHEMATIC1

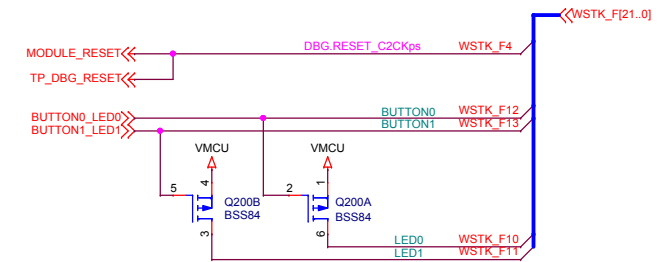
 SILICON LABS		Schematic Title	
		BGM13P WSTK Radio Board	
Designed: DDB		Page Title	
Approved: JNO		Title Page	
Size A3	BOM Doc No: <Cage Code>	Document number	Revision
Design Created Date: Monday, April 02, 2012		BRD4306B	A01
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Module Footprint



Additional Connections

Reset input from mainboard

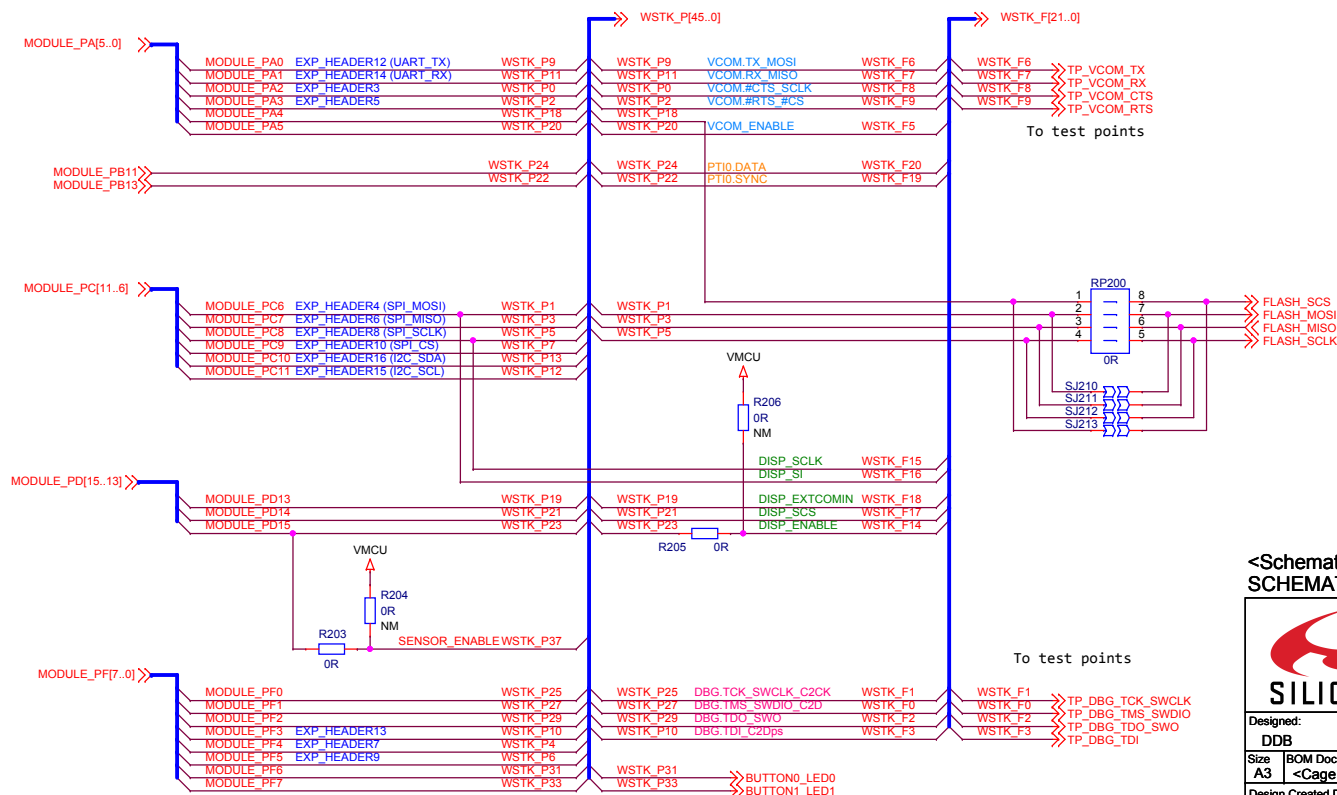


Buttons and leds connected to the same I/O pins:
Output: 0 = LED ON, 1 = LED OFF
Input: 0 = BUTTON PRESSED, 1 = BUTTON RELEASED

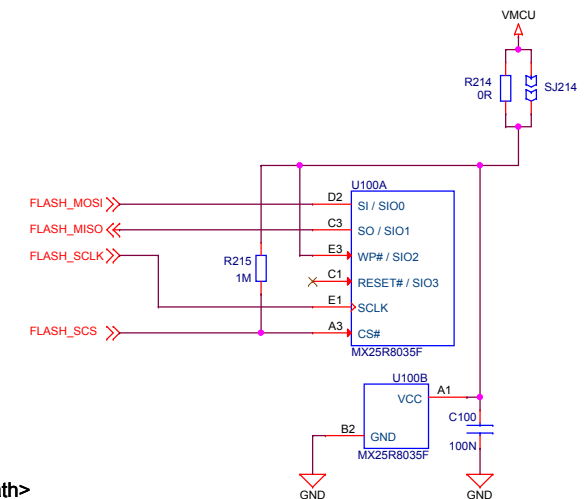
Pin Assignments

Breakout connections

Functional Connections



Serial Flash



<Schematic Path>
SCHEMATIC1



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Design Created Date:
Monday, April 02, 2012

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BGM13P WSTK Radio Board

Page Title

	Module
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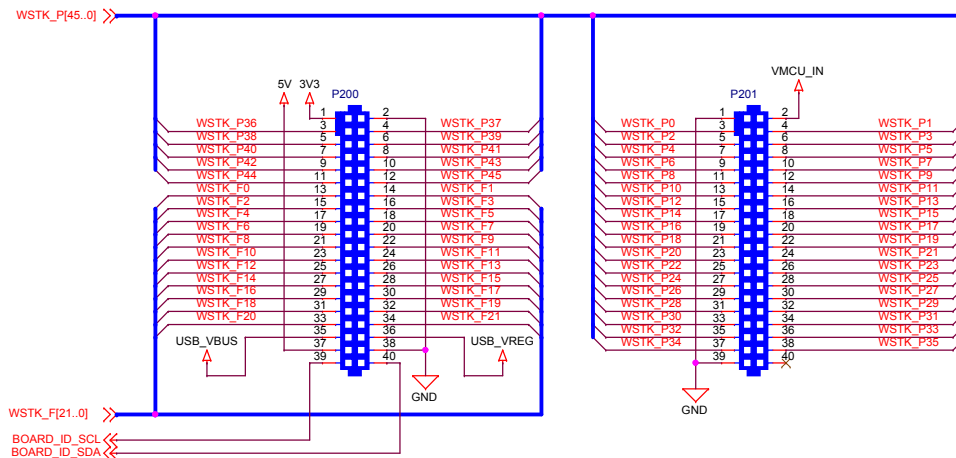
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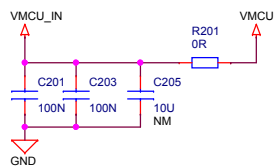
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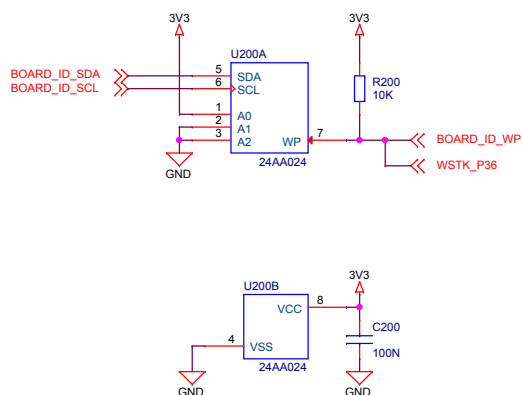
WSTK Connectors



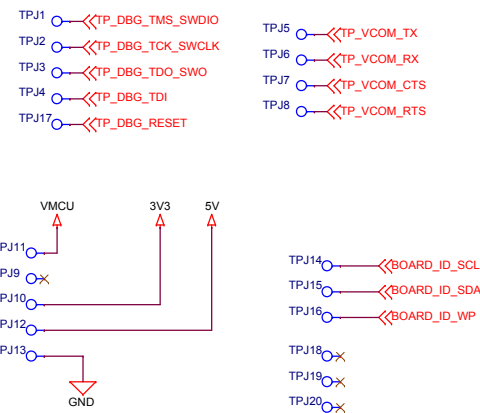
WSTK Power Decoupling



Board Identification



Test Points



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SCHEMATIC1

		Schematic Title	
		BGM13P WSTK Radio Board	
Designed: DDB		Page Title	
Approved: JNO		WSTK Connectors & Board ID	
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