



LEGAL NOTICE:
SILICON LABORATORIES INC. ("SILICON LABS") AND/OR ITS LICENSORS DO NOT WARRANT THE ACCURACY OR COMPLETENESS OF THIS SCHEMATIC OR ANY INFORMATION CONTAINED WITHIN THIS SCHEMATIC. IT IS PROVIDED "AS-IS" FOR REFERENCE ONLY. SILICON LABS DOES NOT WARRANT THAT THIS DESIGN WILL MEET THE SPECIFICATIONS, BE SUITABLE FOR YOUR APPLICATION OR FIT FOR ANY PARTICULAR PURPOSE, OR WILL OPERATE IN YOUR IMPLEMENTATION. SILICON LABS AND ITS LICENSORS DO NOT WARRANT THAT THE DESIGN IMPLIED IN THIS SCHEMATIC IS PRODUCTION-WORTHY. YOU SHOULD COMPLETELY VALIDATE AND TEST YOUR DESIGN IMPLEMENTATION TO CONFIRM SYSTEM FUNCTIONALITY FOR YOUR APPLICATION.



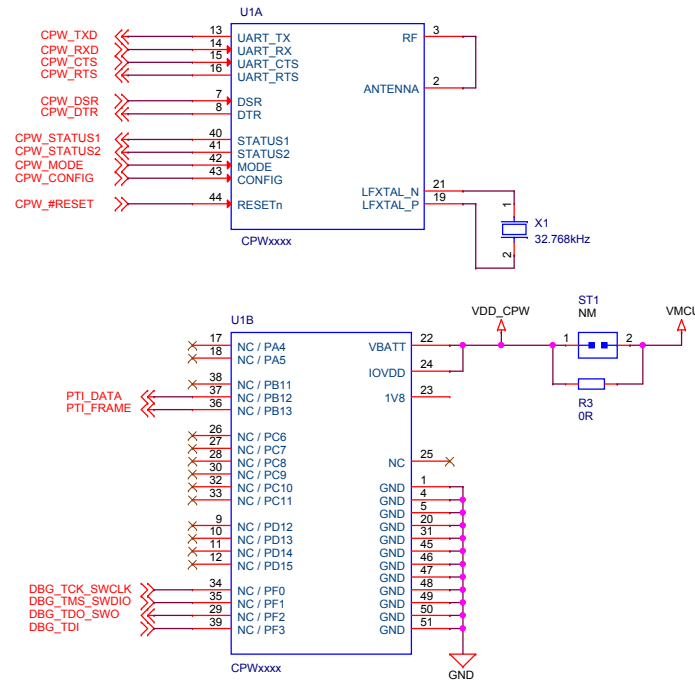
Revision History	
Rev.	Description
A00	Initial version

WirelessXpress EXP Board	
CPWxxx	
Board Function	Page
Title Page	1
wirelessXpress	2
EXP Header & CP2102N	3

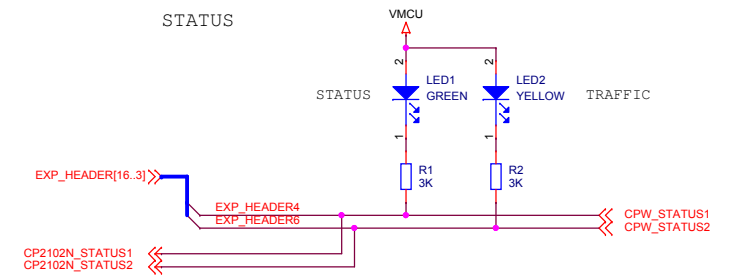
<Schematic Path>
SCHEMATIC1

 SILICON LABS		Schematic Title	
		CPWxxxx WirelessXpress EXP Board	
Designed: DDB		Page Title	
Approved: JNO		Title Page	
Size A3	BOM Doc No: <Cage Code>	Document number BRD8024	Revision A00
Design Created Date: Monday, April 02, 2012		Sheet Created Date Monday, April 02, 2012	Sheet Modified Date Tuesday, October 31, 2017
		Sheet 1 of 3	

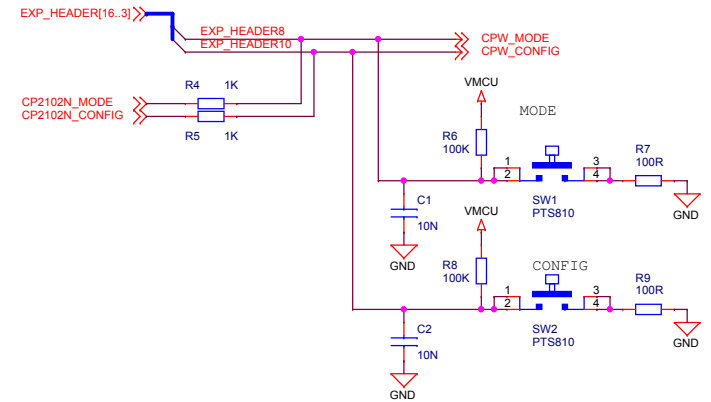
CPWxxxx WirelessXpress 'Cable Replacement' Device



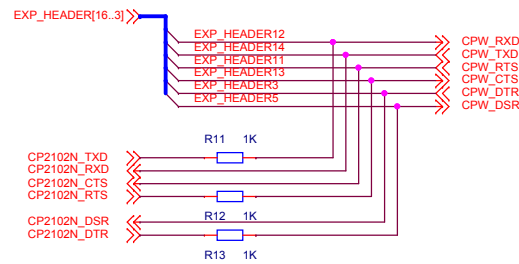
STATUS



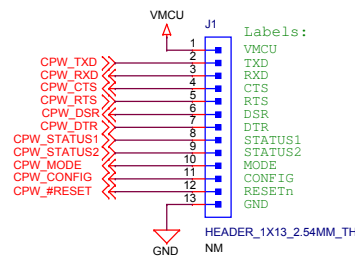
MODE/CONFIG



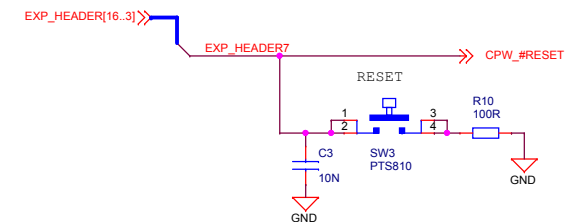
Modem Connections



Breakout Connector



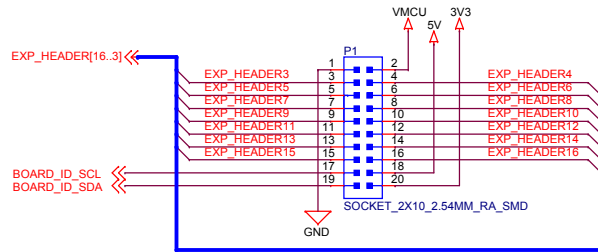
RESET



<Schematic Path> SCHEMATIC1

		Schematic Title	
		CPWxxxx WirelessXpress EXP Board	
Designed: DDB		Page Title	
Approved: JNO		CPWxxxx, buttons, indicators and connections	
Size A3		Document number	
BOM Doc No: <Cage Code>		BRD8024	
Design Created Date: Monday, April 02, 2012		Revision	
Sheet Created Date: Monday, April 02, 2012		A00	
Sheet Modified Date: Monday, October 30, 2017		Sheet	
		2 of 3	

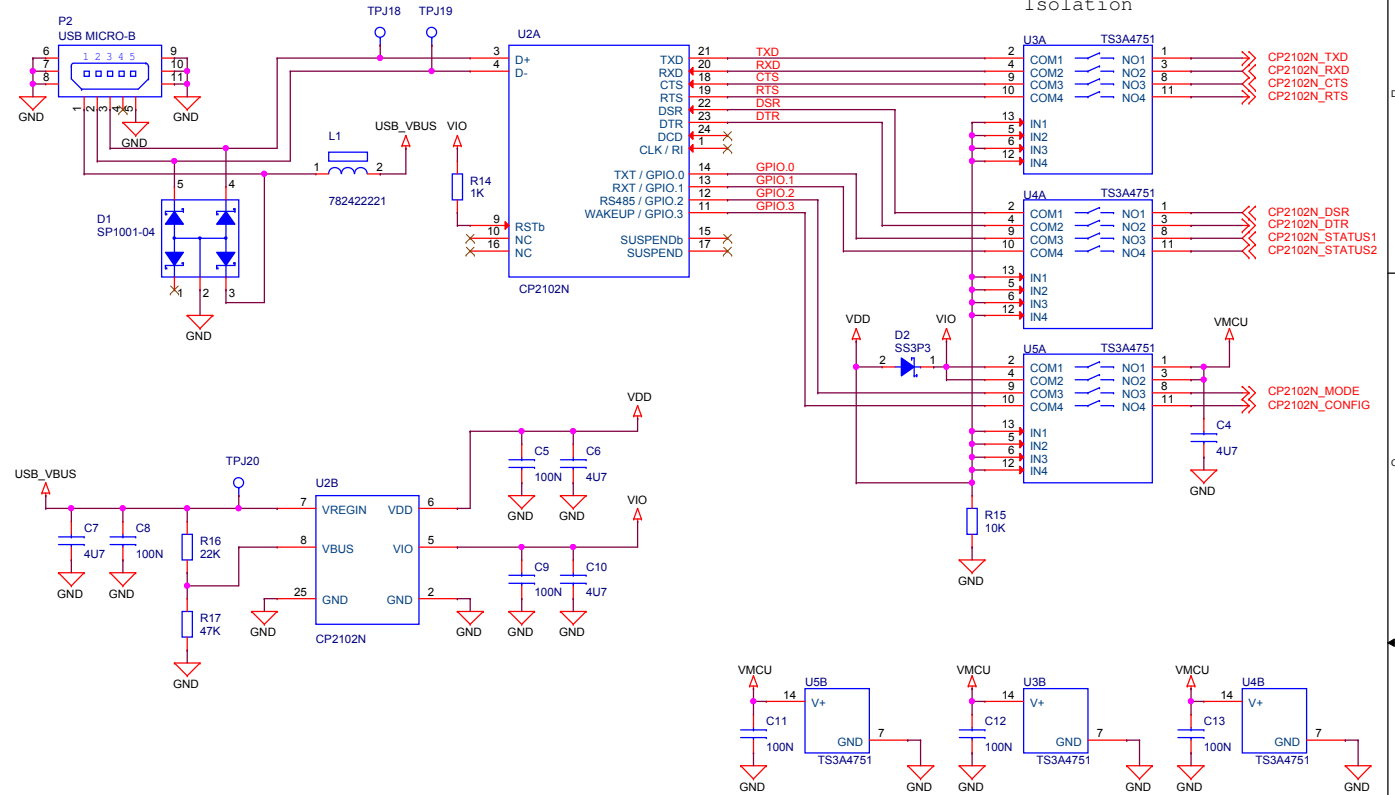
EXP Header



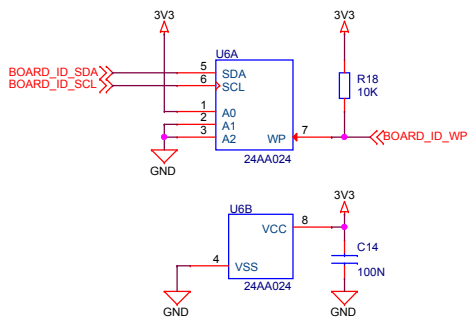
EXP-Header Functionality

1	GND	CPW_DTR
3	GPIO	CPW_DSR
5	GPIO	CPW_RESET
7	GPIO	
9	GPIO	
11	GPIO	CPW_RTS
13	GPIO	CPW_CTS
15	I2C_SCL	
17	Board ID_SCL	
19	Board ID_SDA	
2	VMCU	
4	SPI_MOSI	CPW_STATUS1
6	SPI_MISO	CPW_STATUS2
8	SPI_SCK	CPW_MODE
10	SPI_CS	CPW_CONFIG
12	UART_TX	CPW_RXD
14	UART_RX	CPW_TXD
16	I2C_SDA	
18	5V	
20	3V3	

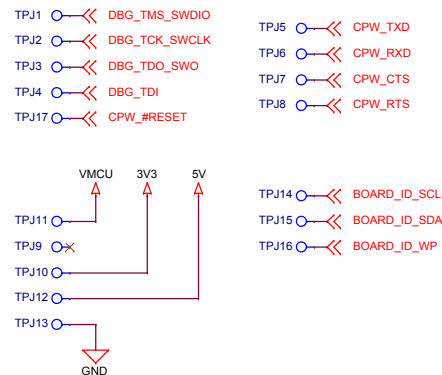
CP2102N USBXpress Bridge



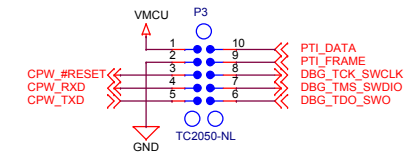
Board Identification



Test Points



Internal Development (!)



<Schematic Path> SCHEMATIC1

		Schematic Title CPWxxxx WirelessXpress EXP Board	
Page Title EXP Header, CP2102N, Board ID and test points		Revision A00	
Designed: <OrgAddr1>	Approved: <OrgAddr2>	Document number BRD8024	Sheet Created Date Thursday, April 06, 2017
Size A3	BOM Doc No: <Cage Code>	Sheet Modified Date Monday, October 30, 2017	Sheet 3 of 3
Design Created Date: Monday, April 02, 2012		Sheet Created Date Thursday, April 06, 2017	