



## EFM32 Leopard Gecko STK

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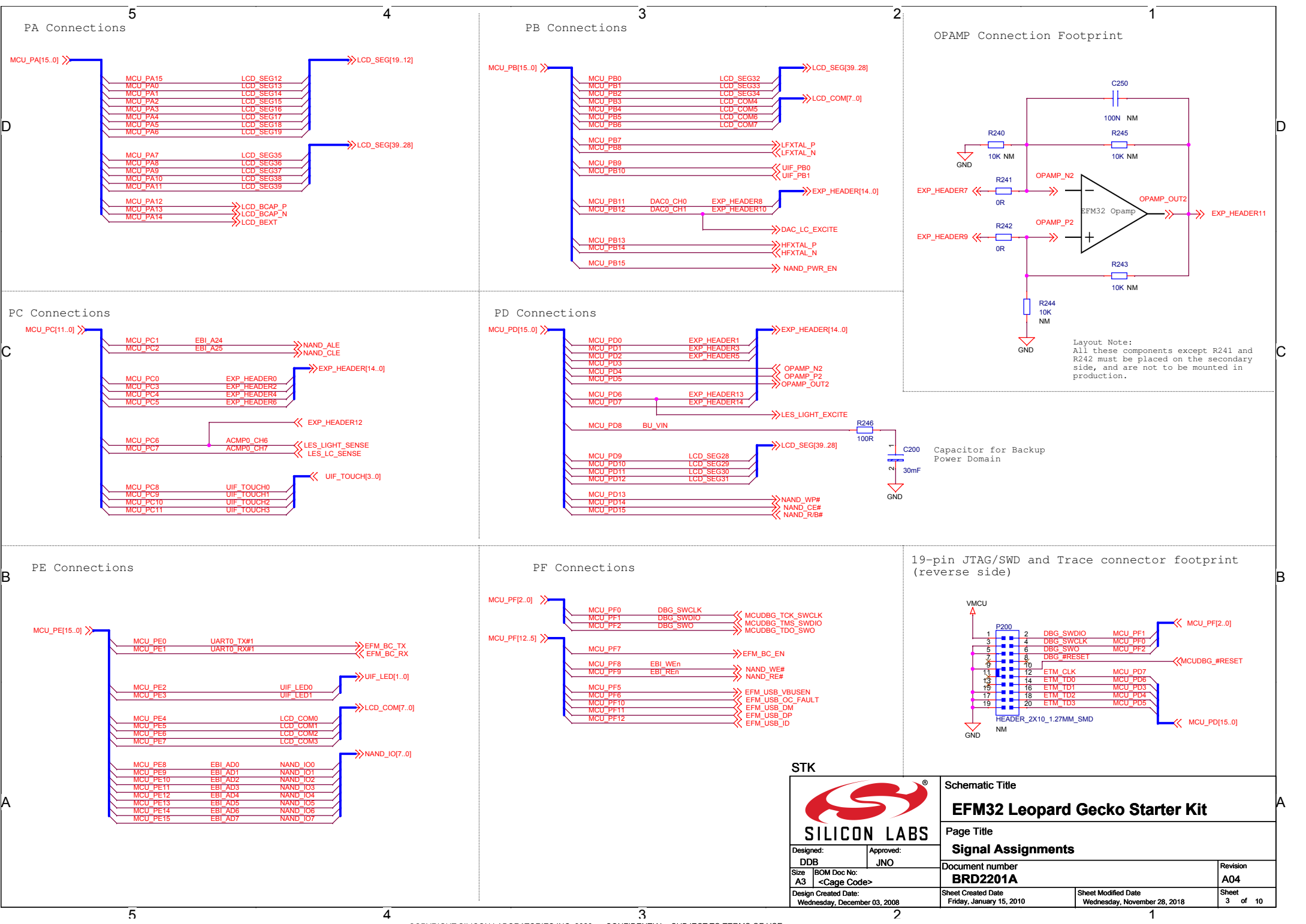
## Revision History

Rev.	Description
A00	Initial Version for series production
A01	Updated EFM32 to Leopard Gecko Rev.D
A02	Added test points for EFM USB. Fixed small error on PCB.
A03	Updated EFM32 to Leopard Gecko Rev.E. Update: Fixed EXP pinout description Change to winbond NAND flash
A04	


### STK

 <b>SILICON LABS</b>		<b>Schematic Title</b> <b>EFM32 Leopard Gecko Starter Kit</b>	
<b>Designed:</b> DDB		<b>Page Title</b> <b>Title Page</b>	
<b>Size:</b> A3	<b>BOM Doc No:</b>	<b>Document number</b> <b>BRD2201A</b>	<b>Revision</b> <b>A04</b>
<b>Design Created Date:</b> Wednesday, December 03, 2008		<b>Sheet Created Date</b> Saturday, March 21, 2009	<b>Sheet Modified Date</b> Wednesday, November 28, 2018
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**SILICON LABS**

**Schematic Title**

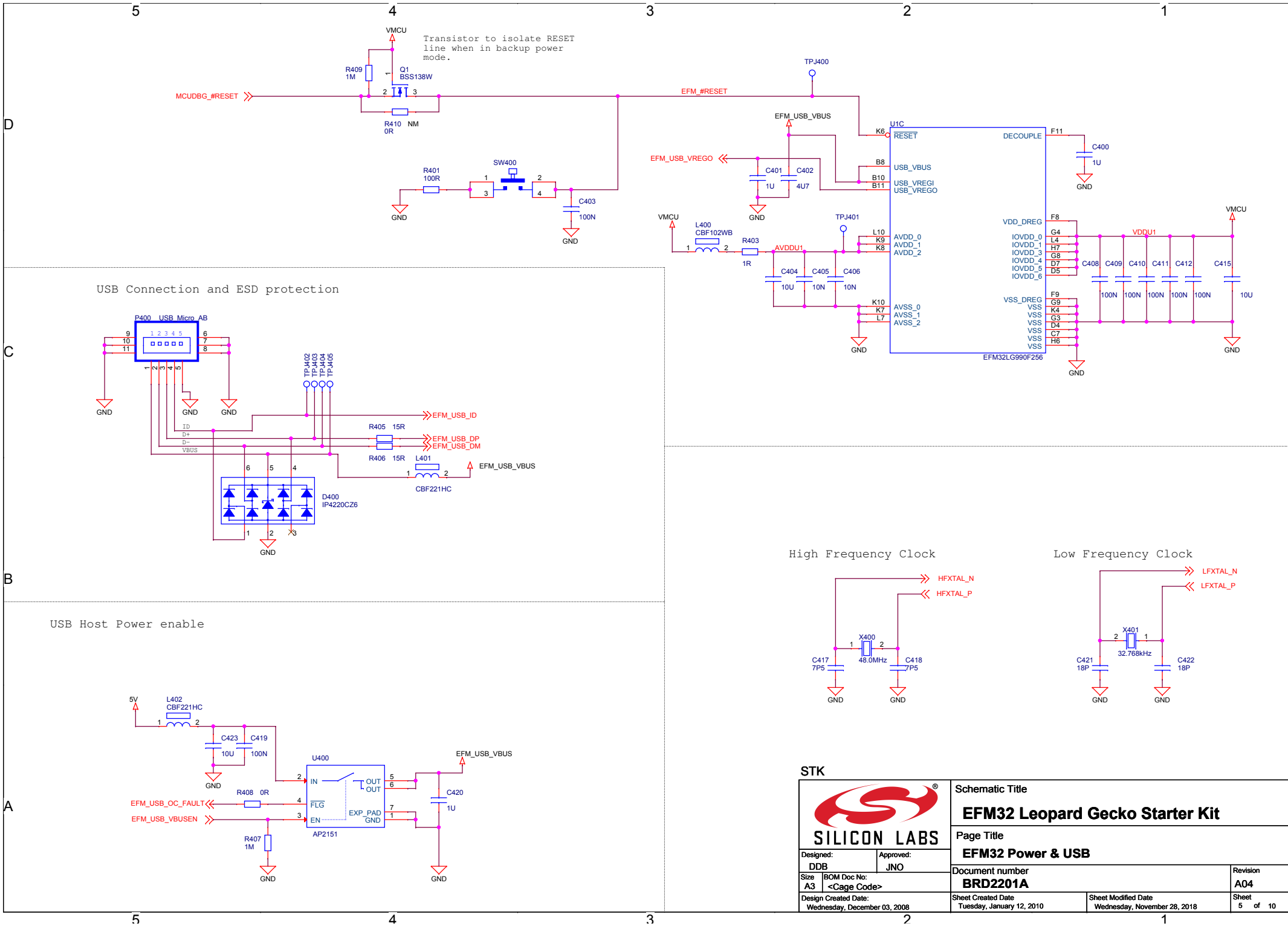
**EFM32 Leopard Gecko Starter Kit**

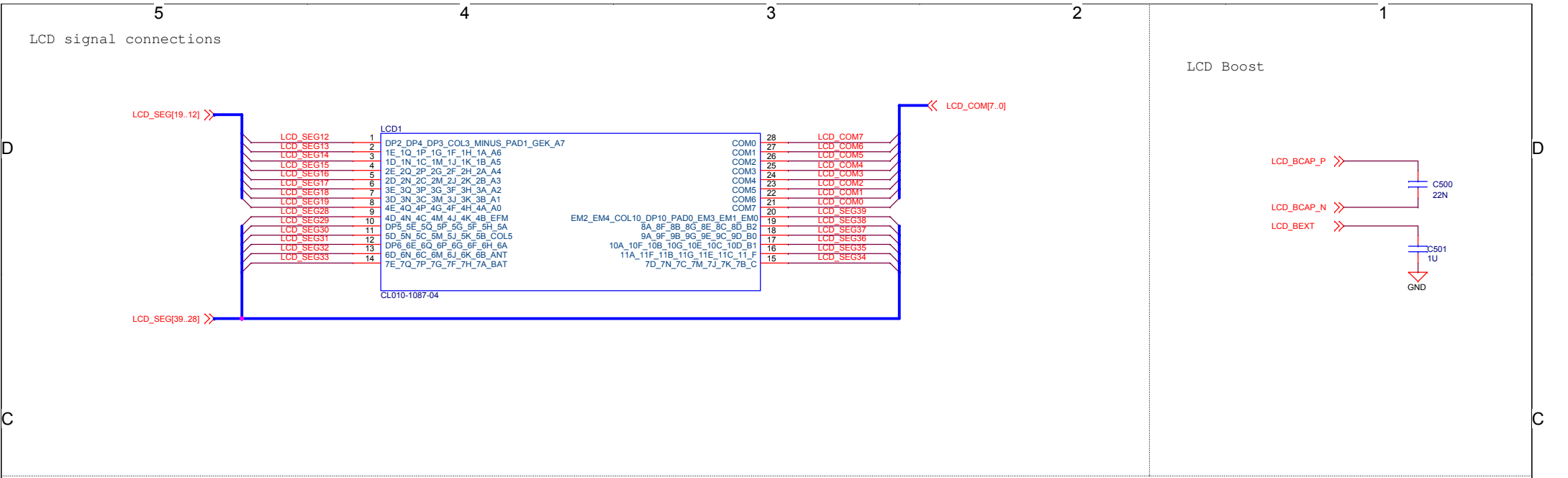
**Page Title**

**EFM32 I/O**

Designed: DDB	Approved: JNO
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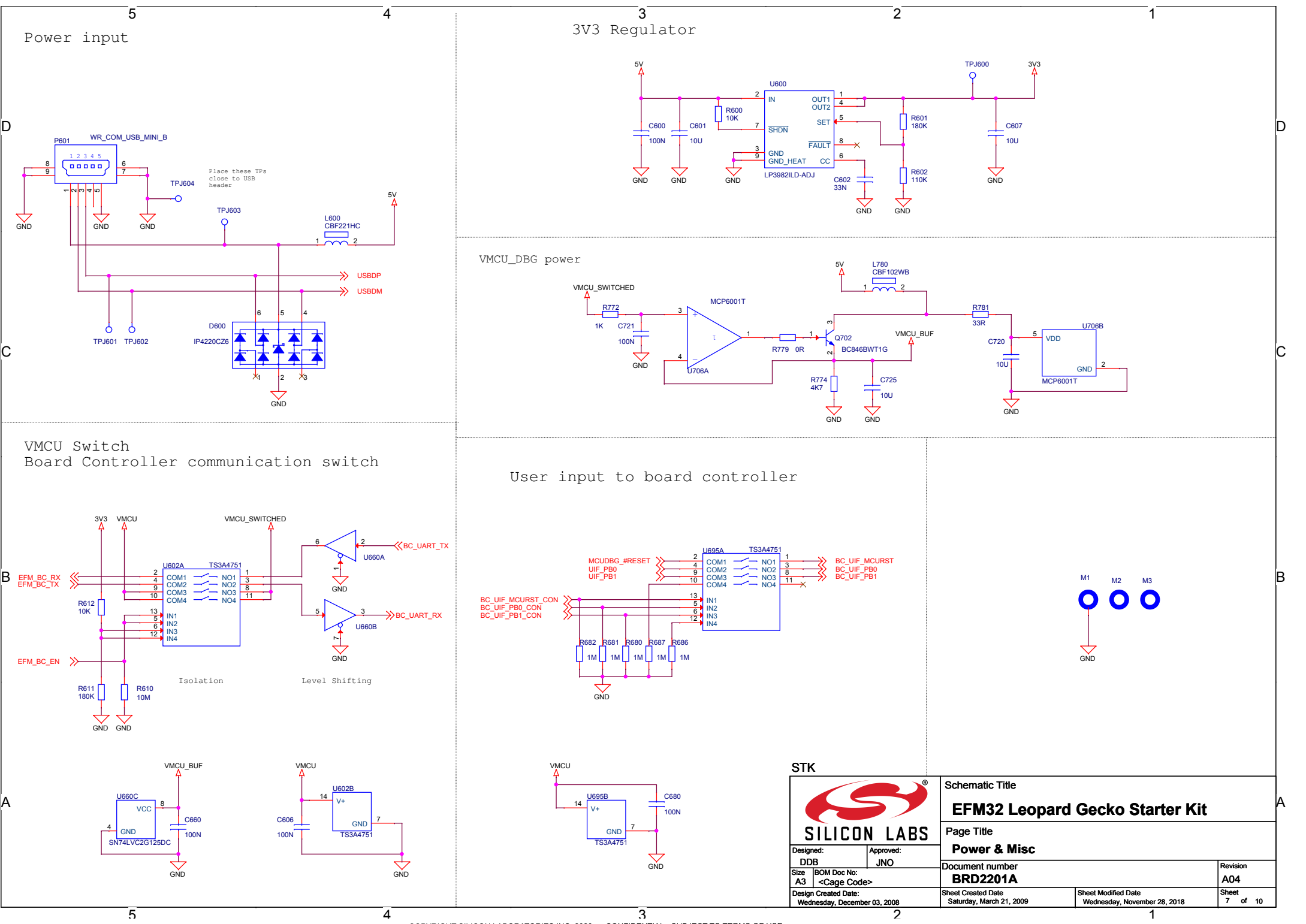


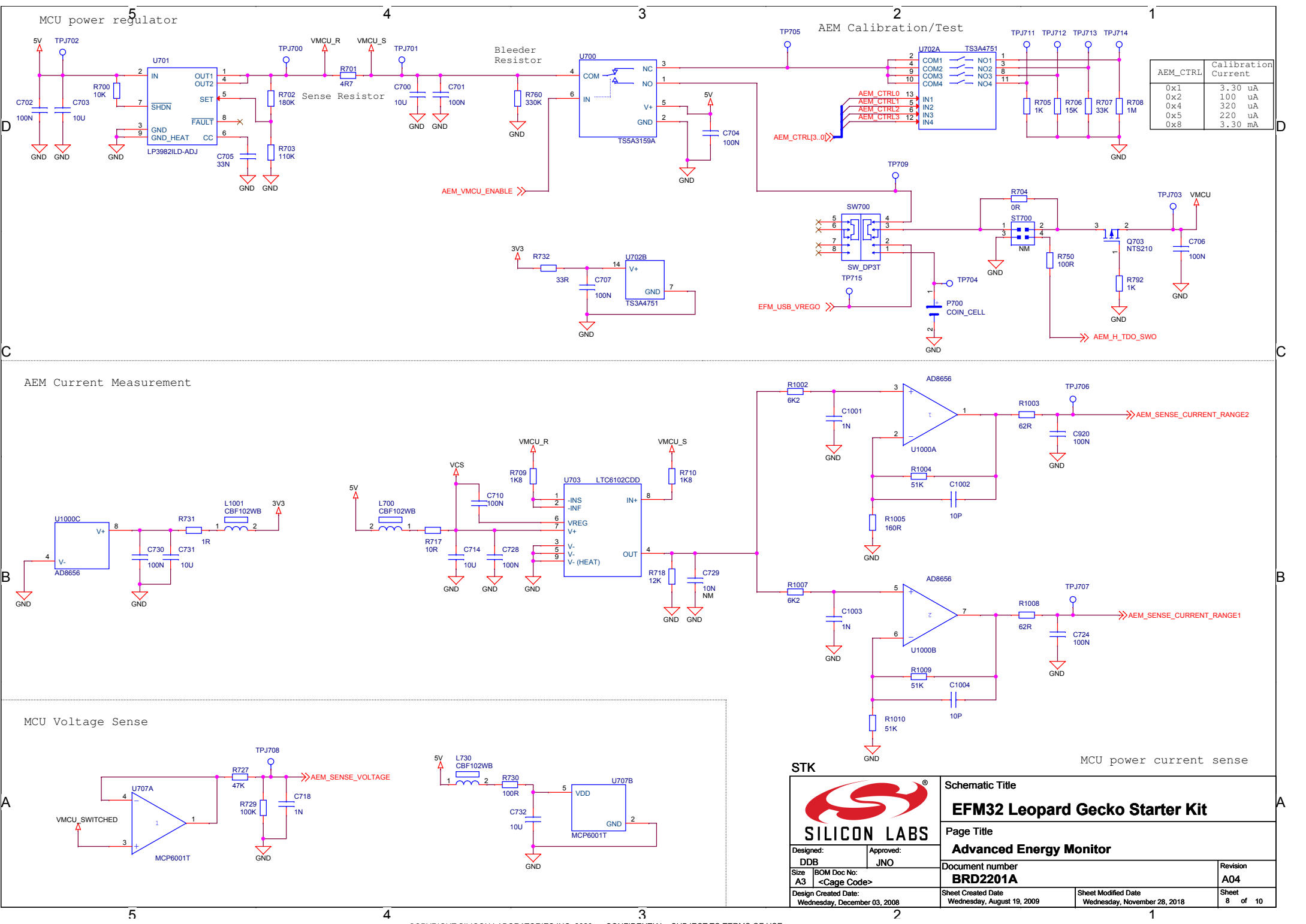


Segment names

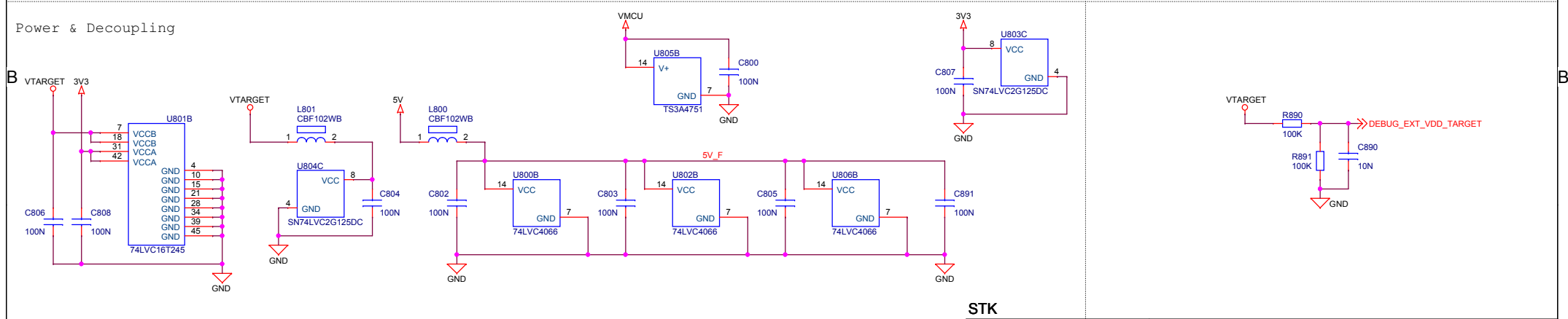
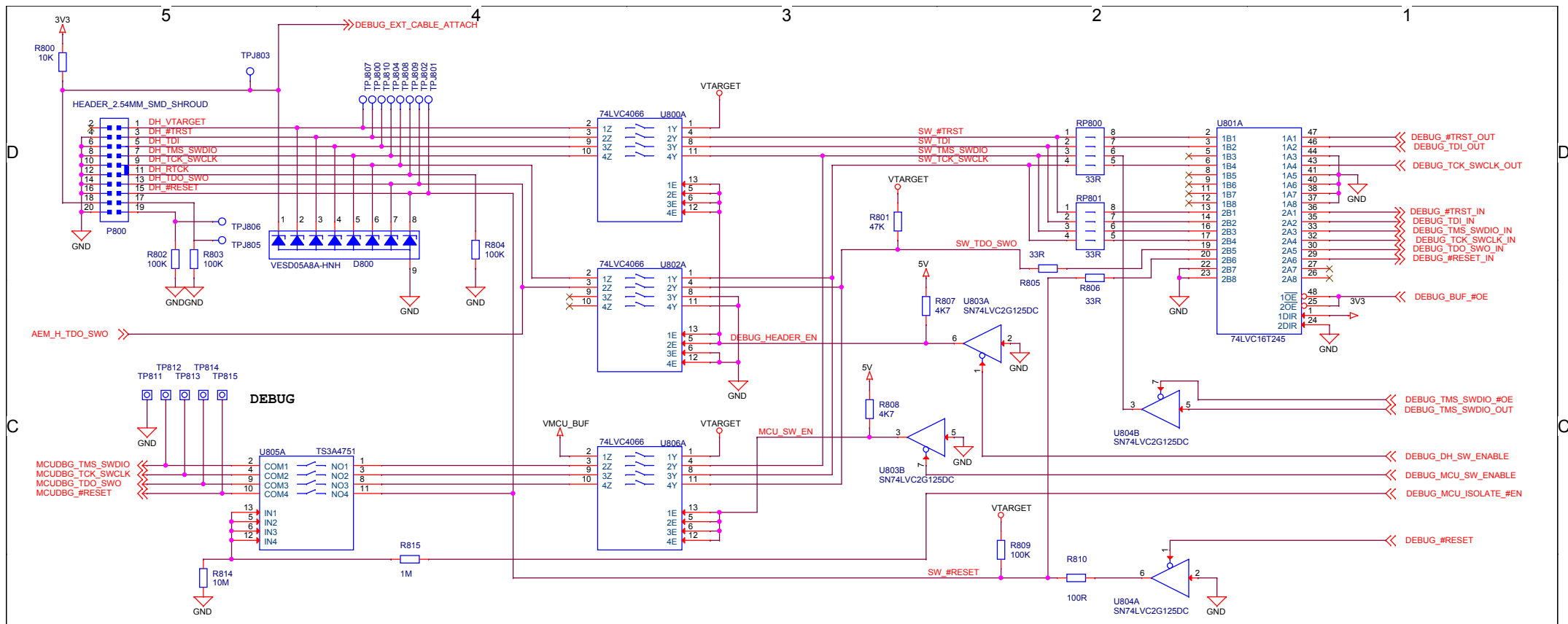
PIN	1	2	3	4	5	6	7	8	9	10	11	12	13	14
---	S0	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	S13
COM0	DP2	1 E	1 D	2 E	2 D	3 E	3 D	4 E	4 D	DP5	5 D	DP6	6 D	7 E
COM1	DP4	1 Q	1 N	2 Q	2 N	3 Q	3 N	4 Q	4 N	5 E	5 N	6 E	6 N	7 Q
COM2	DP3	1 P	1 C	2 P	2 C	3 P	3 C	4 P	4 C	5 Q	5 C	6 Q	6 C	7 P
COM3	COL3	1 G	1 M	2 G	2 M	3 G	3 M	4 G	4 M	5 P	5 M	6 P	6 M	7 G
COM4	MINUS	1 F	1 J	2 F	2 J	3 F	3 J	4 F	4 J	5 G	5 J	6 G	6 J	7 F
COM5	PAD1	1 H	1 K	2 H	2 K	3 H	3 K	4 H	4 K	5 F	5 K	6 F	6 K	7 H
COM6	GEK	1 A	1 B	2 A	2 B	3 A	3 B	4 A	4 B	5 H	5 B	6 H	6 B	7 A
COM7	A7	A6	A5	A4	A3	A2	A1	A0	EFM	5 A	COL5	6 A	ANT	BAT

Segment placement









Mode	DEBUG_MCU_SW_ENABLE	DEBUG_DH_SW_ENABLE	DEBUG_BUF_#OE	ISOLATE_#EN	DH_VTARGET	VTARGET
Debug Out	0	1	0	0	External voltage	External voltage
MCU Debug	1	0	0	1	Disconnected	VMCU
Debug In	1	1	1	1	VMCU	VMCU
Debug Off	1	1	1	0	-	-

**SILICON LABS**

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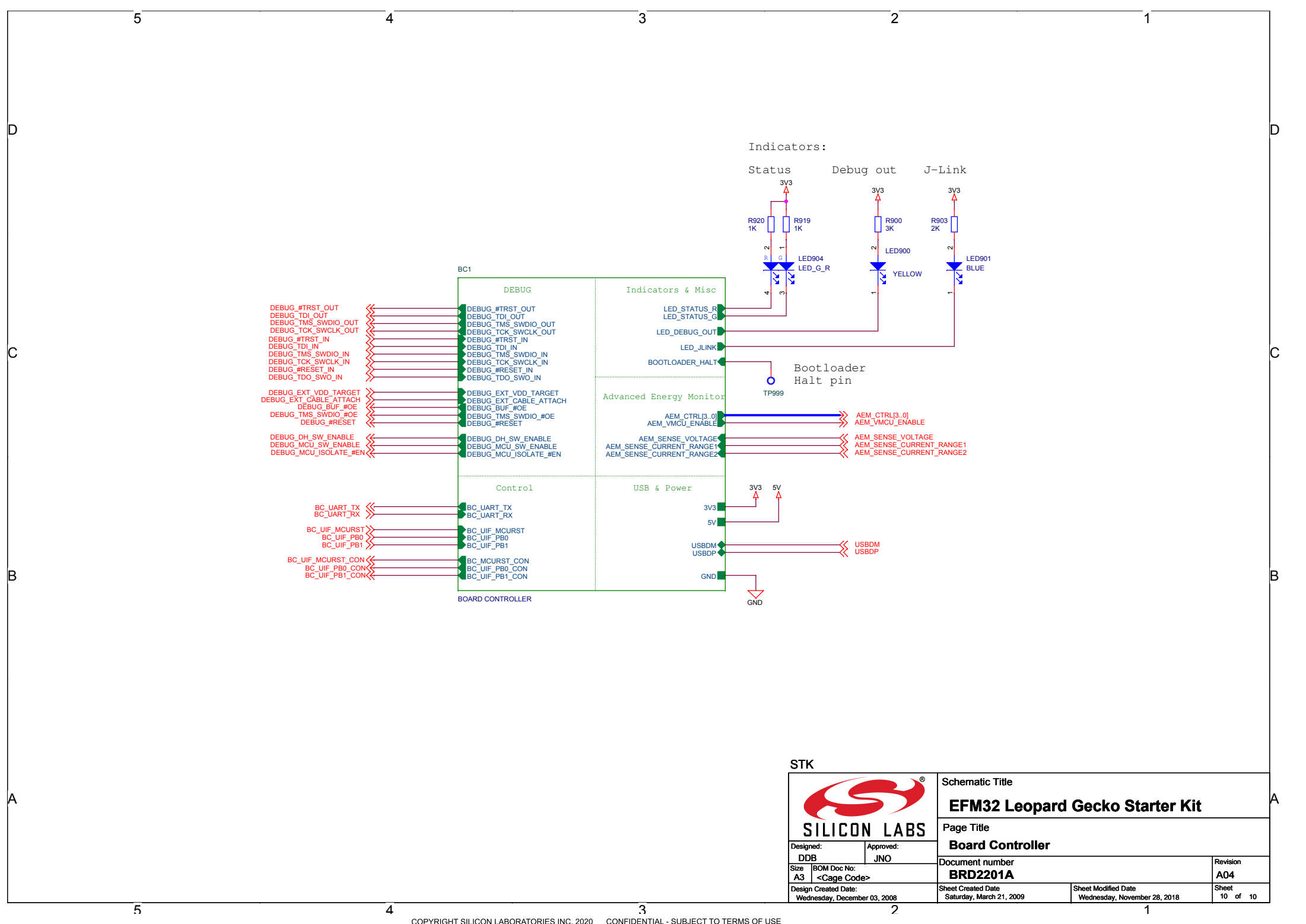
**Schematic Title**  
**EFM32 Leopard Gecko Starter Kit**


**Page Title**  
**Debug Interface**

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**BRD2201A**

**Revision**  
**A04**

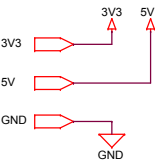
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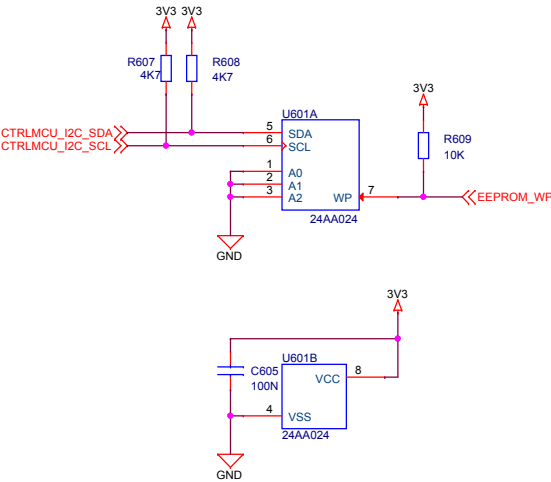
<div>STK</div> <div><div>SILICON LABS</div></div>	Schematic Title		
	EFM32 Leopard Gecko Starter Kit		
Designed: DDB		Approved: JNO	
Size: A3		BOM Doc No: <Cage Code>	
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		BRD2201A	
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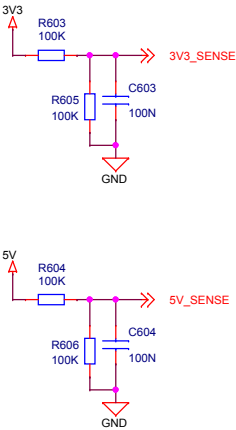
POWER INPUTS



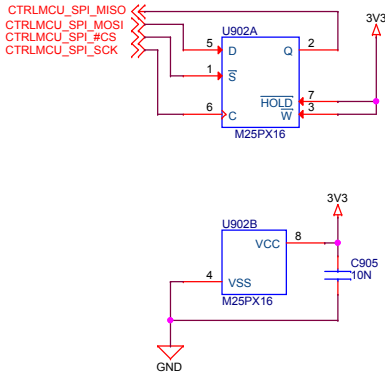
CTRLMCU EEPROM



POWER SENSE



CTRLMCU SERIAL FLASH



<Schematic Path>  
BOARD CONTROLLER

 <b>SILICON LABS</b>		Schematic Title	
		<b>EFM32 Leopard Gecko Starter Kit</b>	
Designed: DDB		Page Title	
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