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Revision History	
Rev.	Description
B00	Initial release for series production.
C01	Updated EFM32ZG chip revision + major updates to debugger

EFM32 ZG STK

		Schematic Title	
EFM32ZG Zero Gecko STK		Page Title	
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5V	5V
GND	GND
F0	D4
F1	D5
F2	D6
F3	D7
F4	NC
F5	E10
NC	E11
NC	E12
RST	E13
NC	NC
NC	NC
NC	NC
GND	GND
3V3	3V3



B



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3



D

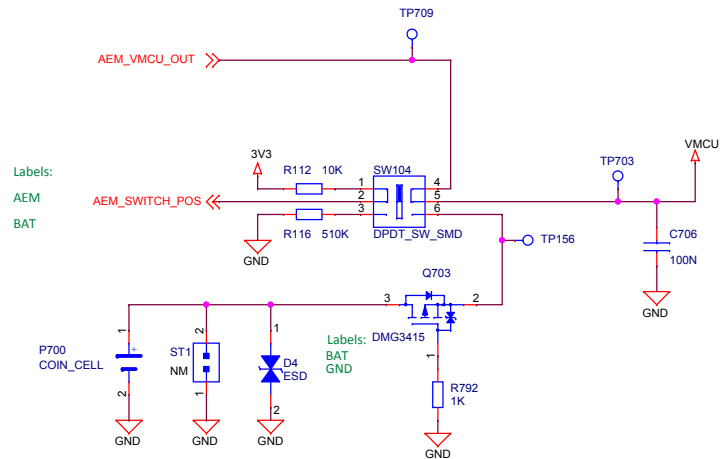


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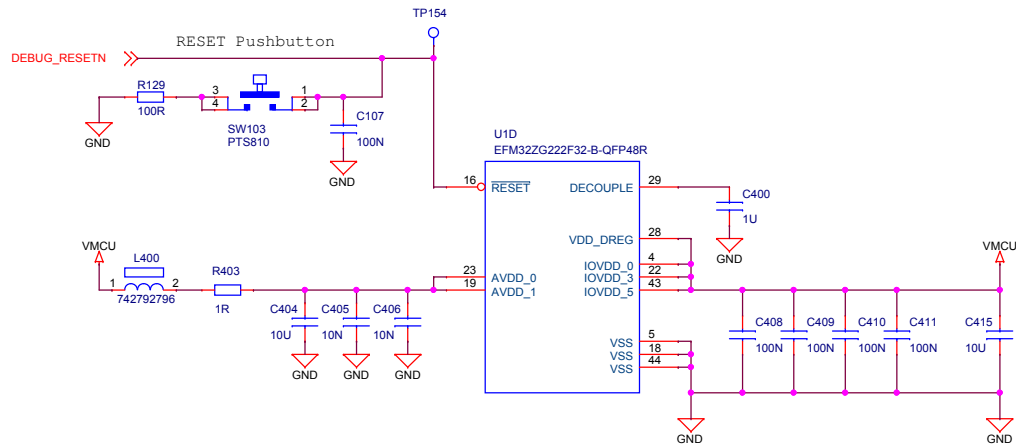
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Power Select Switch: AEM/BAT

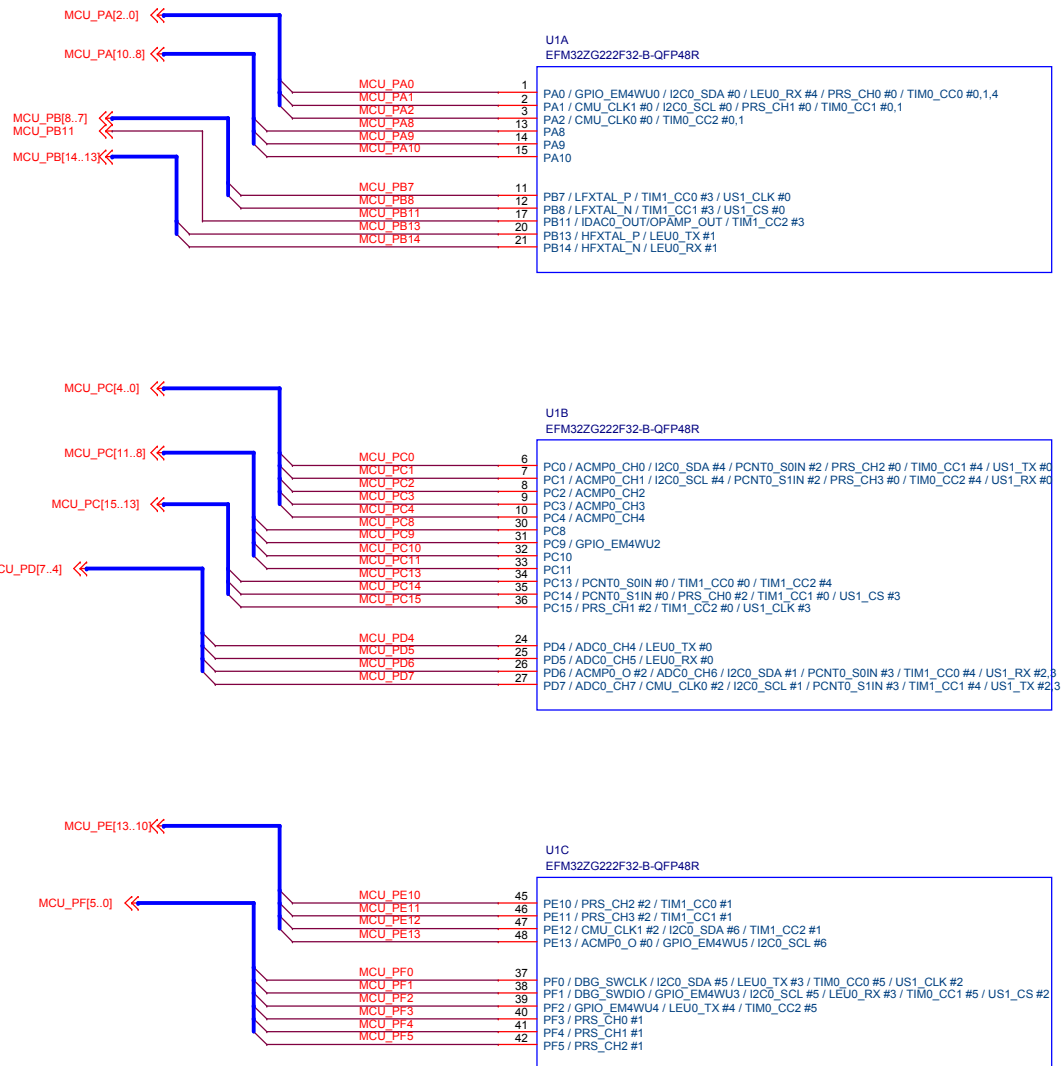


SWITCH POS	MODE DESCRIPTION
AEM	AEM Enabled, VMCU sourced from external 3.3v LDO powered by BC USB 5V supply
BAT	AEM Disabled, VMCU sourced from coin-cell battery or external power supply

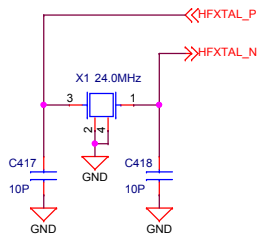
EFM32 Power and Decoupling



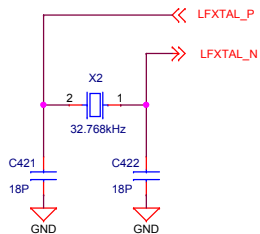
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Designed: HEL		Page Title	
Approved: JNO		EFM32 Power	
Size: A3	BOM Doc No: <Cage Code>	Document number	Revision
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High Frequency Clock



Low Frequency Clock



Designed:
HEL

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EFM32 I/O

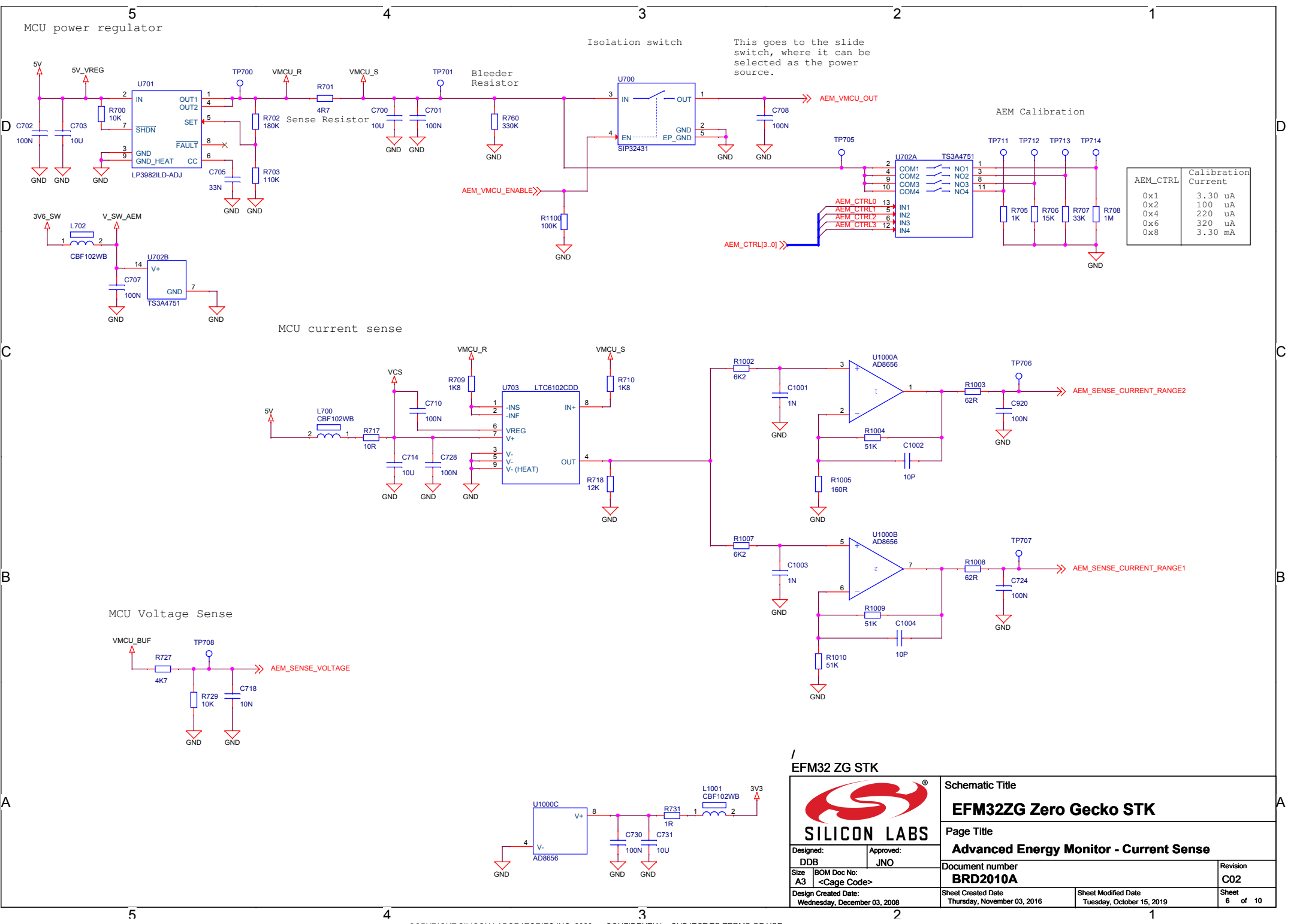
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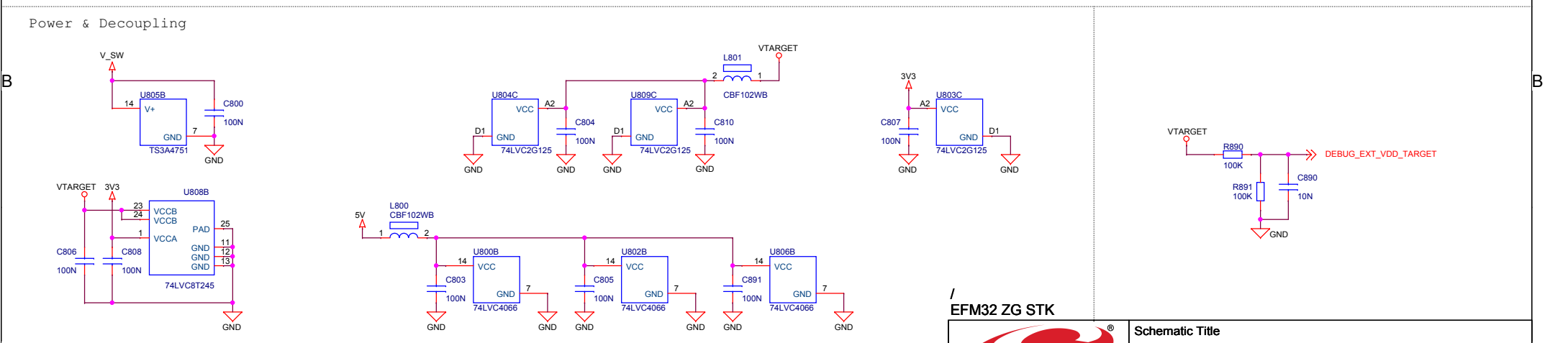
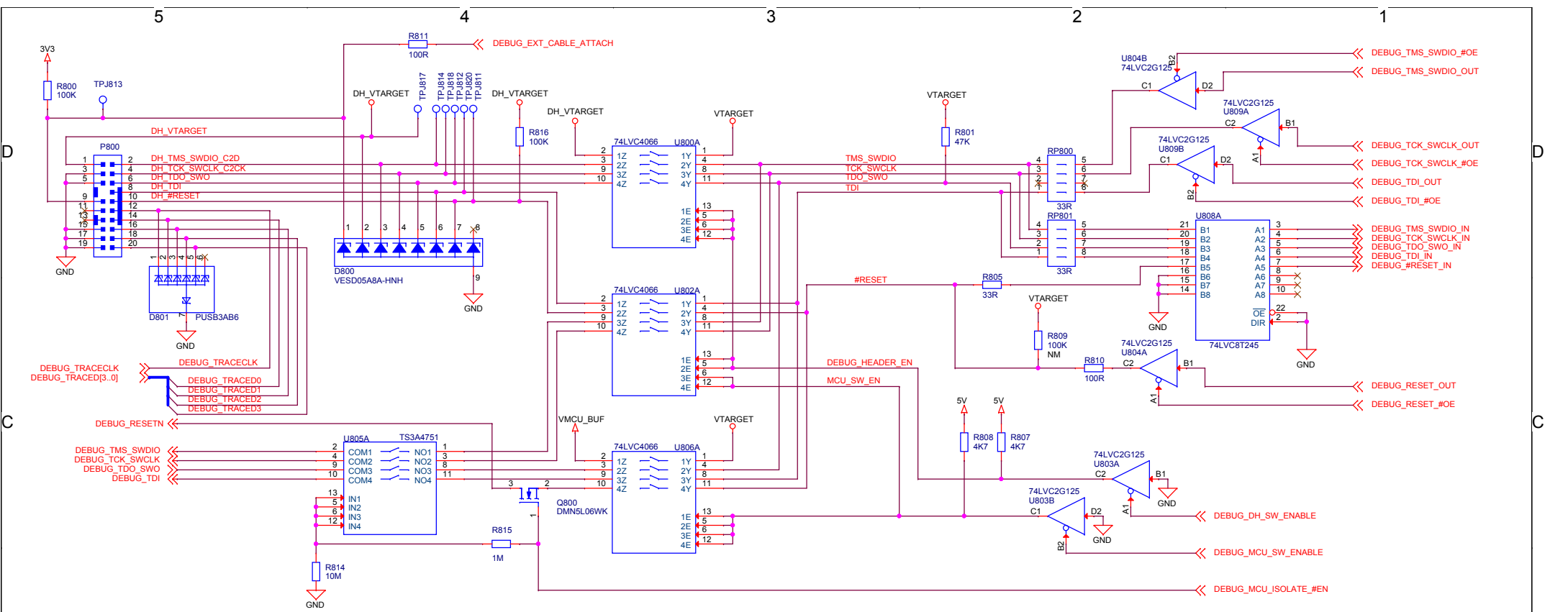
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EFM32ZG Zero Gecko STK

Advanced Energy Monitor - Current Sense



Mode	DEBUG_MCU_SW_ENABLE	DEBUG_DH_SW_ENABLE	DEBUG_BUF_#OE	ISOLATE_#EN	DH_VTARGET	VTARGET
Debug Out	0	1	0	0	External voltage	External voltage
MCU Debug	1	0	0	1	Disconnected	VMCU
Debug In	1	1	1	1	VMCU	VMCU
Debug Off	1	1	1	0	-	-



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Debug Interface

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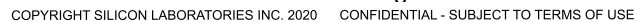
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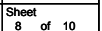
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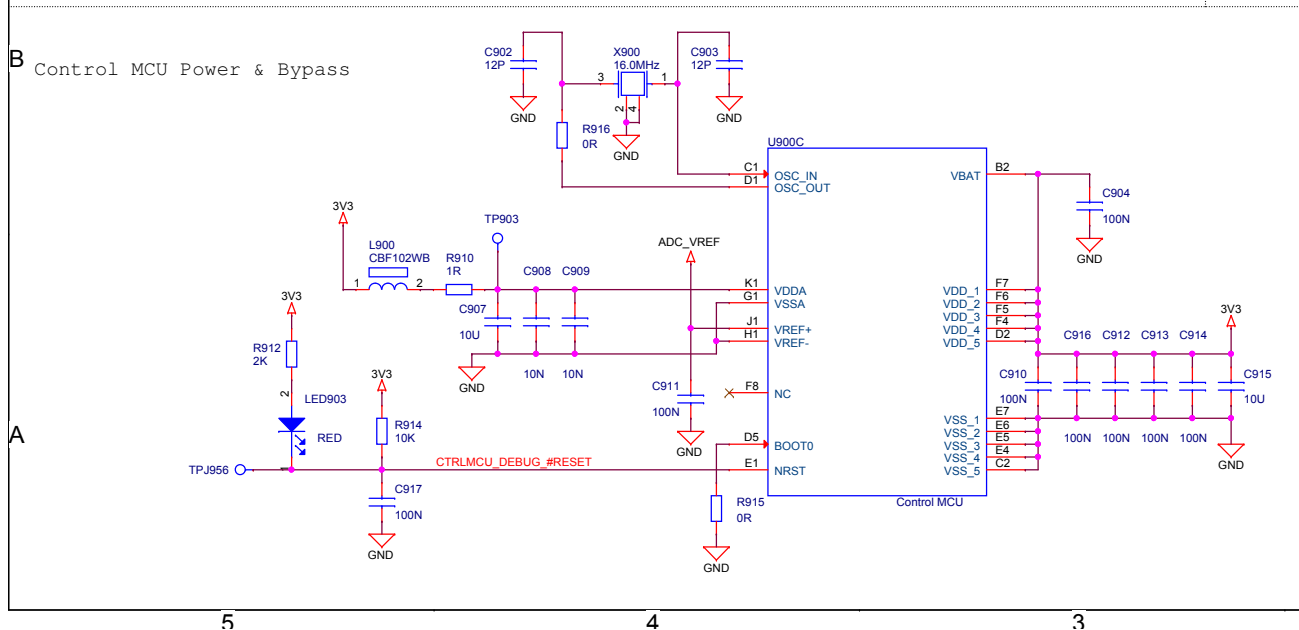
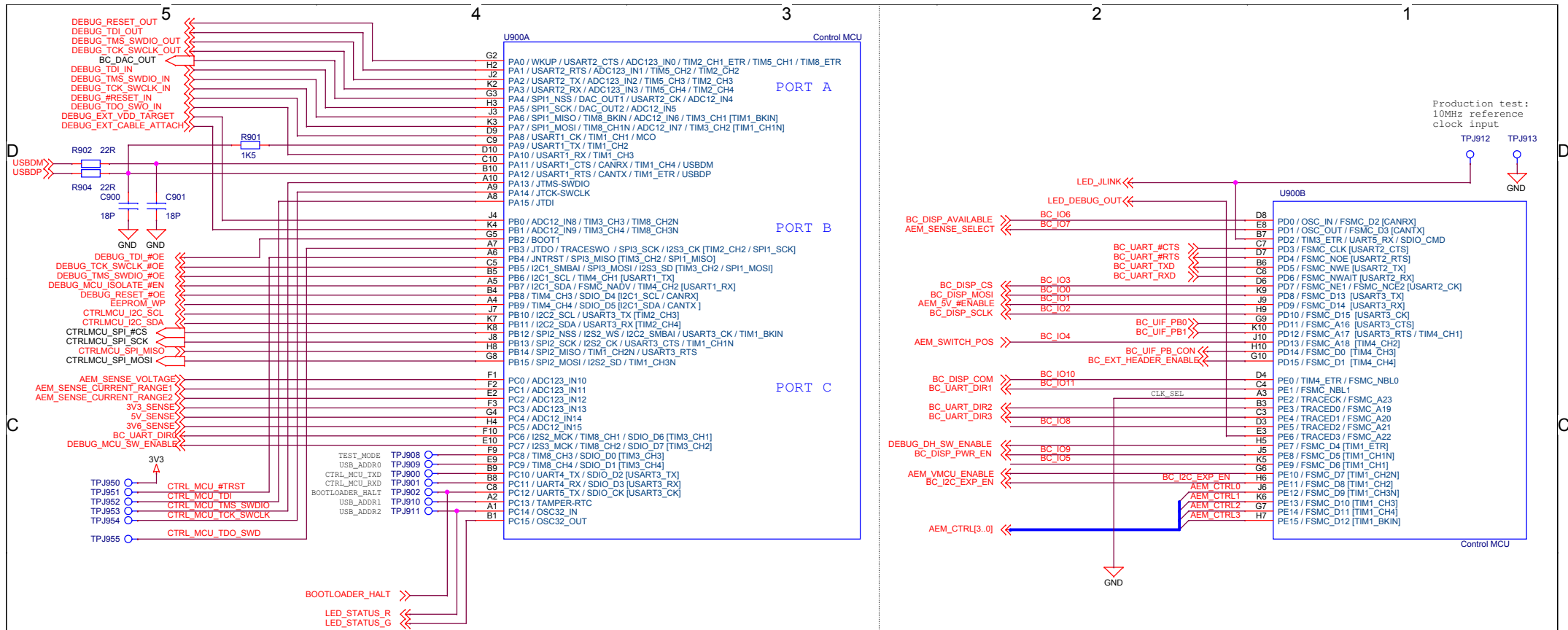
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A



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POWER SENSE



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