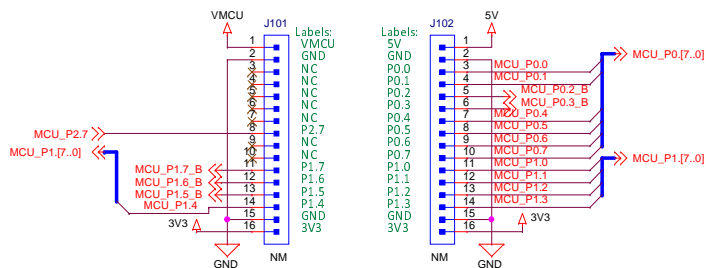
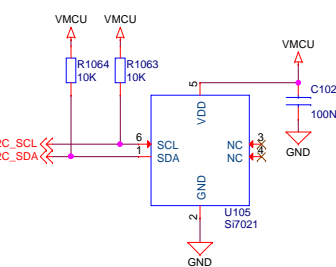


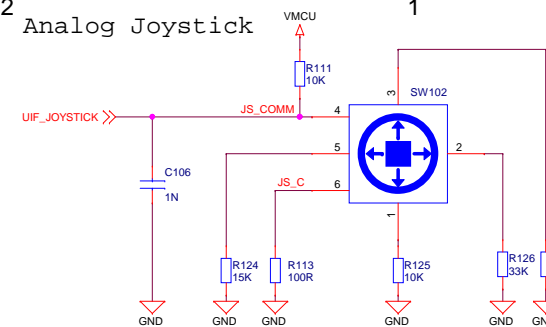
Breakout Connections



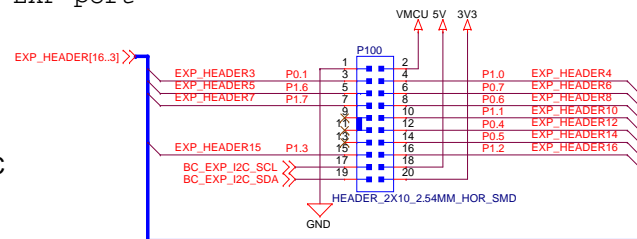
Temperature & Relative Humidity Sensor



Analog Joystick



EXP port



EXP-Header Functionality

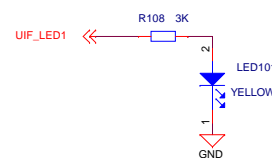
** Indicates potential STK hardware conflicts

1	GND		ADC0.1
3	P0.1		
5	P1.6		
7	P1.7		
9			
11			
13	P1.3	I2C0_SCL	ADC0.11
15	P1.3	I2C0_SCL	ADC0.11
17			Reserved for EXP Board Identification
19			Reserved for EXP Board Identification

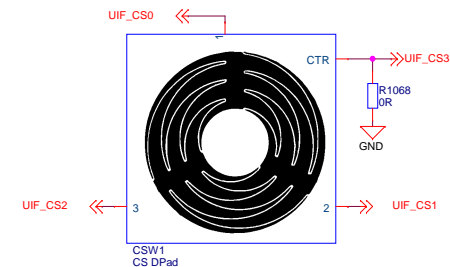
2	VMCU		
4	P1.0	SPI0_MOSI	CMP0P
6	P0.7	SPI0_MISO	ADC0.7
8	P0.6	SPI0_SCK	ADC0.6
10	P1.1	SPI0_NSS	CMP0N
12	P0.4	UART0_TX	ADC0.4
14	P0.5	UART0_RX	ADC0.5
16	P1.2	I2C0_SDA	ADC0.10
18	5V		
20	3V3		

** STK LED0

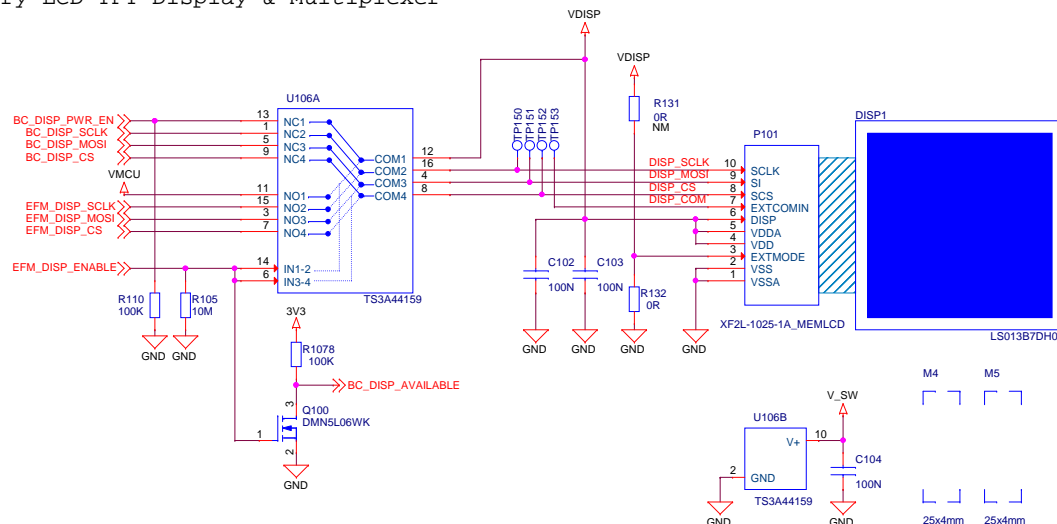
LED



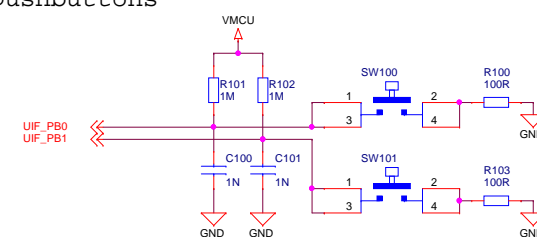
Capsense Rotor



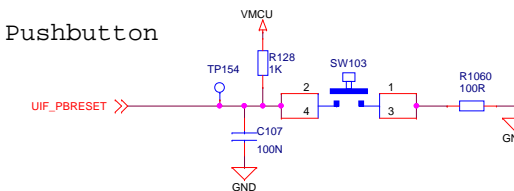
Memory LCD-TFT Display & Multiplexer



User pushbuttons



RESET Pushbutton

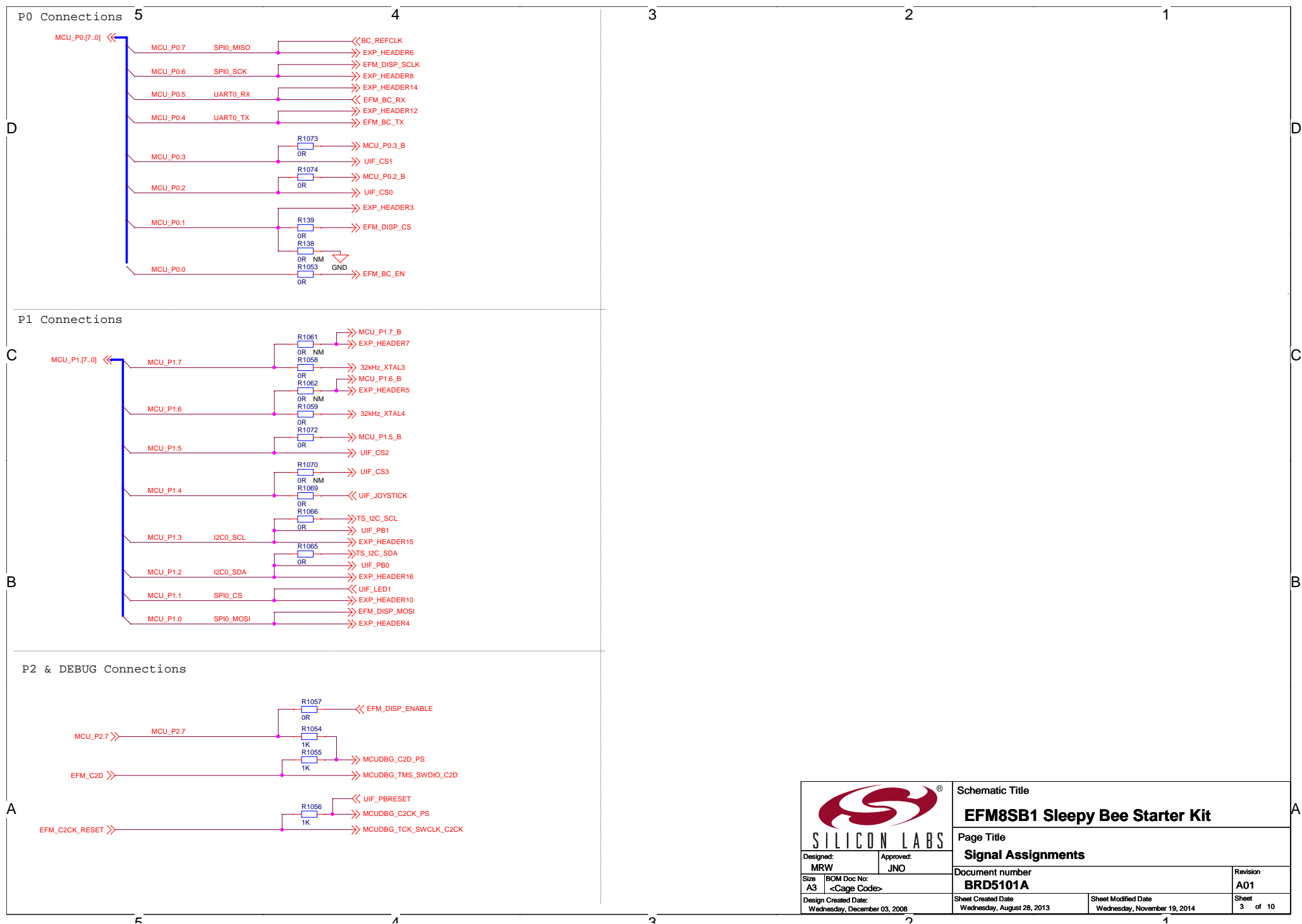



STK

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		EFM8SB1 Sleepy Bee Starter Kit	
Designed: MRW Size: A3 Design Created Date: Wednesday, December 03, 2008		Page Title	
		User Interfaces	
BOM Doc No: Sheet Created Date: Wednesday, August 28, 2013		Document number	
		BRD5101A	
Sheet Modified Date: Friday, November 21, 2014		Revision	
		A01	
Sheet 2 of 10			

A The EFM8 always controls ownership of the display using the EFM_DISP_ENABLE signal.

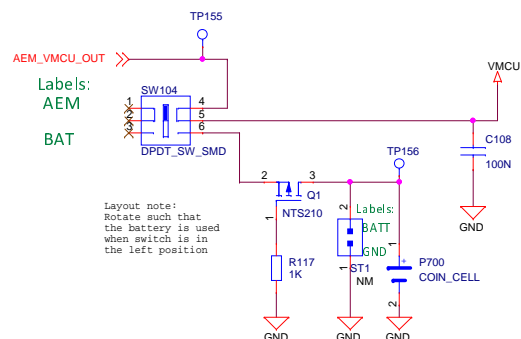
EFM_DISP_ENABLE	DISP_CTRL	VDISP
0	BC	BC_DISP_PWR_EN
1	EFM	VMCU



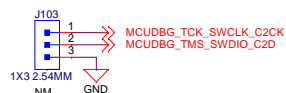
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Size A3		Document number	
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Design Created Date: Wednesday, December 03, 2008		Sheet Created Date: Wednesday, August 28, 2013	Revision A01
		Sheet Modified Date: Wednesday, November 19, 2014	Sheet 3 of 10

Power Switch

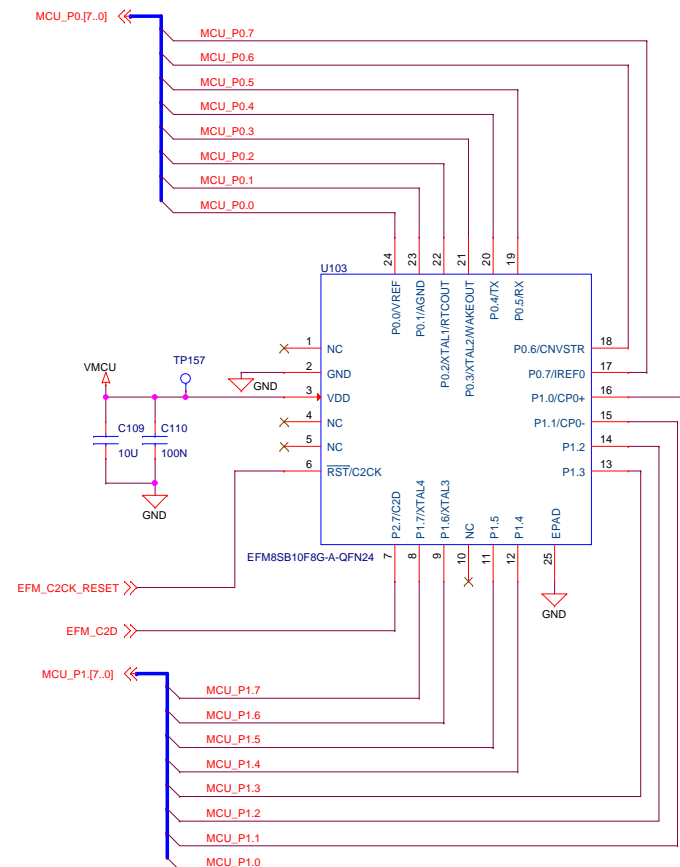
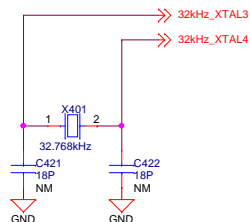
SWITCH POS	MODE DESCRIPTION
AEM	AEM Enabled, VMCU sourced from external 3.3V LDO powered by BC USB 5V supply
BAT	AEM Disabled, VMCU sourced from external coin-cell battery or external power supply




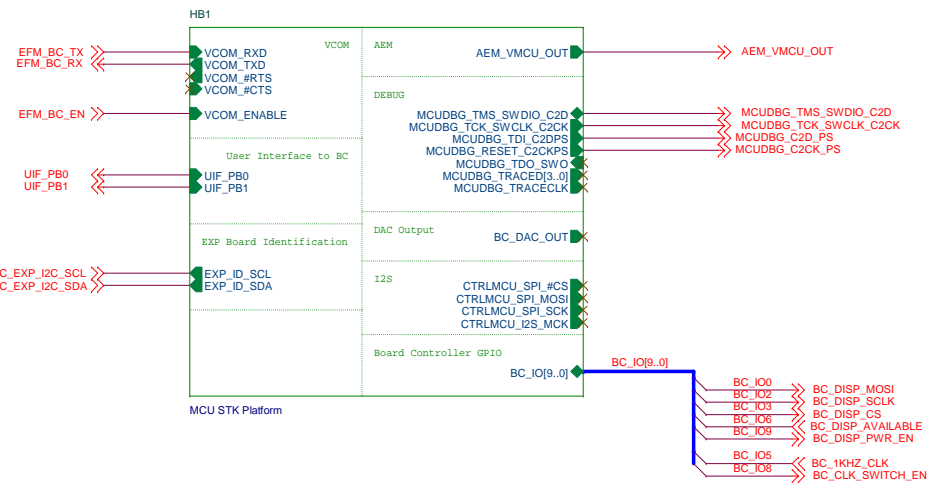
C2 Debug Header



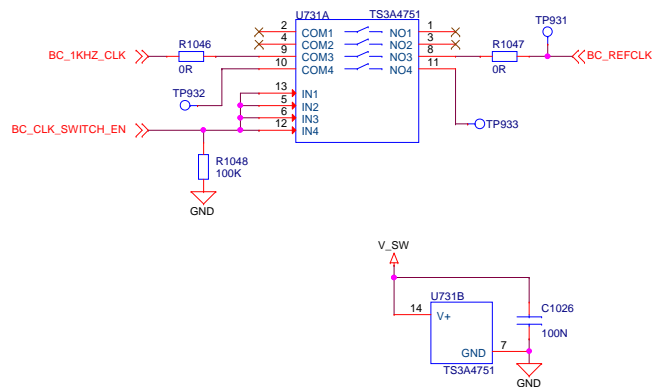
Low Frequency Clock




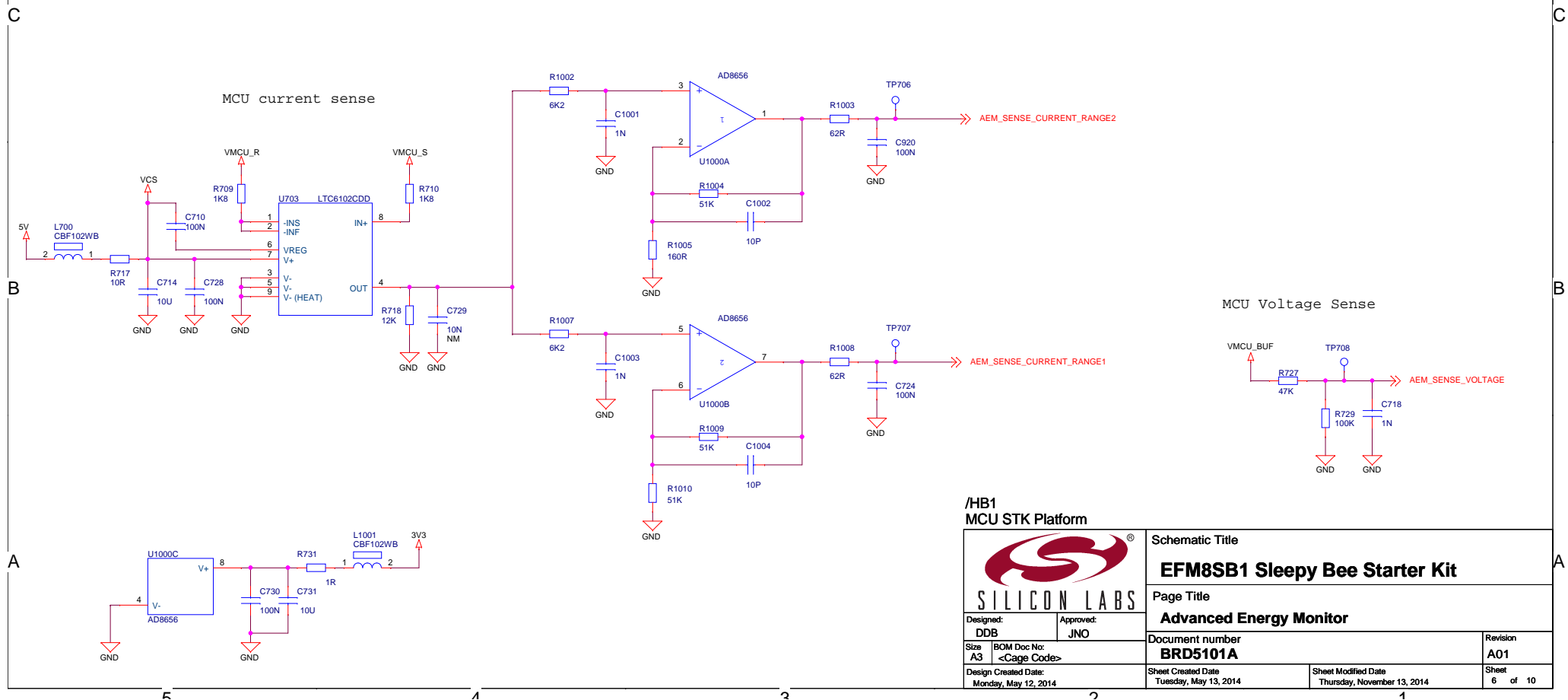
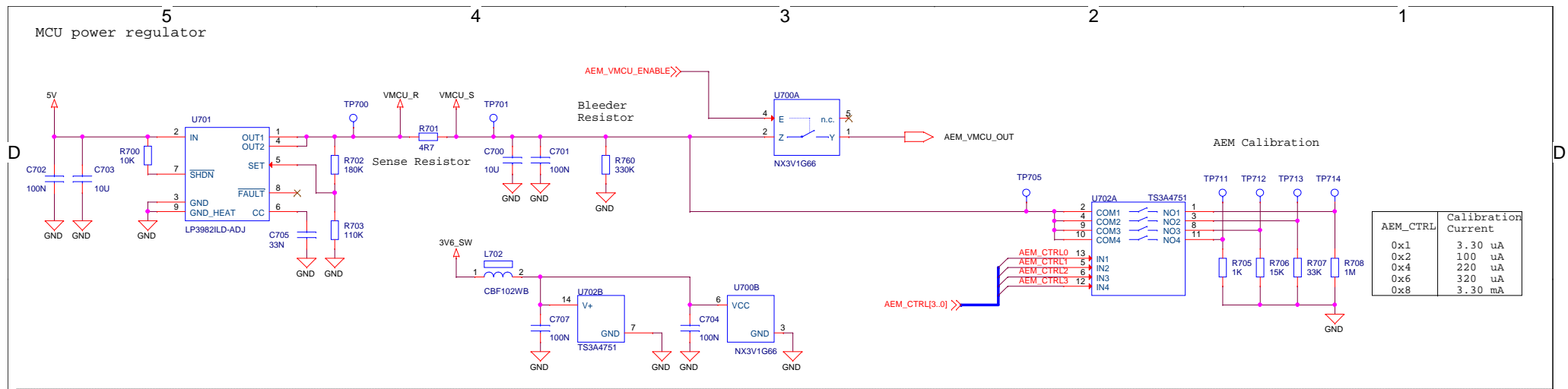
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Designed: MRW		Page Title	
Approved: JNO		EFM8 I/O	
Size A3		Document number	
BOM Doc No: <Cage Code>		BRD5101A	
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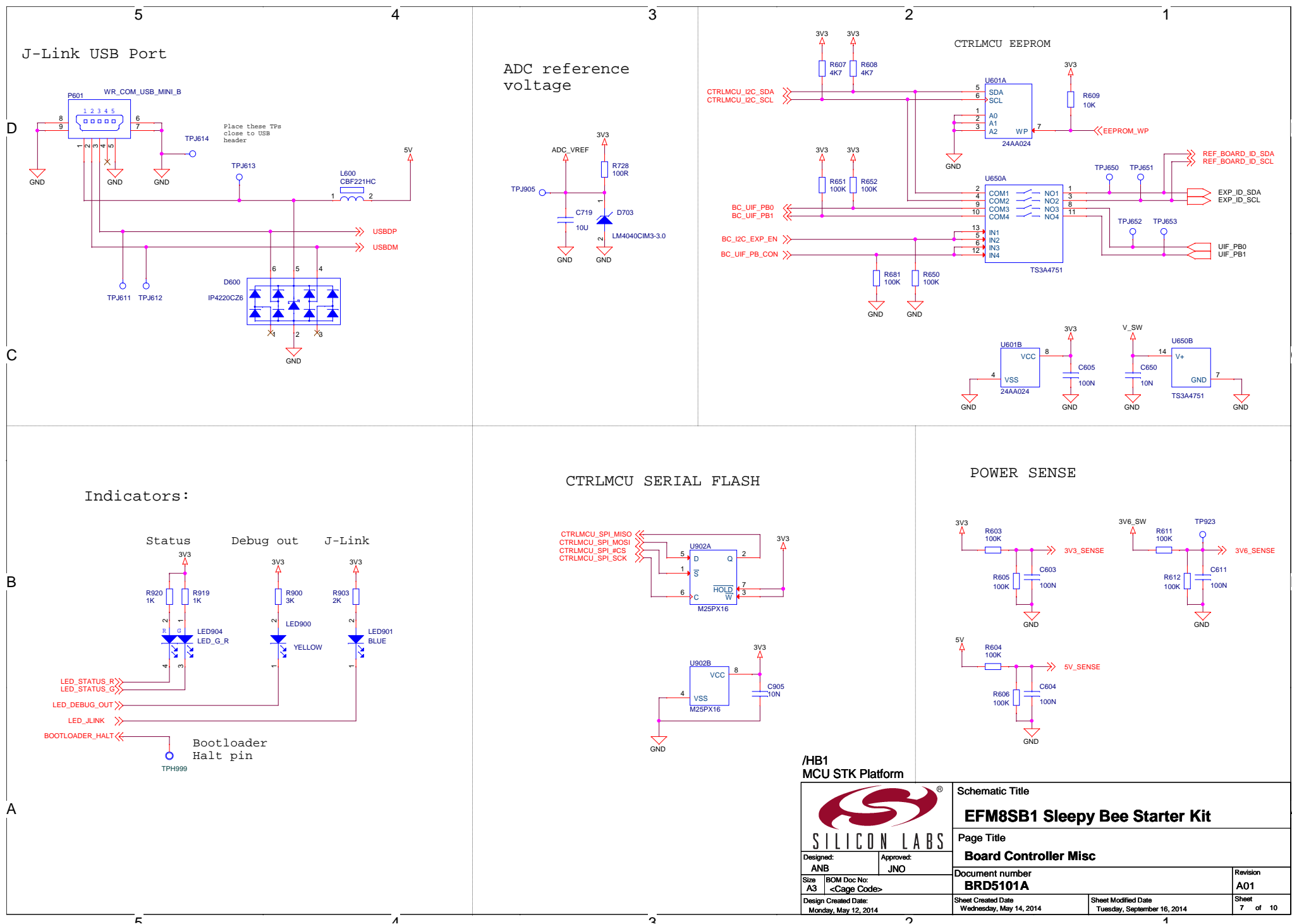


Analog Switch




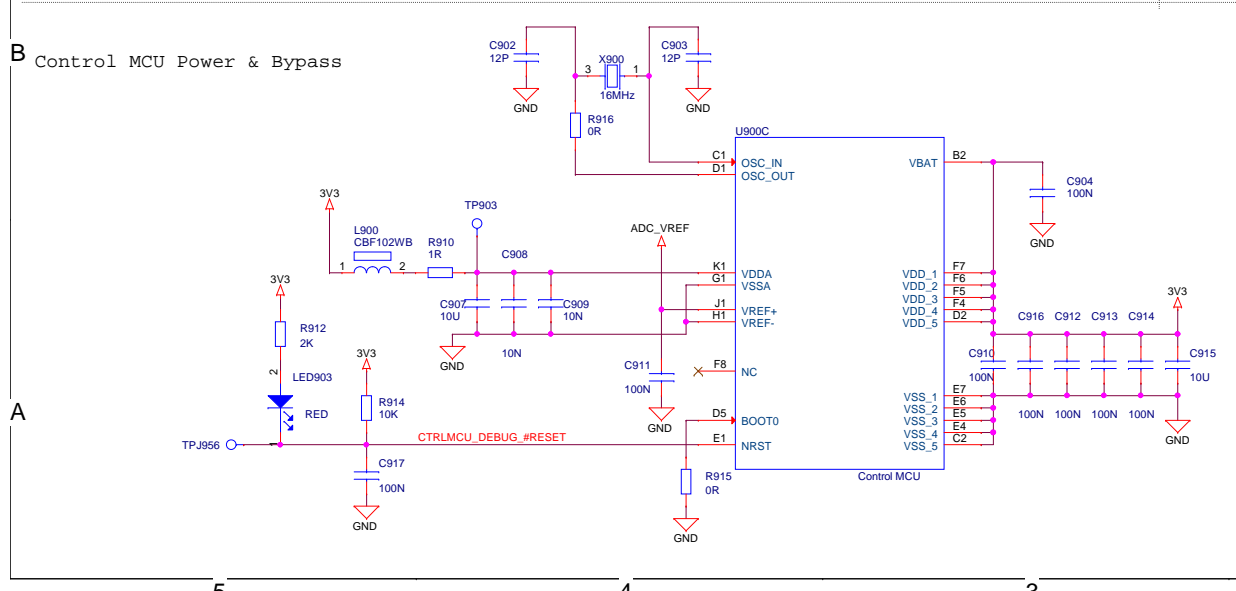
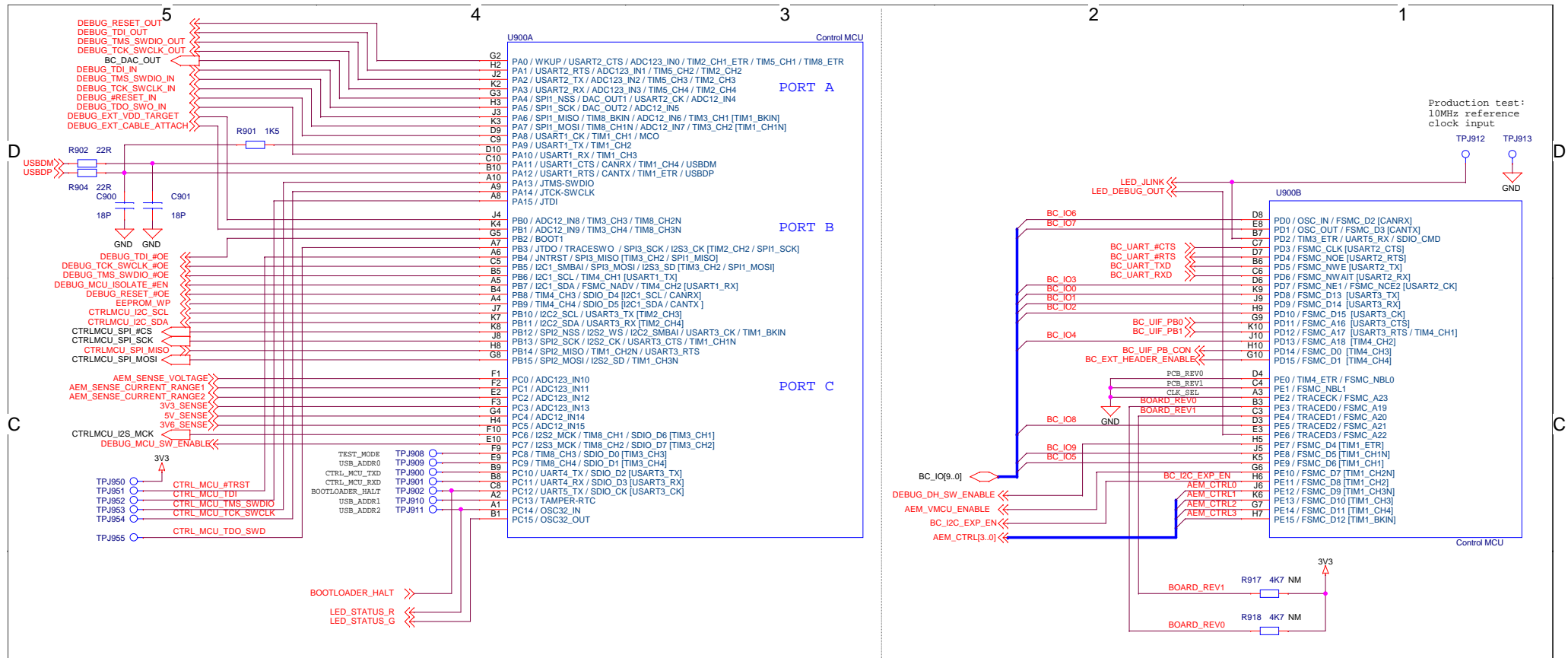
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Design Created Date:		Revision	
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Sheet Created Date		Sheet Modified Date	
Thursday, May 15, 2014		Friday, November 21, 2014	
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5		of 10	





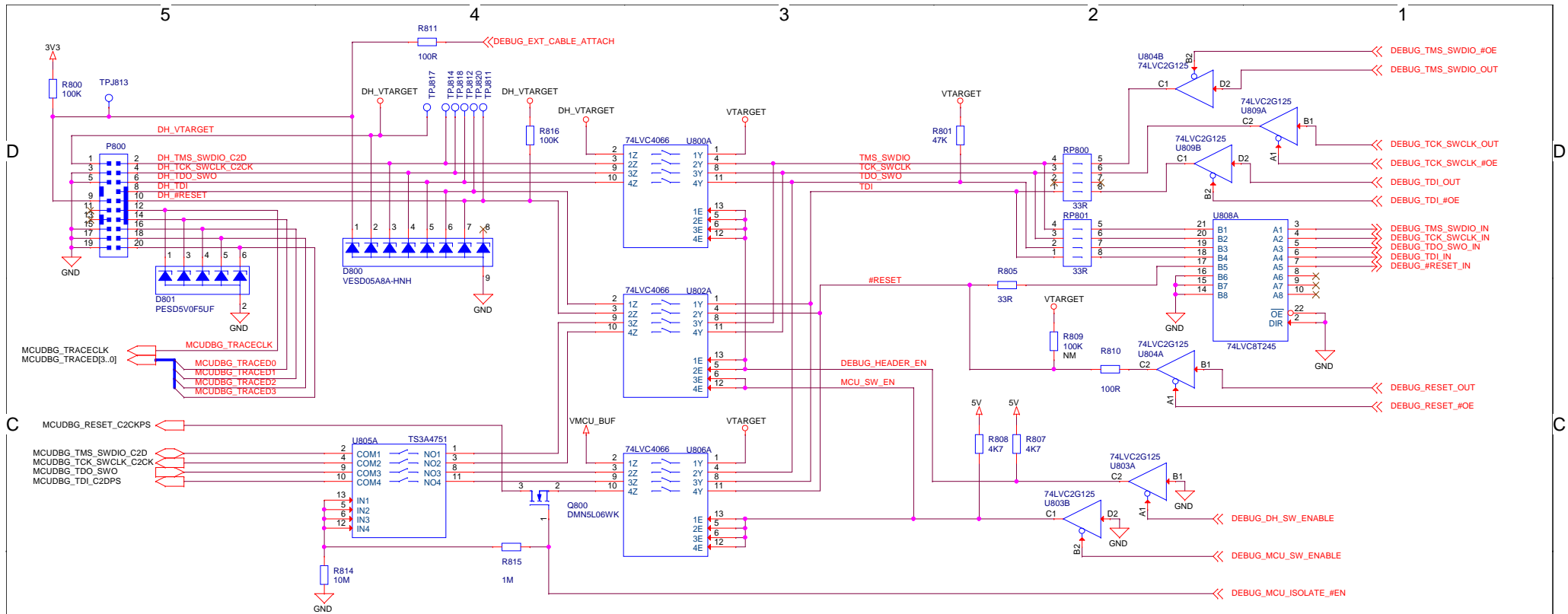
/HB1
MCU STK Platform

 SILICON LABS		Schematic Title	
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Approved: JNO		Board Controller Misc	
Size A3		Document number	
BOM Doc No: <Cage Code>		BRD5101A	
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		Sheet Modified Date Tuesday, September 16, 2014	Sheet 7 of 10

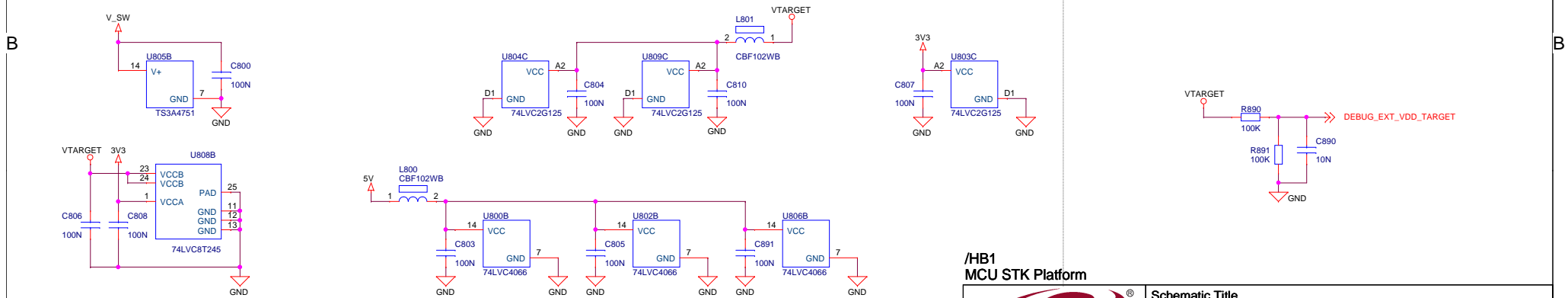


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MCU STK Platform

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BOM Doc No: <Cage Code>		Document number	
		BRD5101A	
Sheet Created Date: Wednesday, May 14, 2014		Sheet Modified Date: Monday, October 20, 2014	Revision: A01 Sheet 8 of 10



Power & Decoupling



Mode	DEBUG_MCU_SW_ENABLE	DEBUG_DH_SW_ENABLE	DEBUG_BUF_#OE	ISOLATE_#EN	DH_VTARGET	VTARGET
Debug Out	0	1	0	0	External voltage	External voltage
MCU Debug	1	0	0	1	Disconnected	VMCU
Debug In	1	1	1	1	VMCU	VMCU
Debug Off	1	1	1	0	-	-

/HB1
MCU STK Platform

		Schematic Title	
		EFM8SB1 Sleepy Bee Starter Kit	
Designed: DDB		Page Title	
Approved: JNO		Debug Interface	
Size A3	BOM Doc No: <Cage Code>	Document number	
Design Created Date: Monday, May 12, 2014		BRD5101A	
Sheet Created Date: Monday, May 12, 2014		Sheet Modified Date: Wednesday, October 22, 2014	Revision A01
Sheet 9 of 10			

