




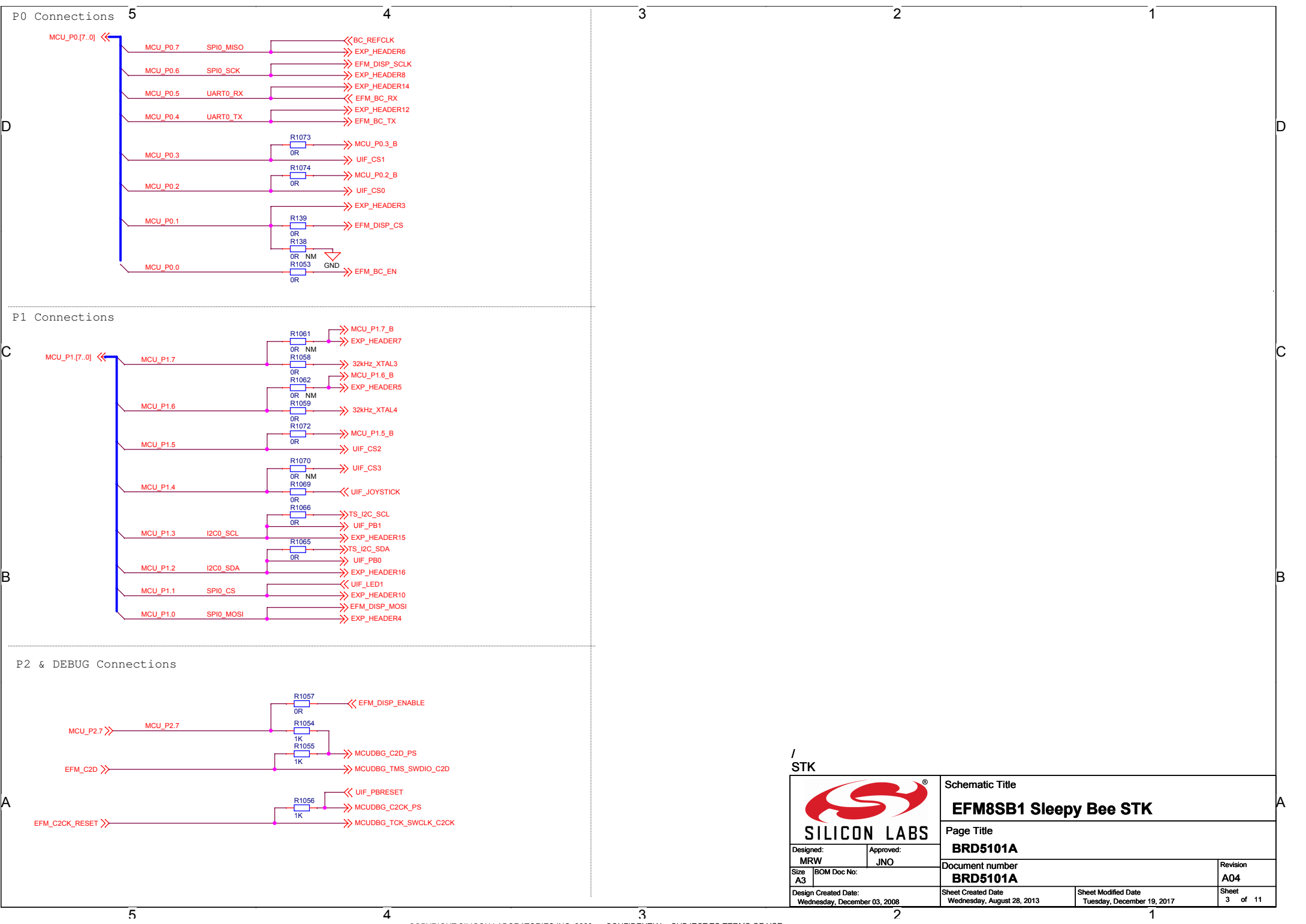
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Revision History	
Rev.	Description
A00	Initial version released for production.
A01	Minor updates to design after prototype series. Updated PCB.
A02	Updated PCB revision.
A03	Un-mounted D801.
A04	Changed joystick to L-KLS7-MT-03

 SILICON LABS		Schematic Title	
		EFM8SB1 Sleepy Bee STK	
Designed: MRW		Approved: JNO	
Size: A3		BOM Doc No:	
Design Created Date: Wednesday, December 03, 2008		Sheet Created Date: Thursday, May 15, 2014	
		Sheet Modified Date: Wednesday, October 30, 2019	
		Revision: A04	
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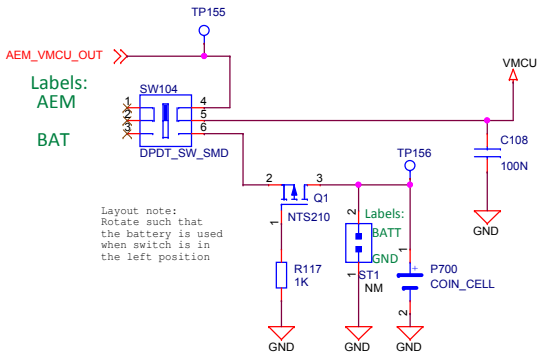


/ STK

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		EFM8SB1 Sleepy Bee STK	
Designed: MRW		Approved: JNO	
Size A3		Document number	
BOM Doc No:		BRD5101A	
Design Created Date: Wednesday, December 03, 2008		Sheet Created Date: Wednesday, August 28, 2013	Revision A04
		Sheet Modified Date: Tuesday, December 19, 2017	Sheet 3 of 11

Power Switch

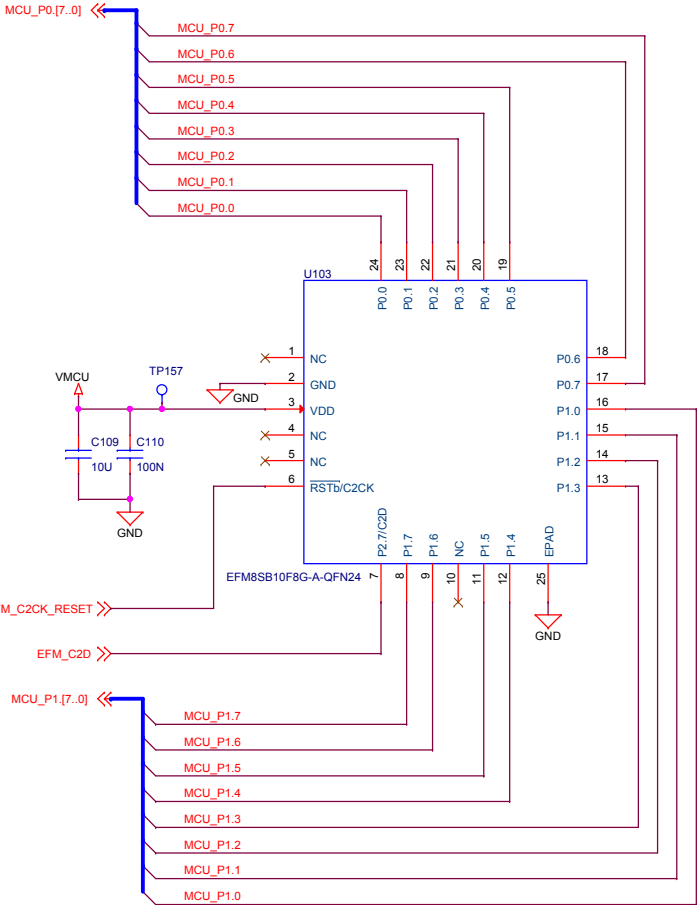
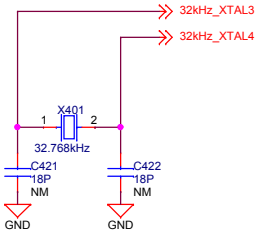
SWITCH POS	MODE DESCRIPTION
AEM	AEM Enabled, VMCU sourced from external 3.3V LDO powered by BC USB 5V supply
BAT	AEM Disabled, VMCU sourced from external coin-cell battery or external power supply




C2 Debug Header



Low Frequency Clock





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Approved: JNO
Size: A3
BOM Doc No:
Design Created Date: Wednesday, December 03, 2008

Schematic Title

EFM8SB1 Sleepy Bee STK

Page Title

EFM8 I/O & Power

Document number

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Revision

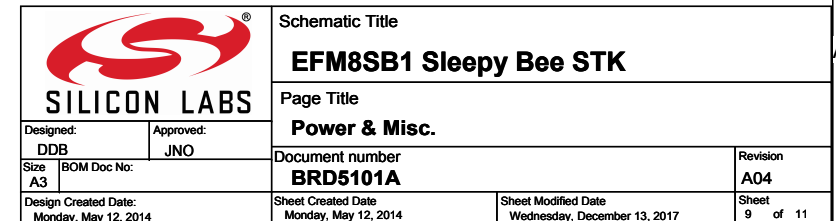
A04

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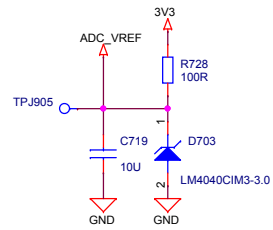
C



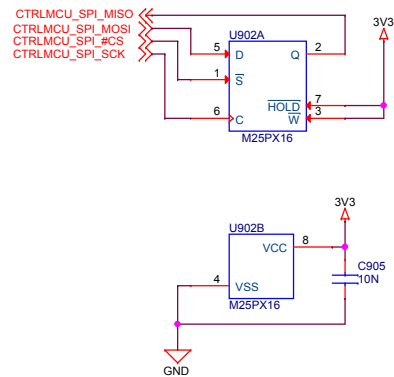
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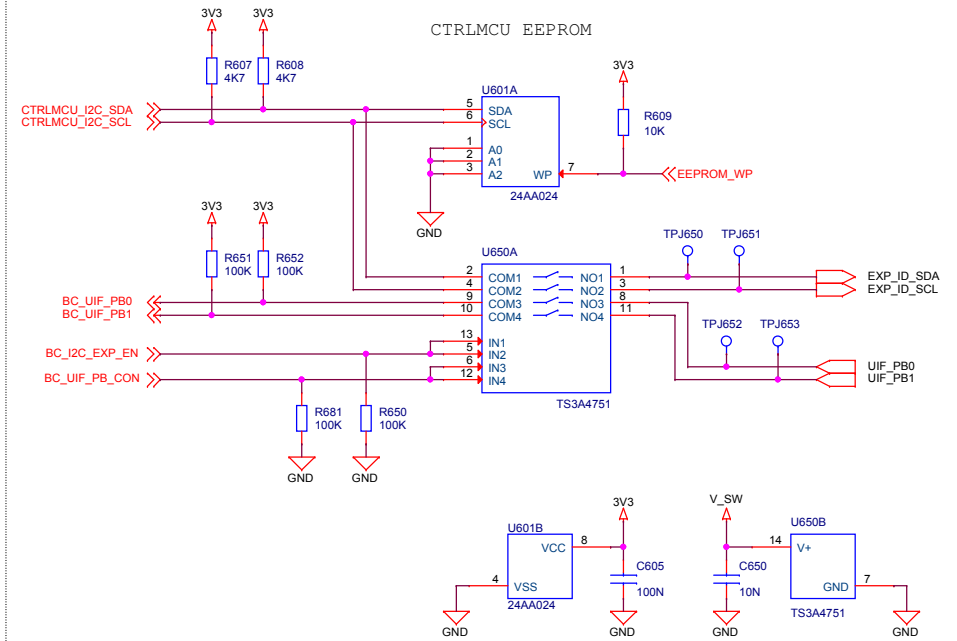
ADC reference voltage



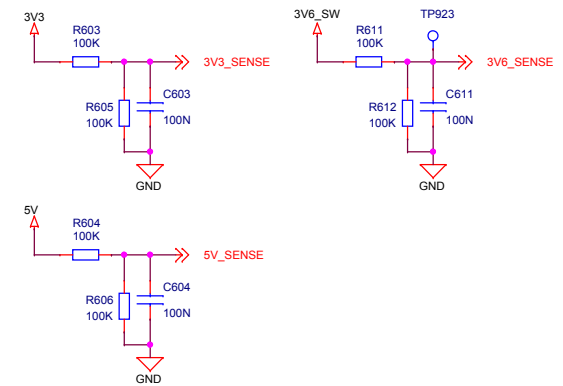
CTRLMCU SERIAL FLASH




CTRLMCU EEPROM



POWER SENSE



/HB1/CMCU1
Control MCU

		Schematic Title	
		EFM8SB1 Sleepy Bee STK	
Designed: ANB		Page Title	
Approved: JNO		Control MCU Misc.	
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		Sheet 11 of 11	