

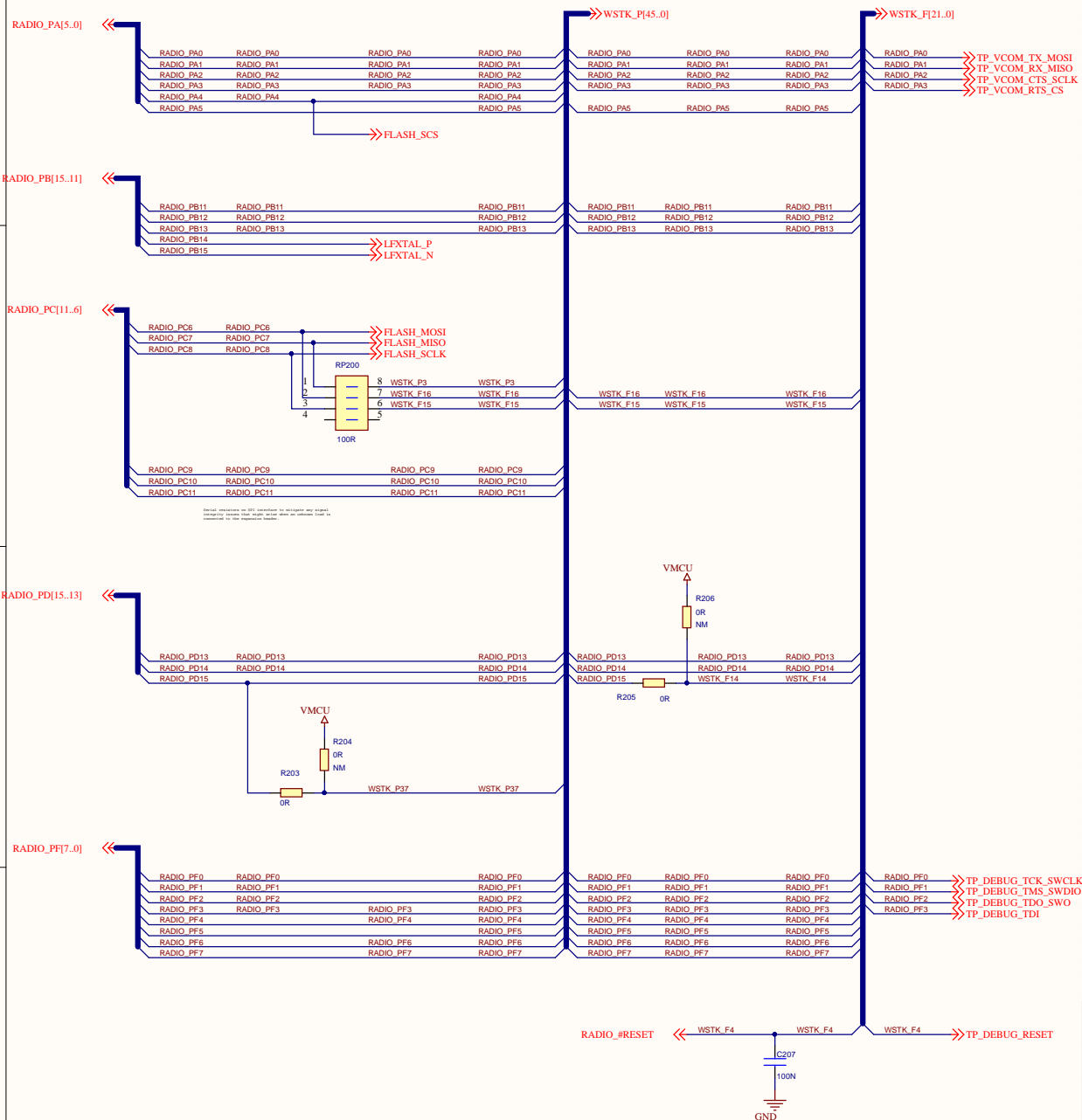
EFR32xG1x_DISC_REF_DES 434 MHz, 490 MHz, 610 MHz, 868 MHz, 915 MHz 14 / 20 dBm	
Board Function	Page
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RF, Antenna & Power	2
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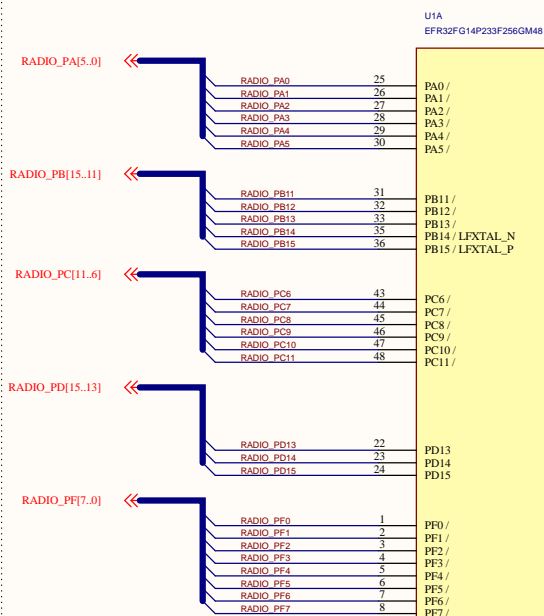
Revision History	
Rev.	Description
Rev1: A00	Discr1A: Initial revision.

The Silicon Labs logo features a stylized red 'S' composed of two curved, flame-like shapes. Below the 'S' is the text 'SILICON LABS' in a bold, black, sans-serif font.		Schematic Title	
		EFR32xG1x_DISC_REF_DES	
		Page Title	
		Title Page	
Designed Allan	Approved XX	Document number DBRB	Revision A00
Doc A3	WSTK Doc No	Design Created Date	Sheet 1 of 4
		Sheet Modified Date	

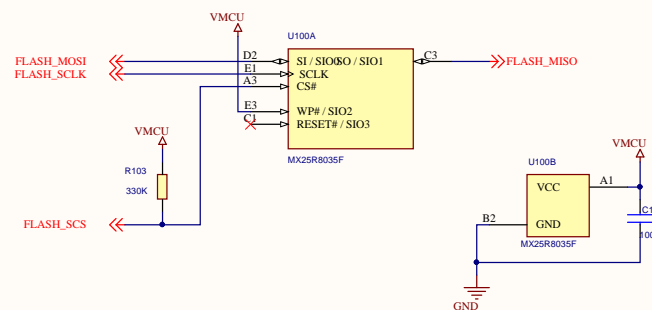
EFR32 Pin Mapping



EFR32 I/O Port Pins

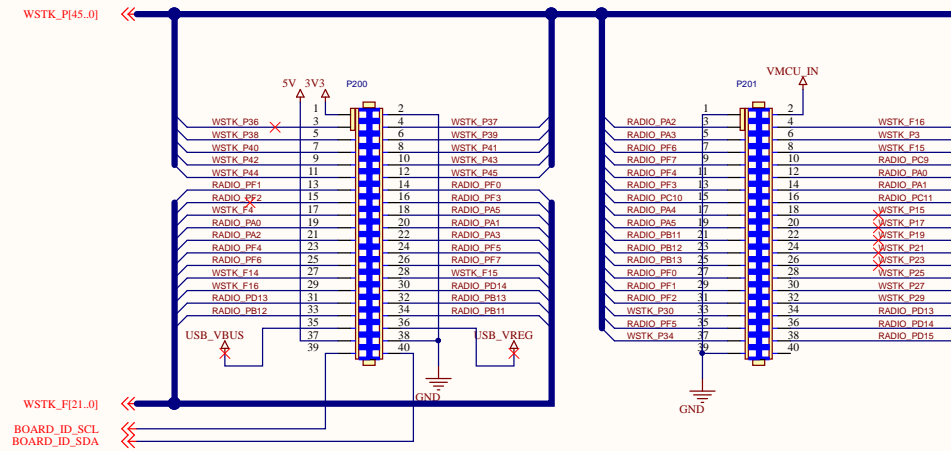


Serial Flash

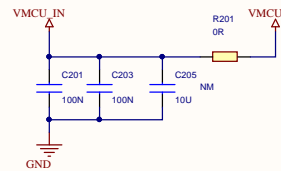


		Schematic Title	
		EFR32xG1x_DISC_REF_DES	
Page Title		Document number	
A3		DBRB	
Design Created Date	Design Created Date	Design Modified Date	Design Modified Date
Author	Reviewer	Author	Reviewer
XX		A00	
3	4		

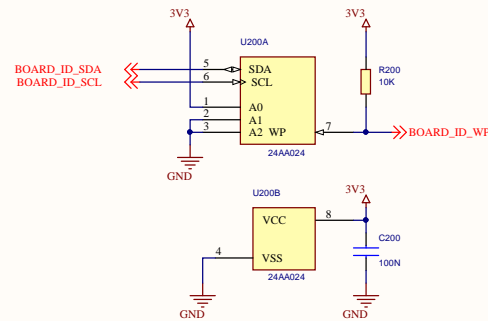
WSTK Connectors



WSTK Power Decoupling



Board Identification



Test Points

