



EFR32 SubGHz Discrete Wideband Match

780-950 MHz (18-19 dBm)

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Revision History

Rev.	Description
A00	Release version.



Schematic Title

EFR32 SubGHz Discrete Wideband Match

Page Title

Title Page

Document number

EFR32xG1x_DISC_WIDEBAND_REF_DES_A00

Revision

A00

Sheet

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Designed:

Approved:

Size

A3

BOM Doc No:

Design Created Date:

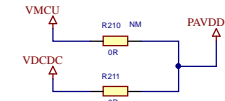
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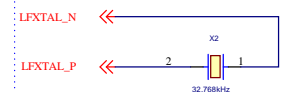
2.4 GHz matching network



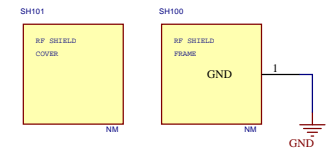
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R210	Mount	Not mount
R211	Not mount	Mount



Low Frequency Crystal



RF Shielding



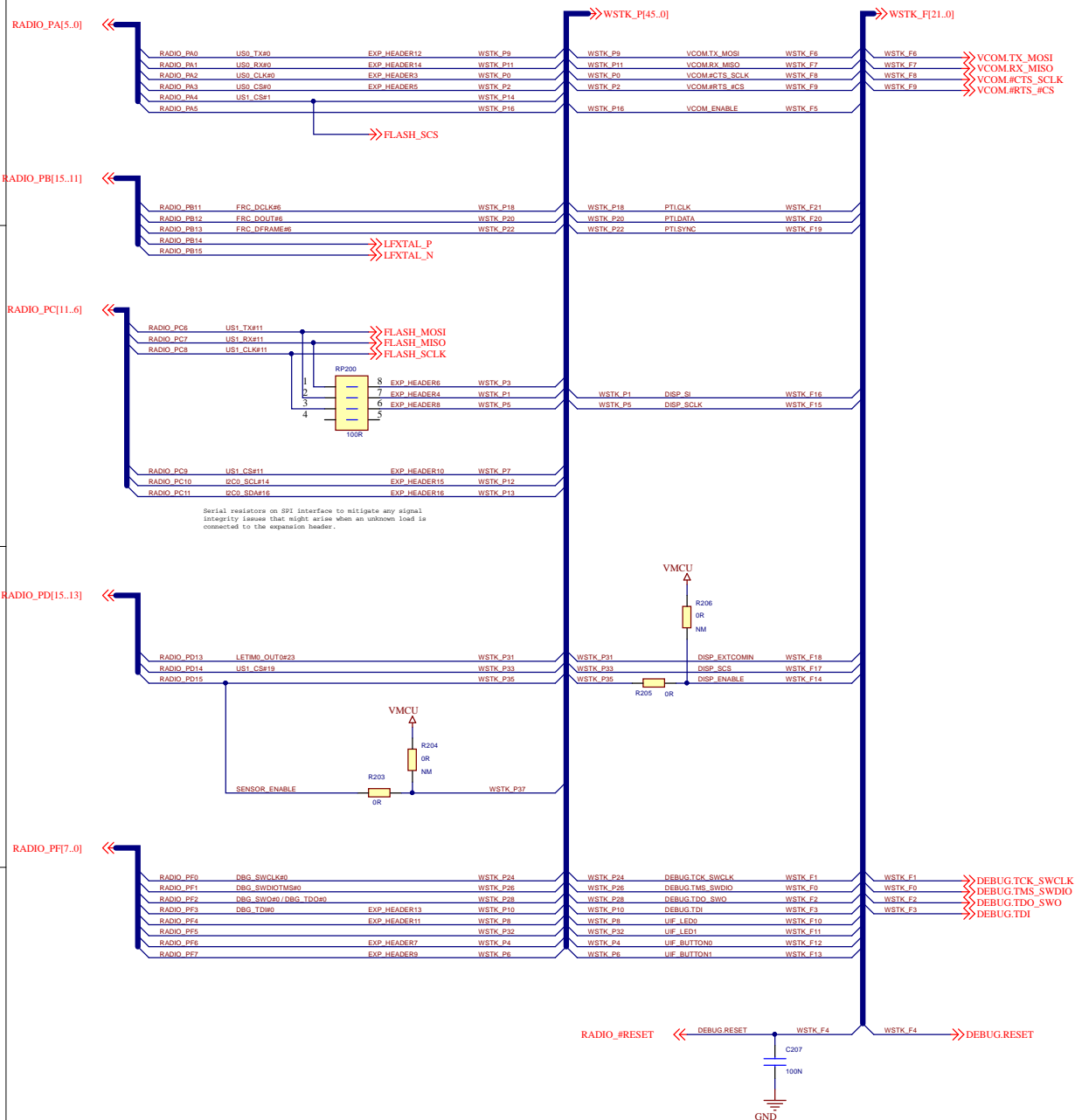
RF, Antenna & Power

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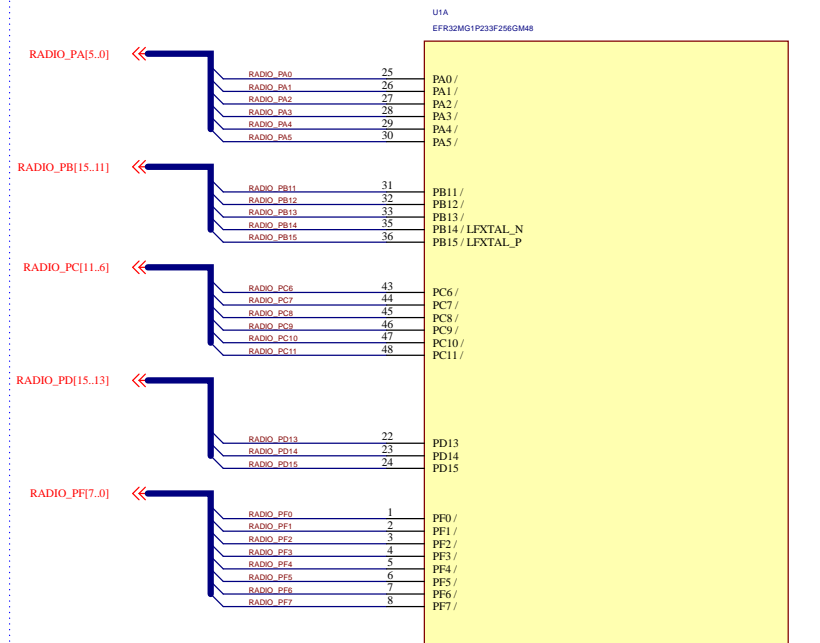
A00

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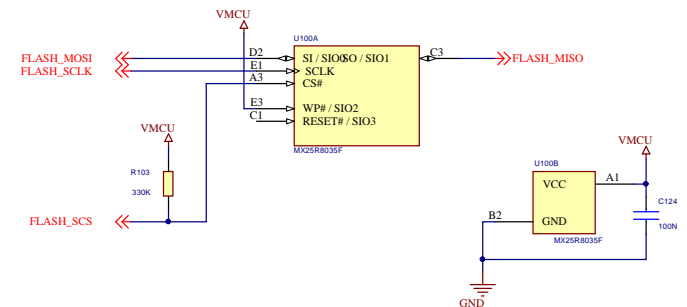
EFR32 Pin Mapping



EFR32 I/O Port Pins

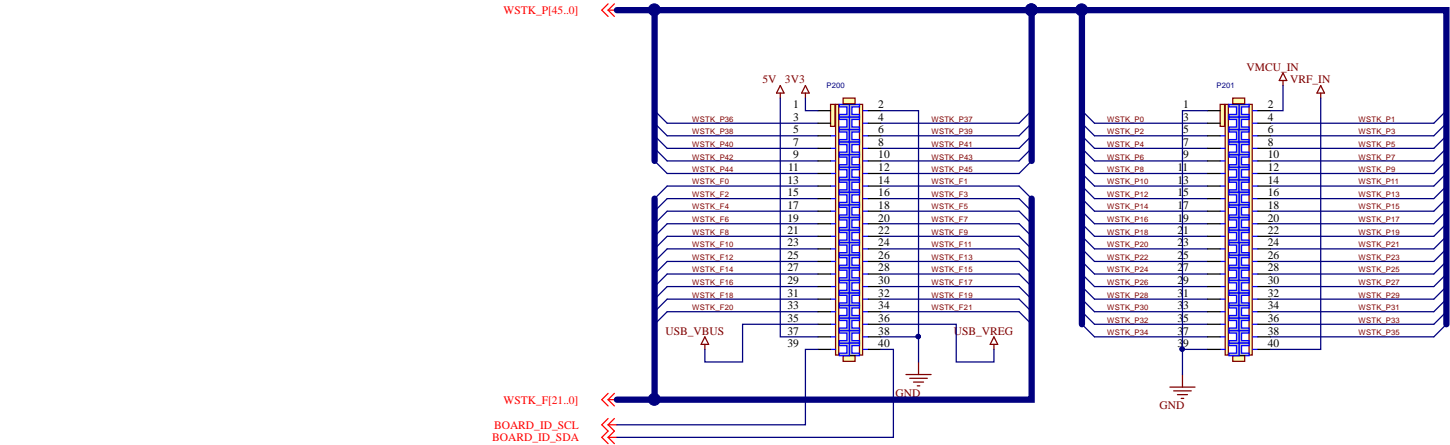


Serial Flash

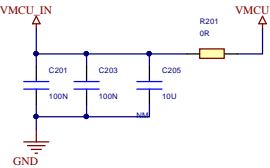


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		EFR32 SubGHz Discrete WB Match	
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		EFR32 I/O Port Connections	
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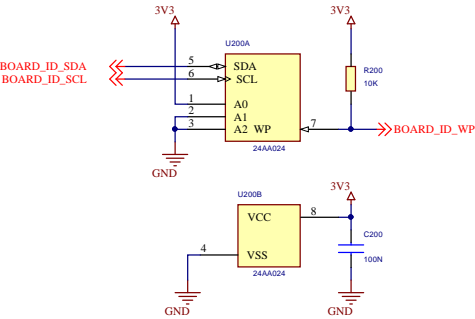
WSTK Connectors



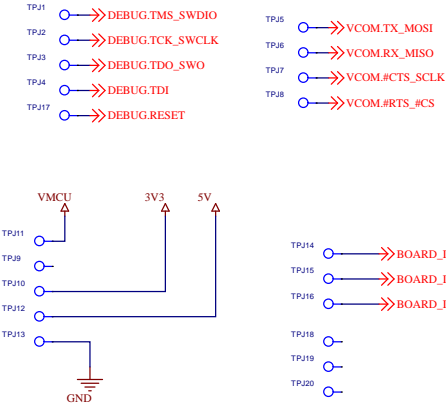
WSTK Power Decoupling




Board Identification



Test Points





SILICON LABS

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