



EFR32 SubGHz Discrete Wideband Match

315-510 MHz (18-19 dBm)

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Revision History

Rev.	Description
A00	Release version.



Schematic Title

EFR32 SubGHz Discrete Wideband Match

Page Title

Title Page

Document number

EFR32xG1x_DISC_WIDEBAND_REF_DES_A00

Revision

A00

Sheet

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Designed:

Approved:

Size

A3

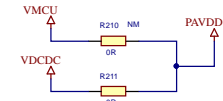
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Design Created Date:

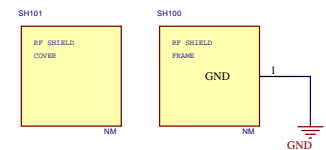
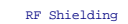
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2.4 GHz matching network

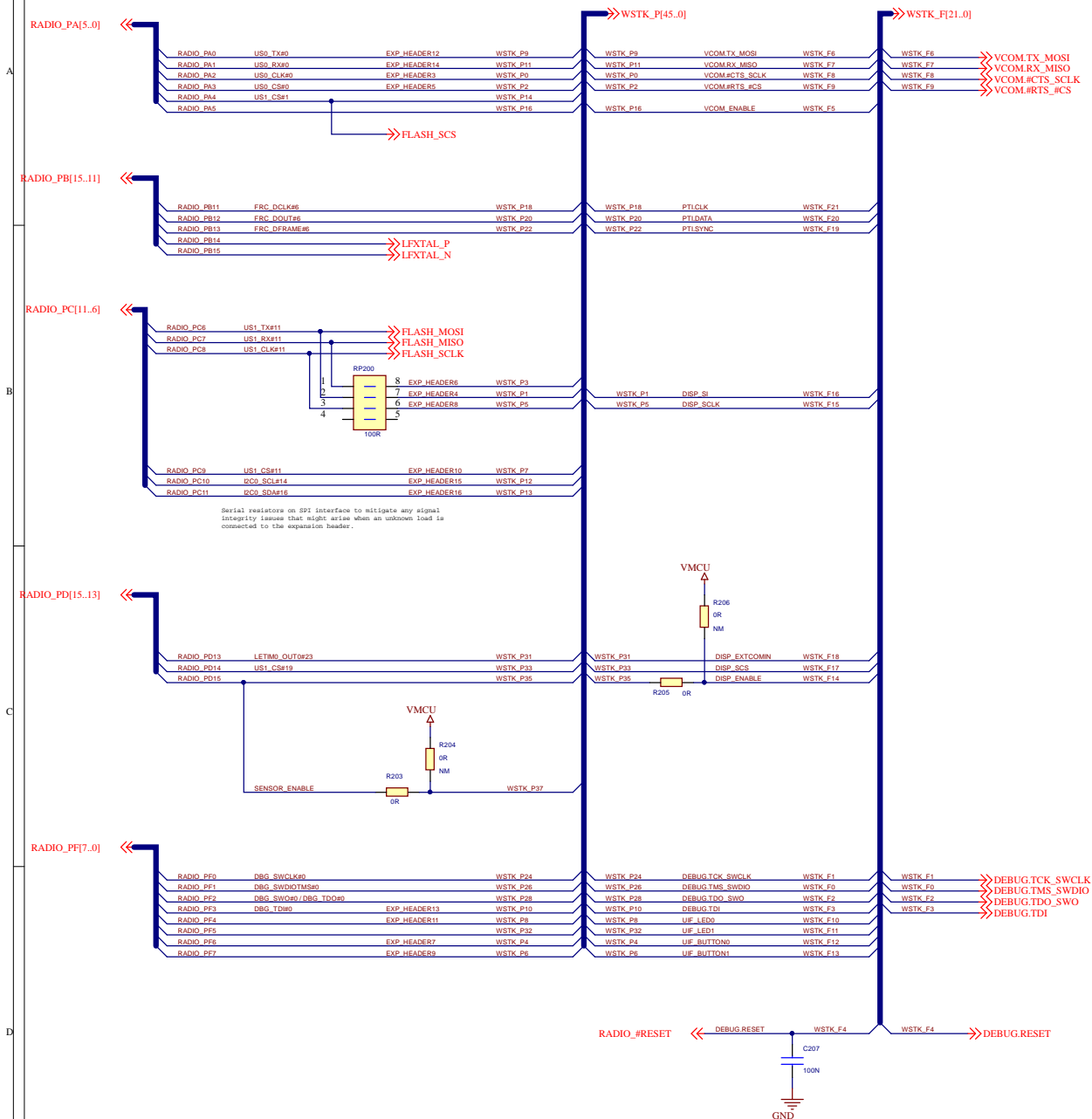


Low Frequency Crystal

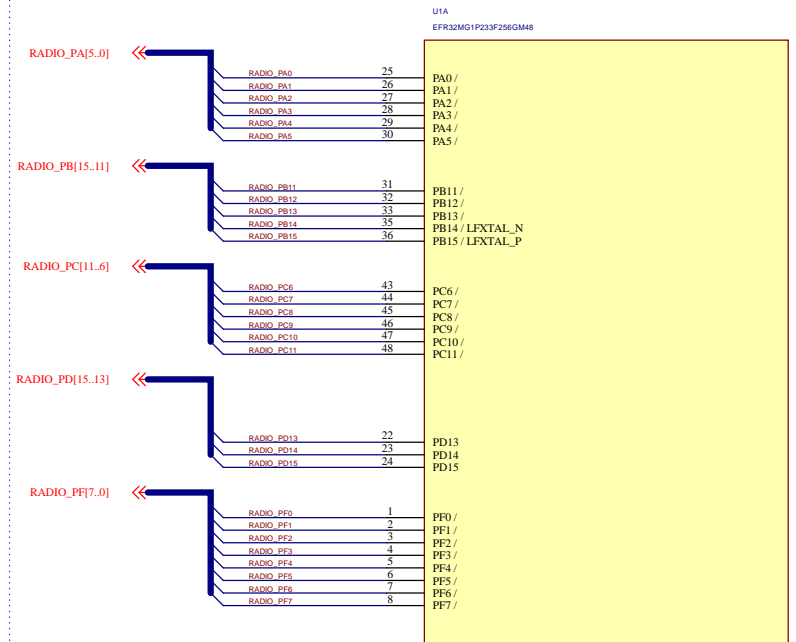


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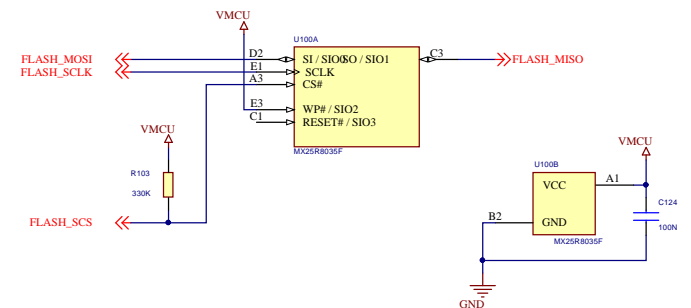
EFR32 Pin Mapping



EFR32 I/O Port Pins



Serial Flash



Schematic Title

EFR32 SubGHz Discrete WB Match

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EFR32 I/O Port Connections

Document number

EFR32xG1x_DISC_WIDEBAND_REF_DES_A00

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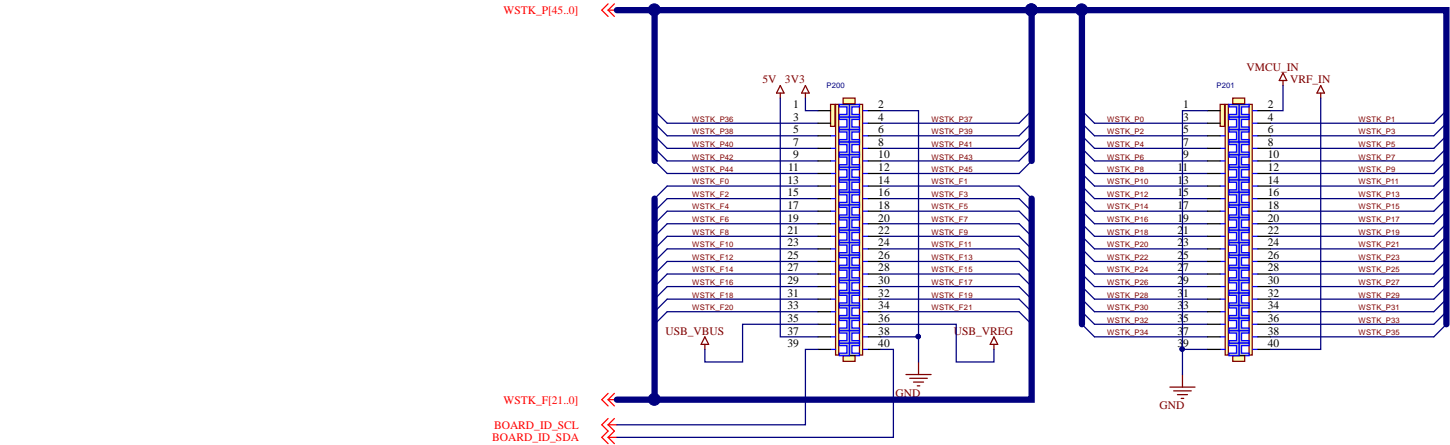
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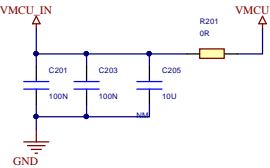
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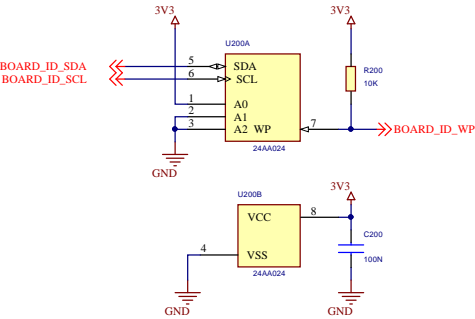
WSTK Connectors



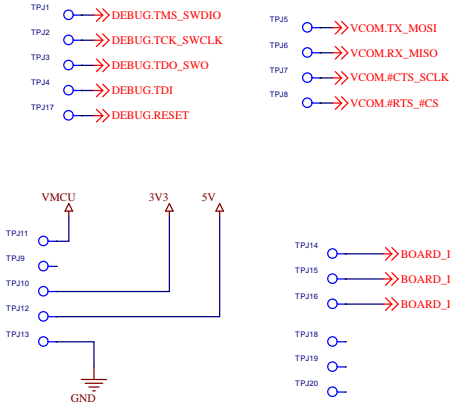
WSTK Power Decoupling




Board Identification



Test Points



 SILICON LABS		Schematic Title	
		EFR32 SubGHz Discrete WB Match	
Designed:		Page Title	
		WSTK Connectors & Board ID	
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