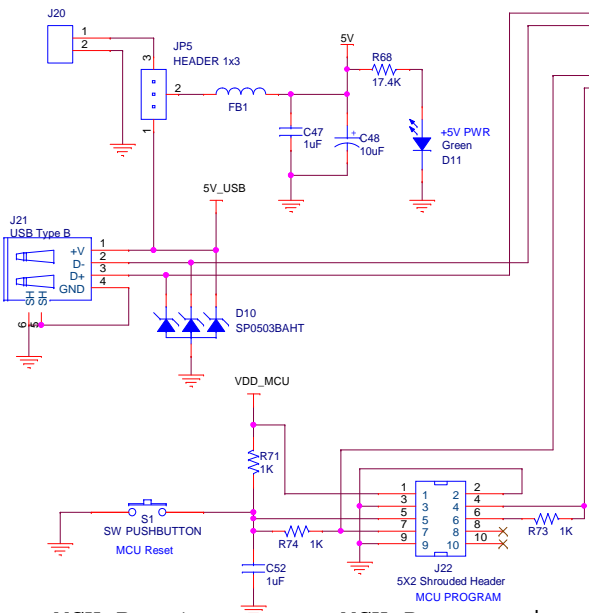
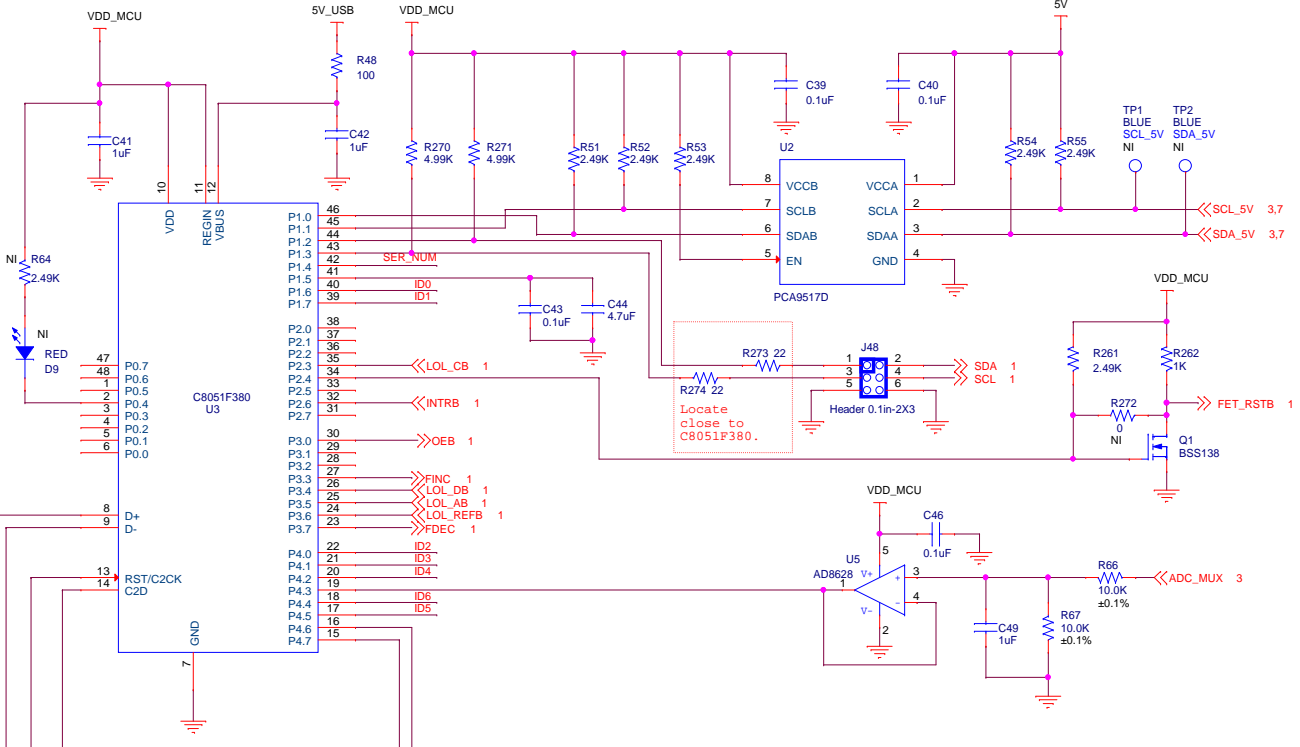


Board Serial Number



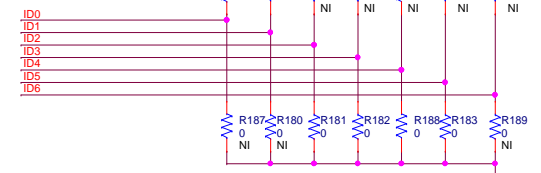
MCU Reset

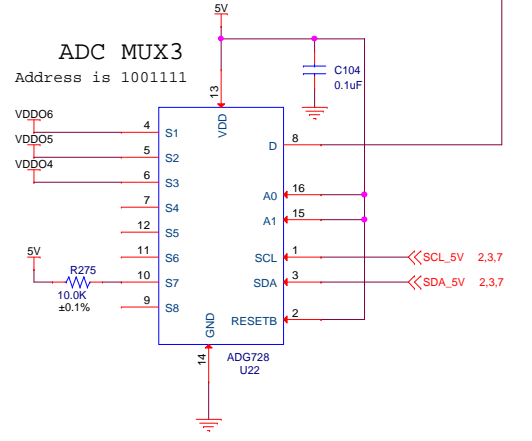
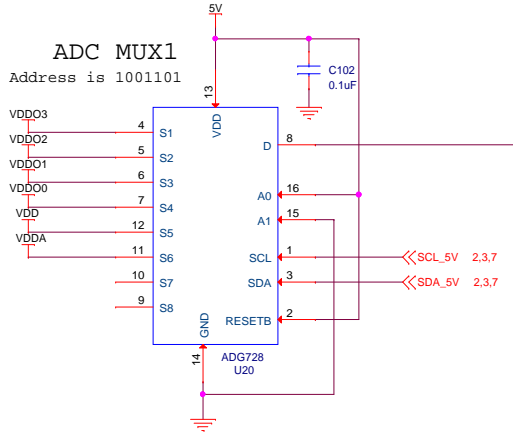
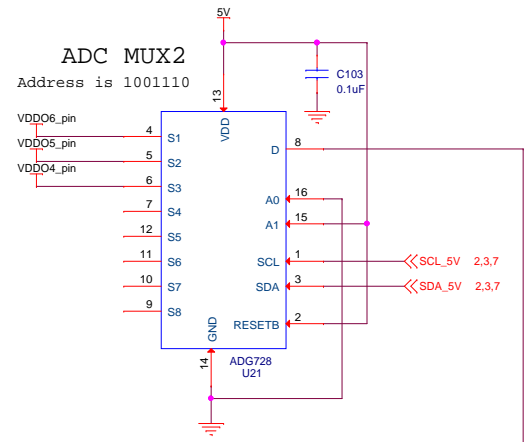
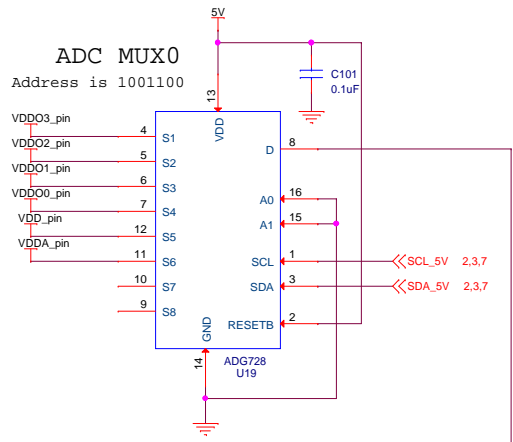
MCU Programming

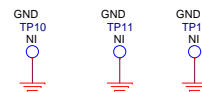
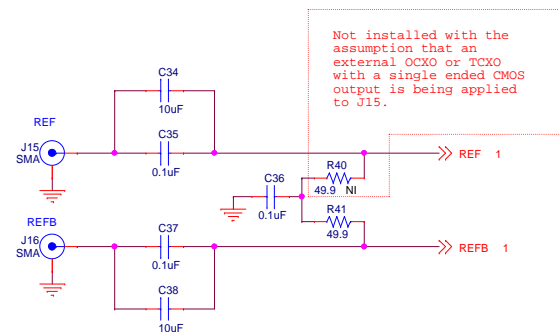
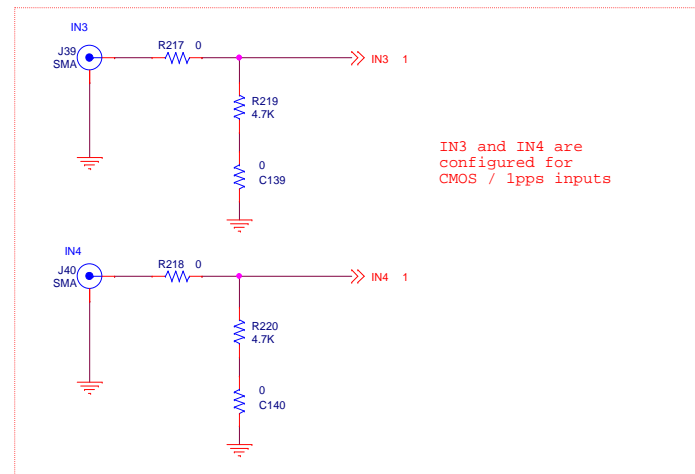
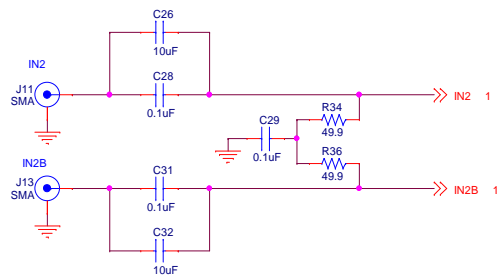
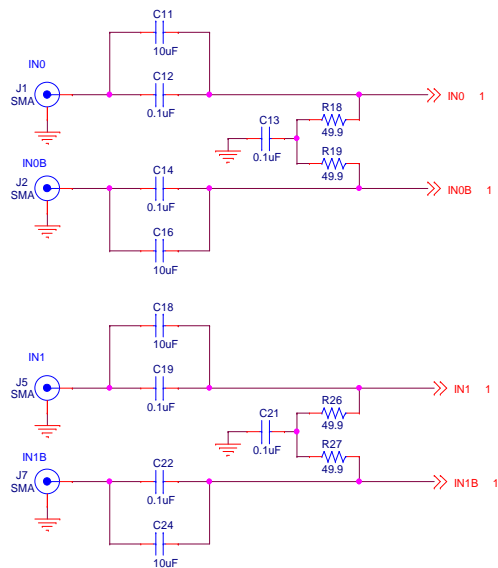



MCU Status LEDs

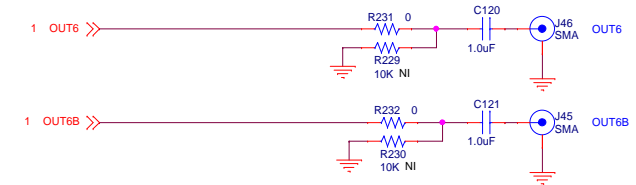
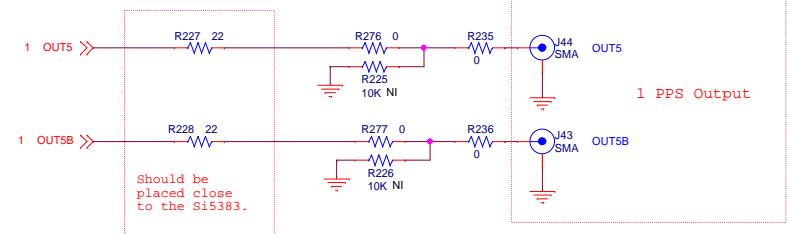
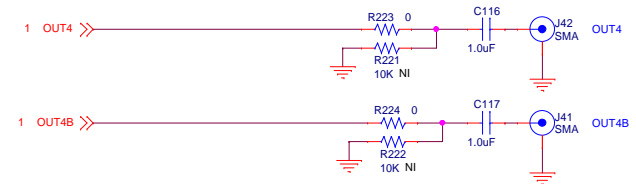
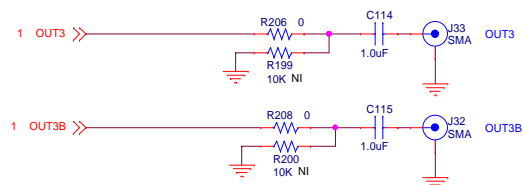
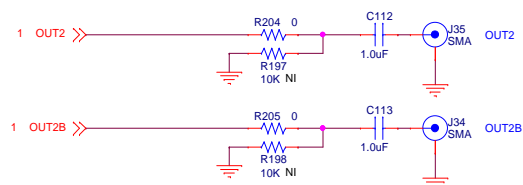
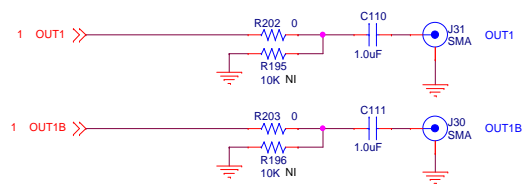
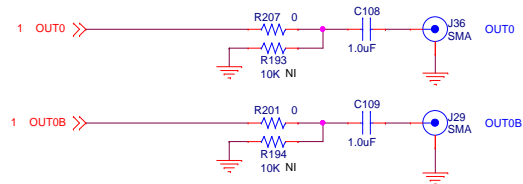
Board Address is
000011 = 3d



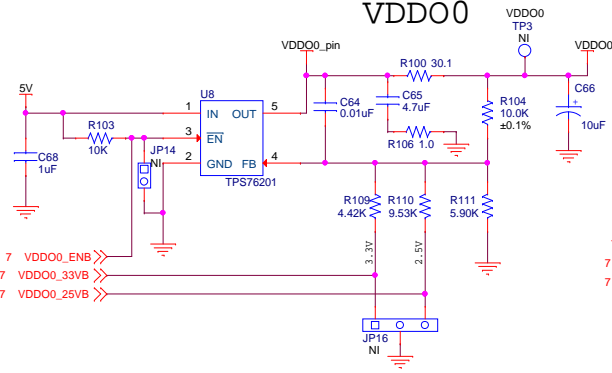




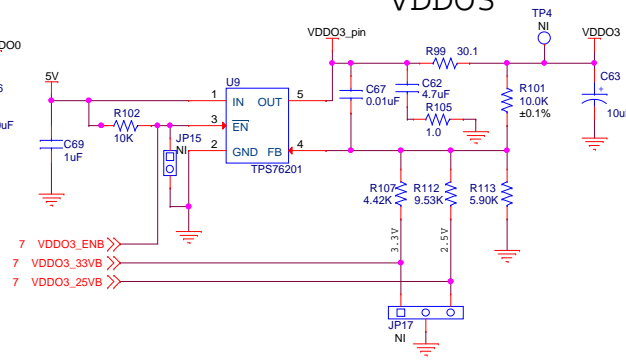
 SILICON LABS		400 W Cesar Chavez Austin, TX 78701	
Title DUT Clock Inputs			
Size B	Document Number SIS383-EB	<Variant Name>	Rev 2.1
Date:	Monday, November 26, 2016	Sheet 4 of 8	



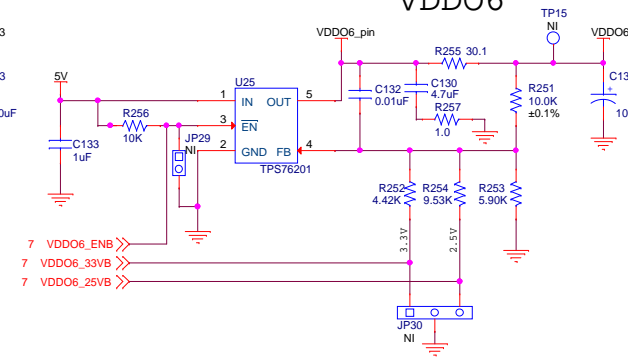
VDDO0



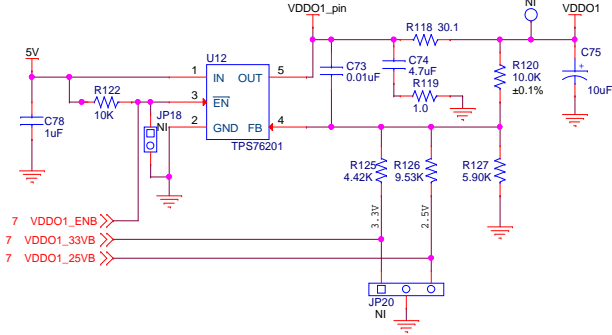
VDDO3



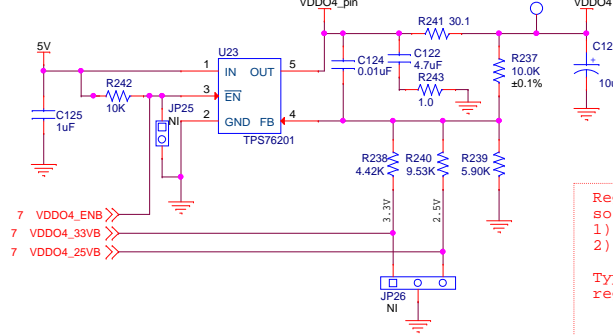
VDDO6



VDDO1



VDDO4

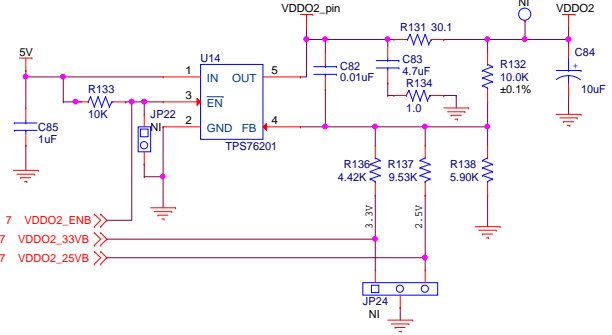


Regulators on this EVB are included to allow the EVB software to:

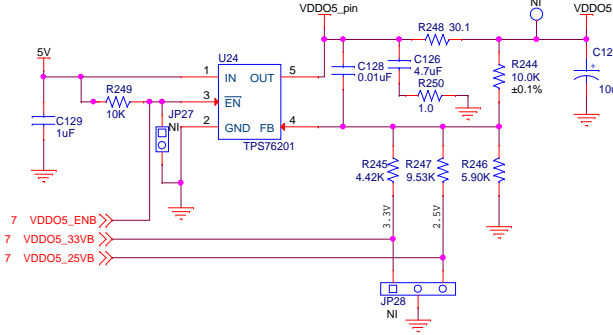
- 1) Measure the current to each DUT VDD pin
- 2) Independently control the VDD0x voltages as needed.

Typical applications will not require independent regulation for each VDD.

VDDO2



VDDO5



400 W Cesar Chavez
Austin, TX 78701

Title Output Voltage Regulators

Size B Document Number
SIS383-EB

<Variant Name>

Rev 2.1

Date: Monday, November 28, 2016

Sheet 6 of 8

Revision History

- 1.0 Initial Release - Travis Turner 9/16/2015
- 1.1 BOM update -
 - J27 is now Not Installed
 - J47 is now Not Installed
 - Change Quantity from 5 to 4 on the NSS-4-4-01
 - Change R227 and R228 from 33.2 ohms to 22.1 ohmsTravis Turner 1/5/2016
- 1.2 BOM Update - J27 and J47 changed to install
Travis Turner 1/19/2016
- 2.0 Changed J28 to four 1X2 headers, moved J20
from under J21, fixed silkscreen error on JP16.
- 2.1 R219 and R220 change from 10 Kohm NI to 4.7 Kohm and installed
C139 and C140 from 0.01 uF NI to 0 ohm and installed
Y1 part number changed to 7M48072002
U1 changed to part number Si5383A-D06791-GM