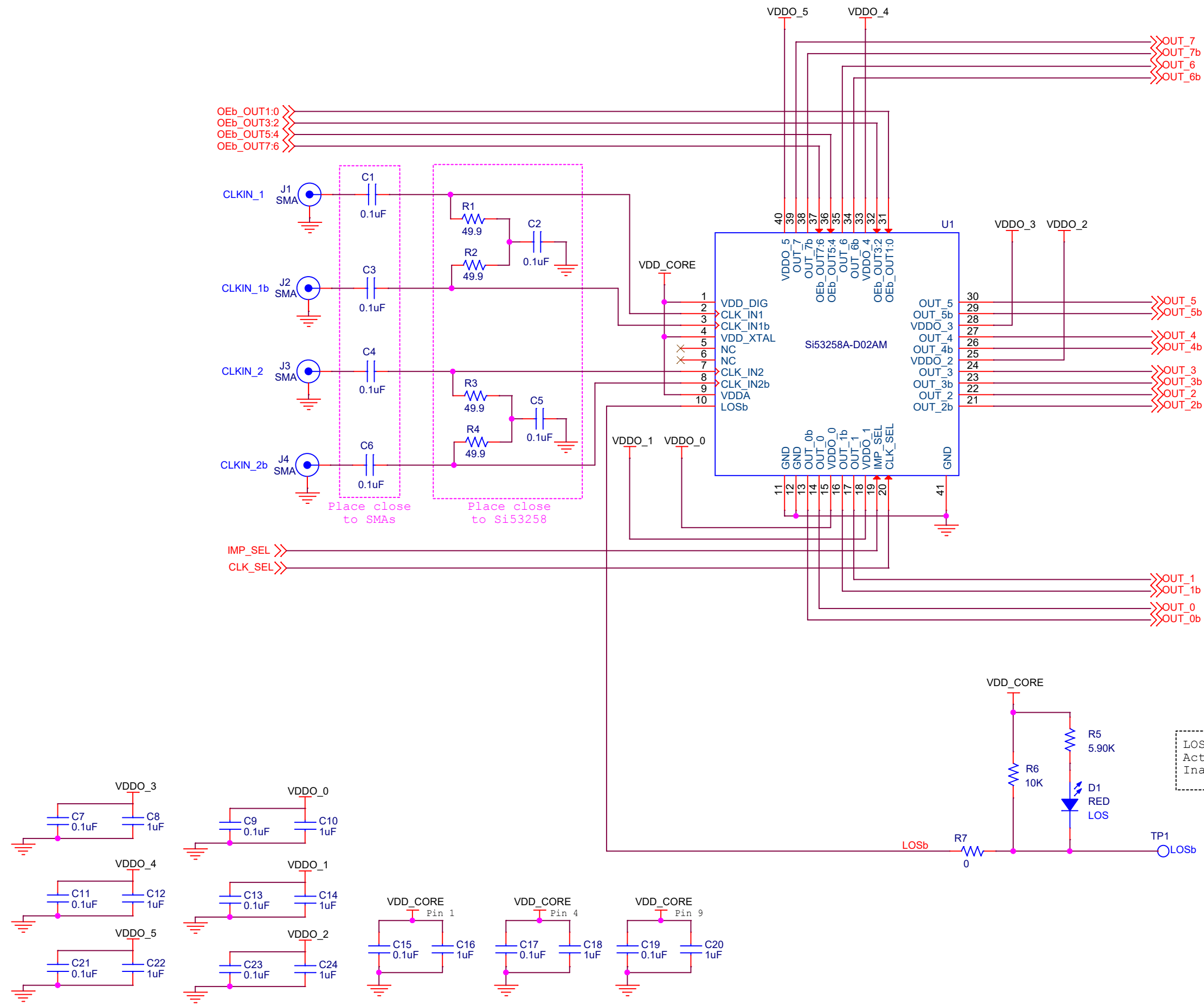

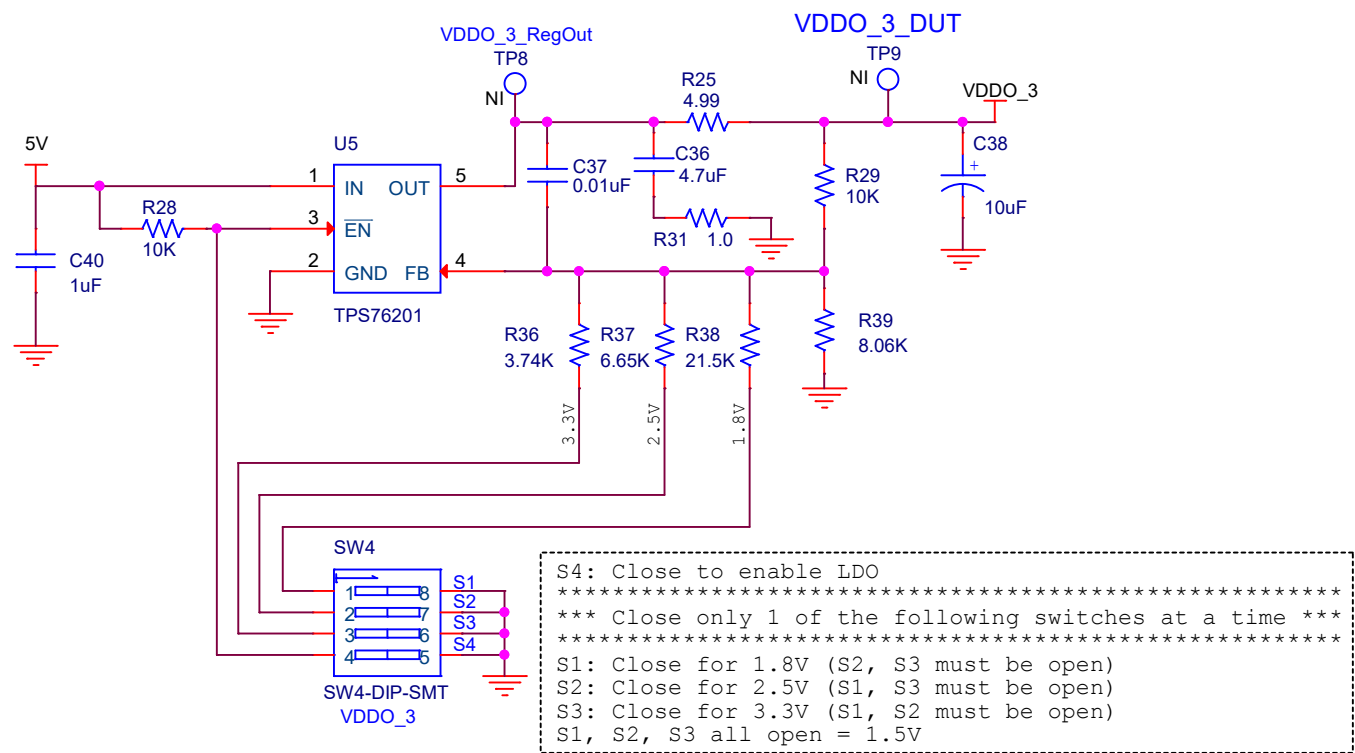
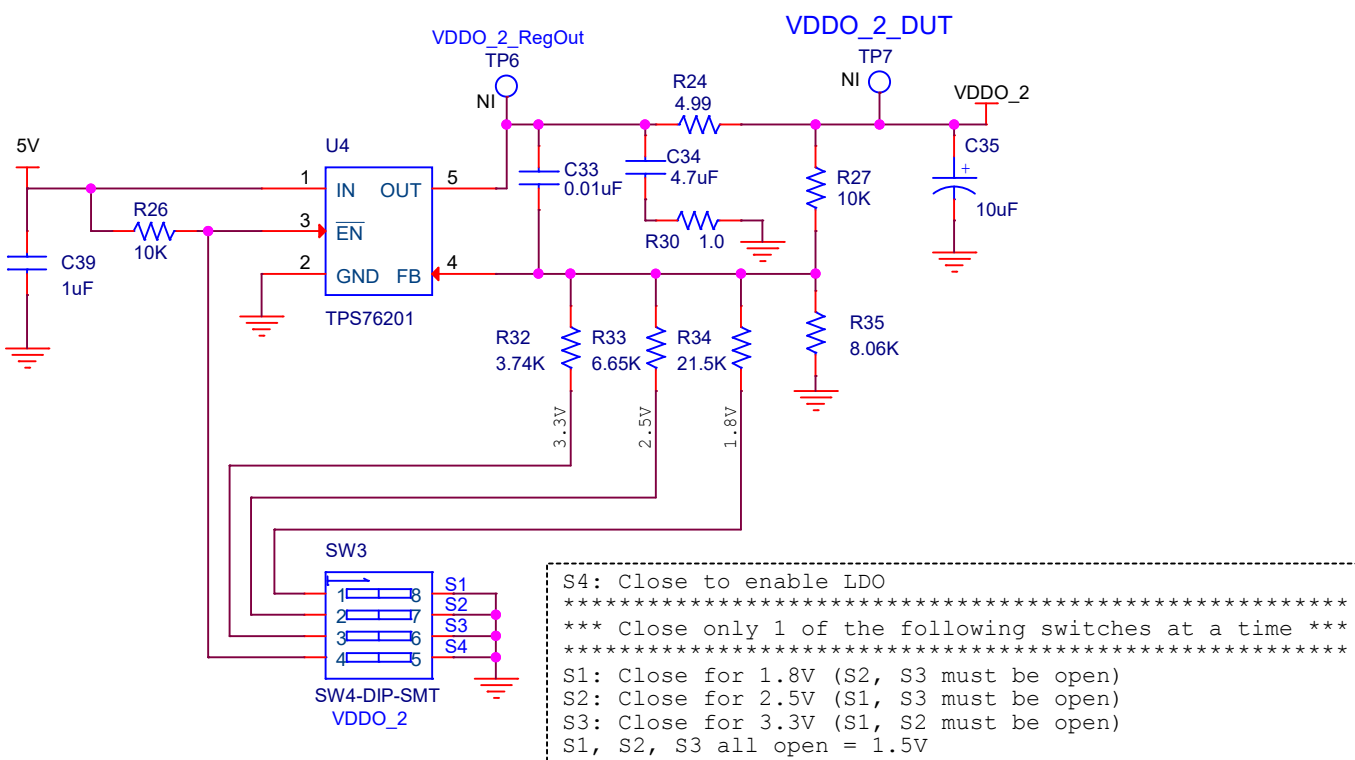
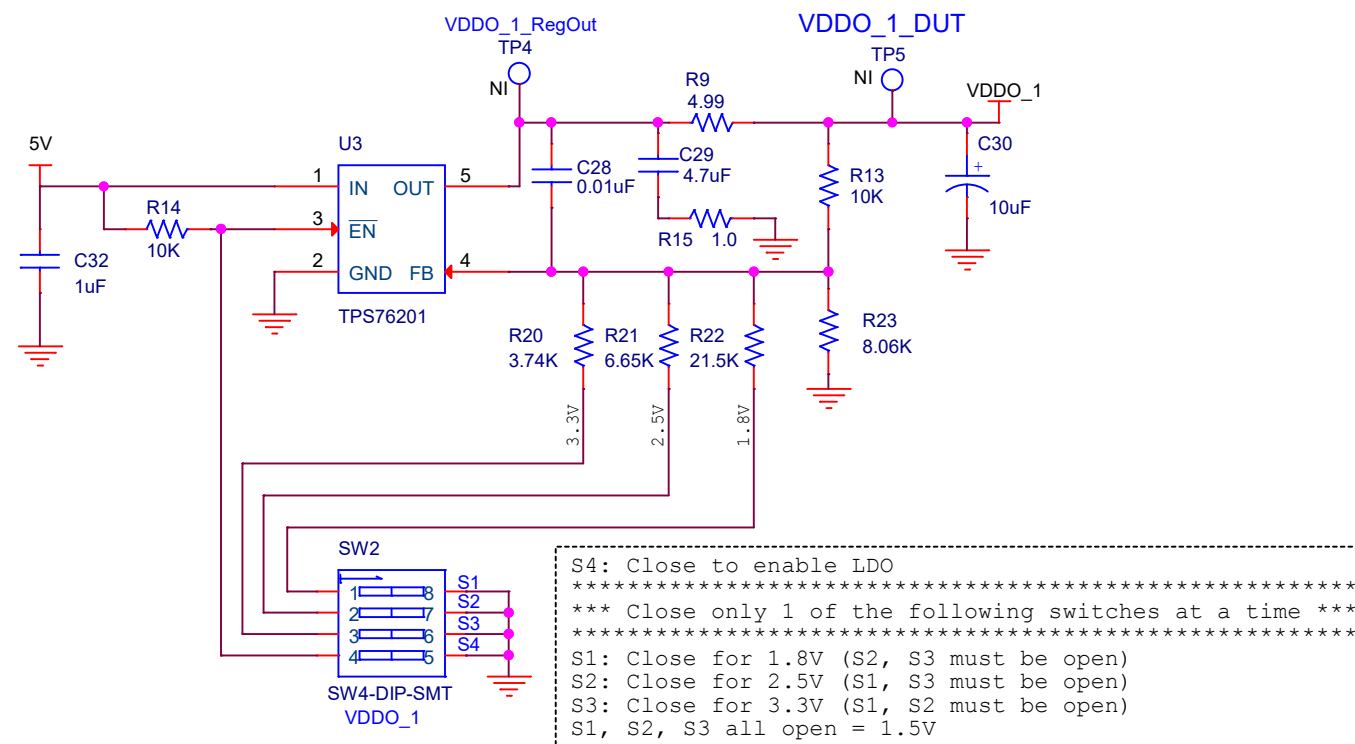
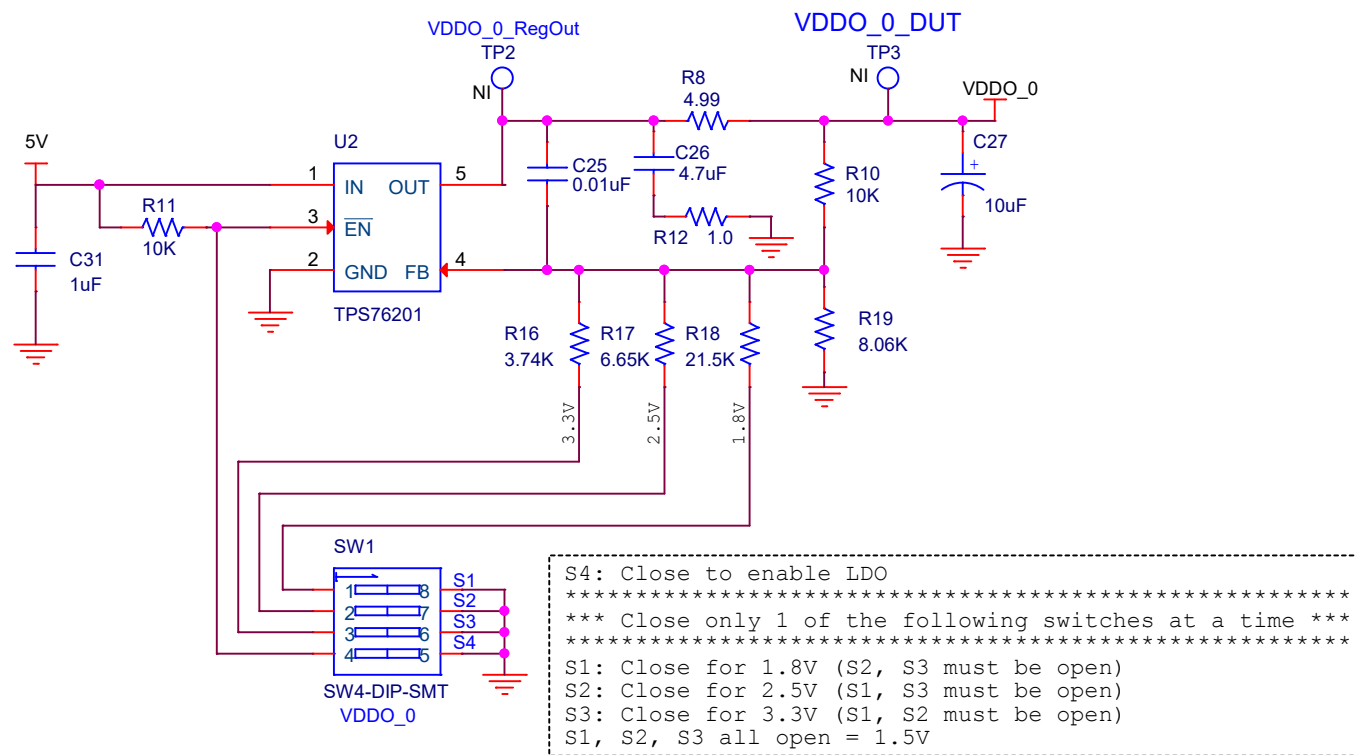
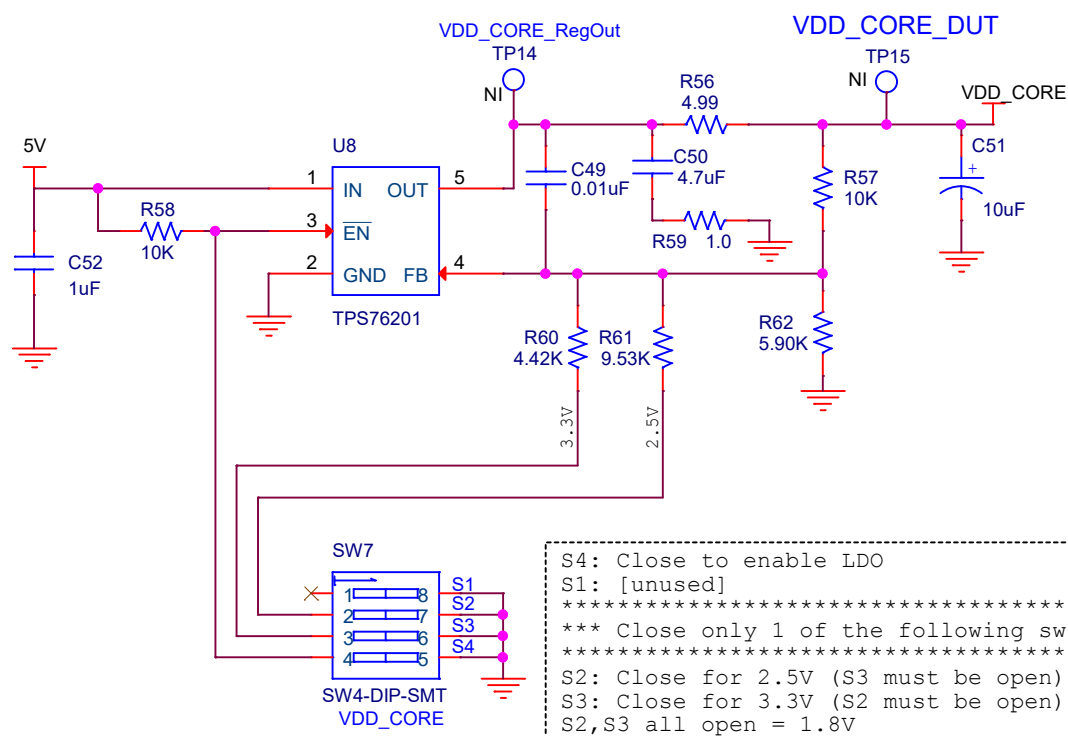
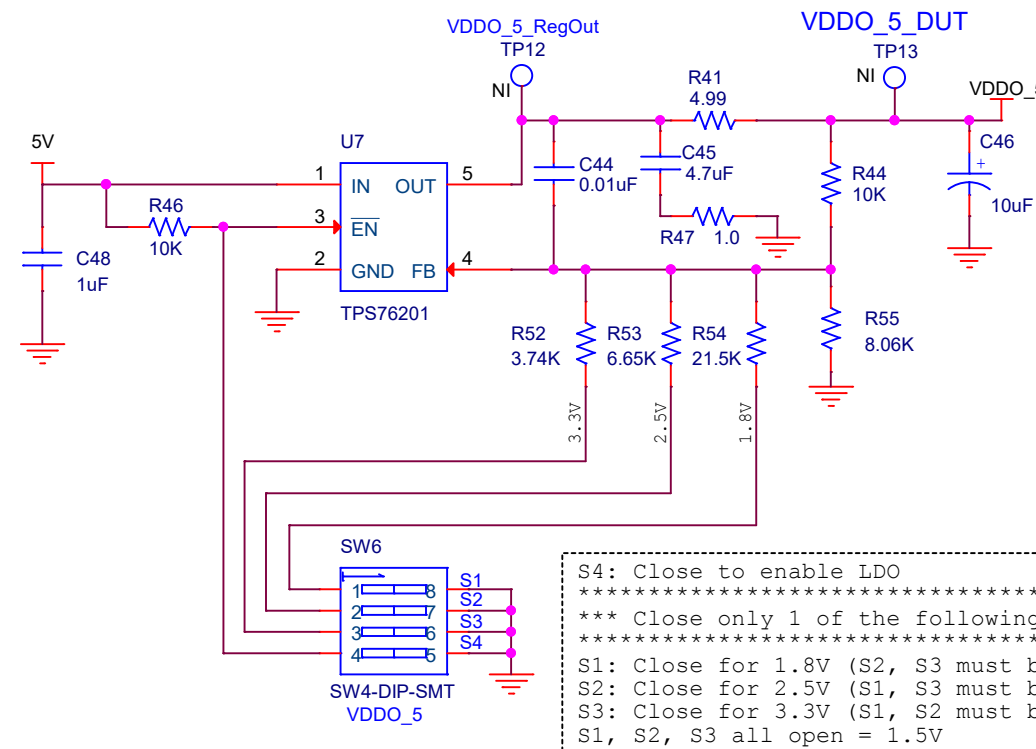
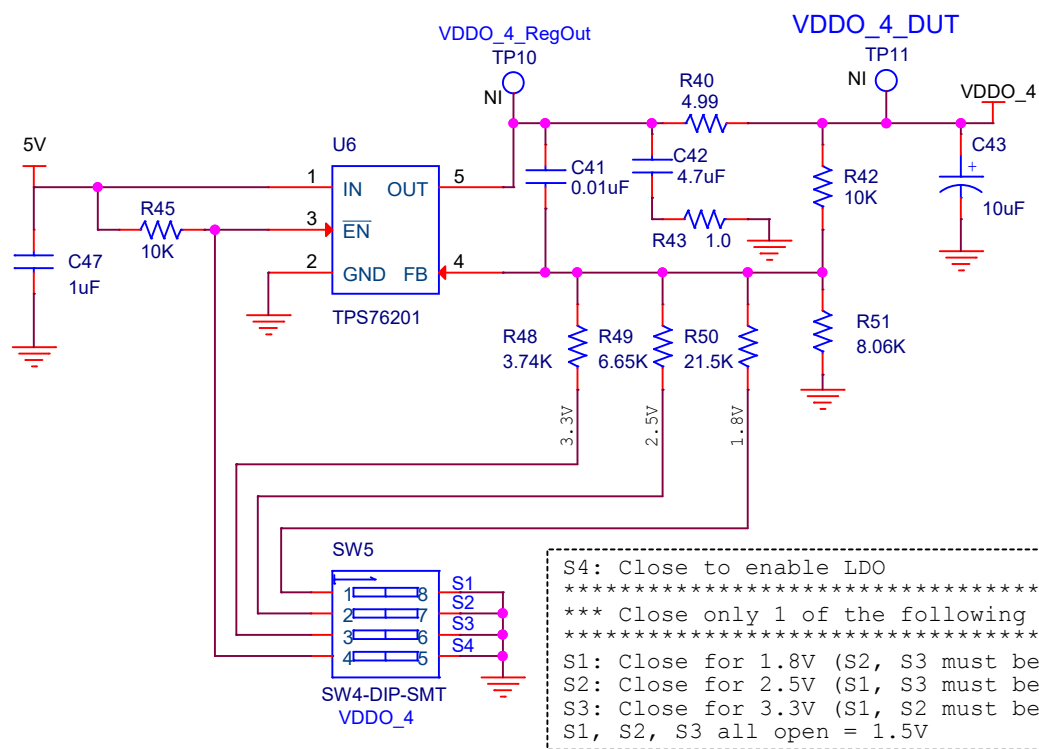


Si53258-D02-AM PCIe Buffer

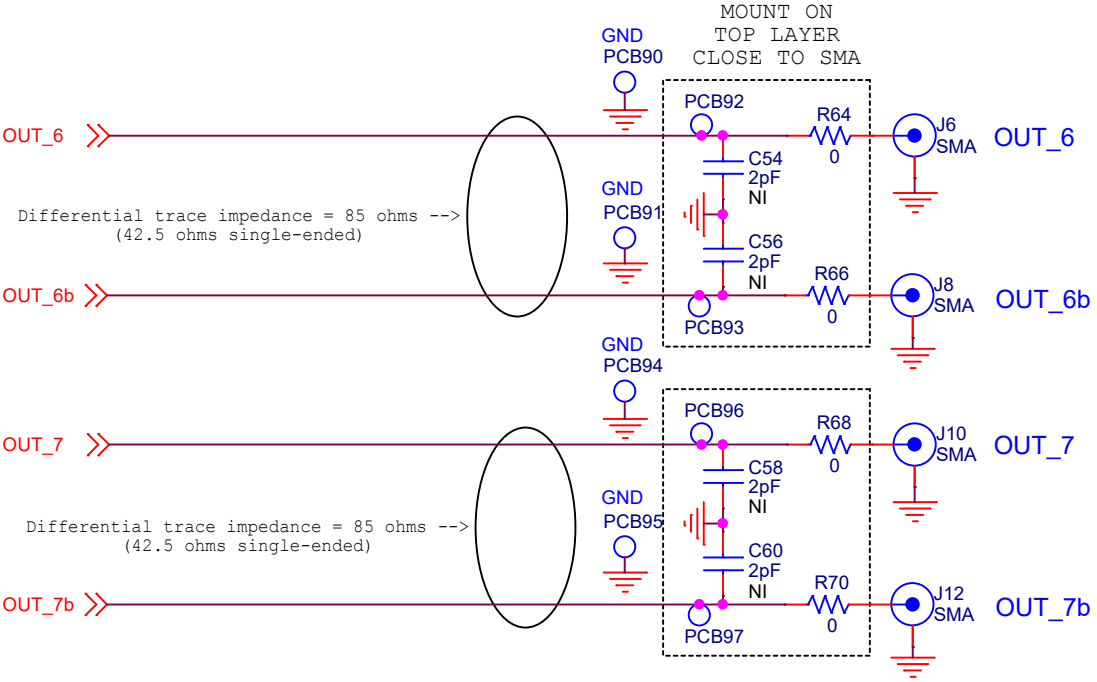
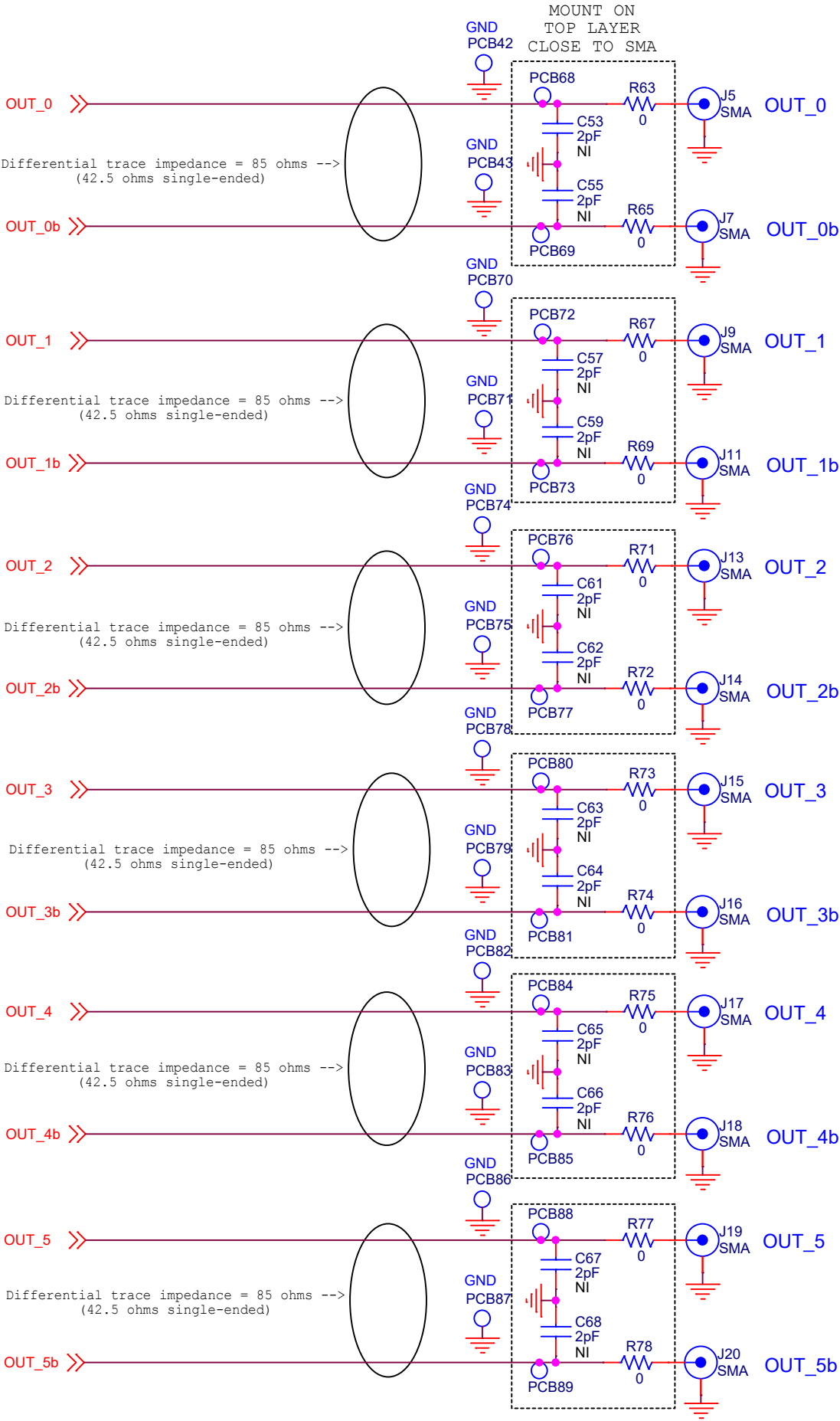


 400 W Cesar Chavez Austin, TX 78701		
Title Si53258-D02-AM		
Size B	Document Number SI53258-D02-AM 40-PIN QFN CUSTOMER EVB	Rev 1.0
Date:	Tuesday, August 06, 2019	Sheet 1 of 5





Clock Outputs



Output Termination Note:
Components can be replaced as needed to create desired termination scheme. For example, the 0 ohm resistors can be replaced with 0.1uF caps if AC coupling is desired.

See Si53258 EVB User's Manual for more information.

This figure contains two circuit diagrams for the SI53258-D02-AM 40-PIN QFN CUSTOMER EVB.

Device Function Selects

The top diagram shows the configuration of the device's function selects using a SW DIP-8 switch. The switch is connected to VDD_CORE and GND. The pins are labeled as follows:

- TP16: OEb_OUT1:0 (NI)
- TP17: OEb_OUT3:2 (NI)
- TP18: OEb_OUT5:4 (NI)
- TP19: OEb_OUT7:6 (NI)
- TP20: IMP_SEL (NI)
- TP21: CLK_SEL (NI)
- TP22: GND

The switch is labeled "Function Selects*" and has the following settings:

- OEb_OUT1:0
- OEb_OUT3:2
- OEb_OUT5:4
- OEb_OUT7:6
- CLK_SEL
- IMP_SEL

Legend for DIP switches:

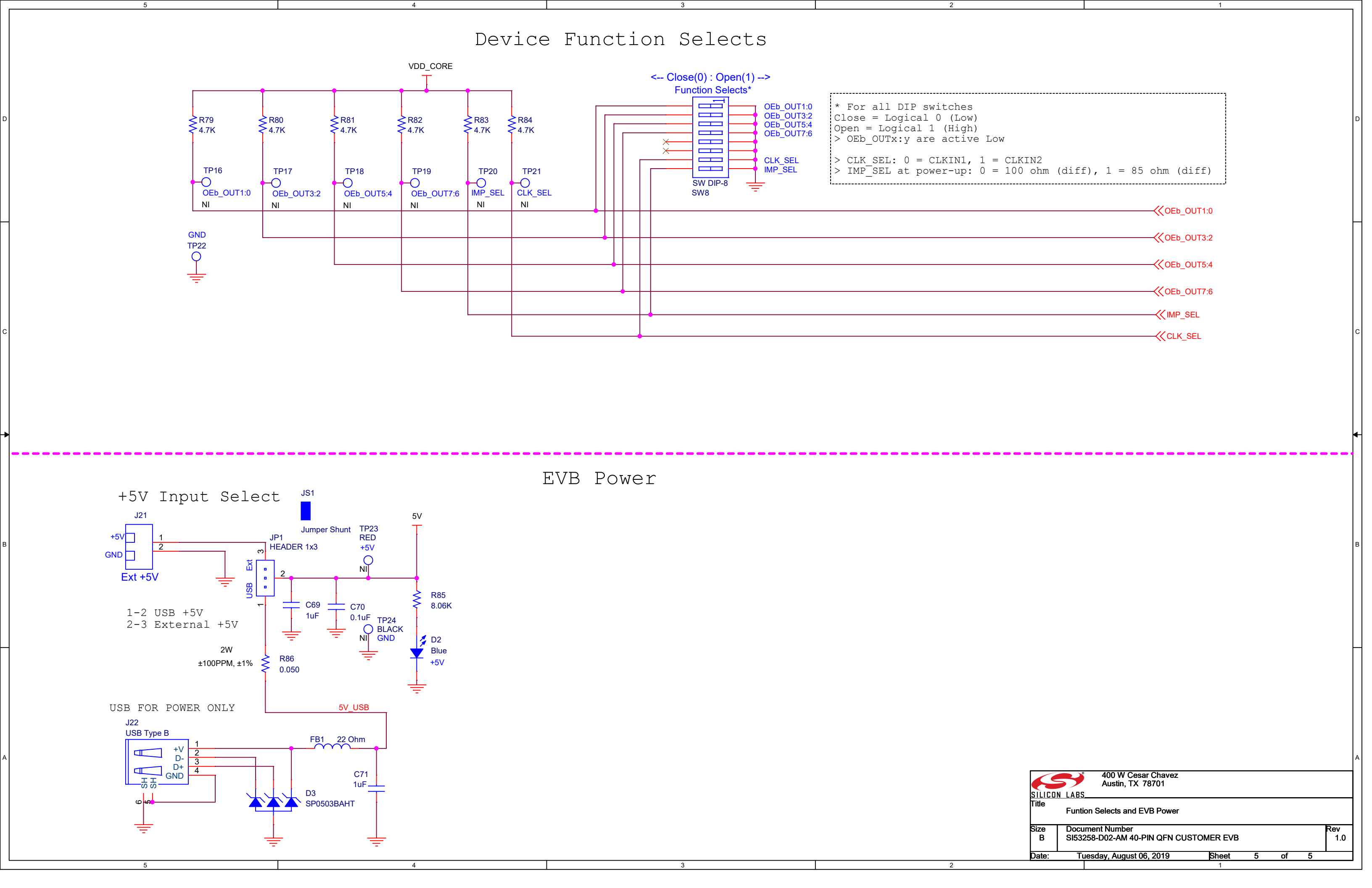
- * For all DIP switches
- Close = Logical 0 (Low)
- Open = Logical 1 (High)
- > OEb_OUTx:y are active Low
- > CLK_SEL: 0 = CLKIN1, 1 = CLKIN2
- > IMP_SEL at power-up: 0 = 100 ohm (diff), 1 = 85 ohm (diff)

EVB Power

The bottom diagram shows the EVB power supply circuit. It includes a +5V Input Select (J21) and a USB FOR POWER ONLY (J22) section. The circuit is powered by a 5V USB source (TP23) and a 5V input (TP24). The output is connected to the device's VDD_CORE pin (TP25) through a 22 Ohm resistor (FB1) and a 1uF capacitor (C71). The output is also connected to the device's GND pin (TP26) through a 1uF capacitor (C71).

Legend for EVB Power:

- 1-2 USB +5V
- 2-3 External +5V
- 2W ±100PPM, ±1%
- 5V_USB
- 5V
- TP23 RED +5V
- TP24 BLACK GND
- TP25 RED +5V
- TP26 BLACK GND
- TP27 RED +5V
- TP28 BLACK GND
- TP29 RED +5V
- TP30 BLACK GND
- TP31 RED +5V
- TP32 BLACK GND
- TP33 RED +5V
- TP34 BLACK GND
- TP35 RED +5V
- TP36 BLACK GND
- TP37 RED +5V
- TP38 BLACK GND
- TP39 RED +5V
- TP40 BLACK GND
- TP41 RED +5V
- TP42 BLACK GND
- TP43 RED +5V
- TP44 BLACK GND
- TP45 RED +5V
- TP46 BLACK GND
- TP47 RED +5V
- TP48 BLACK GND
- TP49 RED +5V
- TP50 BLACK GND
- TP51 RED +5V
- TP52 BLACK GND
- TP53 RED +5V
- TP54 BLACK GND
- TP55 RED +5V
- TP56 BLACK GND
- TP57 RED +5V
- TP58 BLACK GND
- TP59 RED +5V
- TP60 BLACK GND
- TP61 RED +5V
- TP62 BLACK GND
- TP63 RED +5V
- TP64 BLACK GND
- TP65 RED +5V
- TP66 BLACK GND
- TP67 RED +5V
- TP68 BLACK GND
- TP69 RED +5V
- TP70 BLACK GND
- TP71 RED +5V
- TP72 BLACK GND
- TP73 RED +5V
- TP74 BLACK GND
- TP75 RED +5V
- TP76 BLACK GND
- TP77 RED +5V
- TP78 BLACK GND
- TP79 RED +5V
- TP80 BLACK GND
- TP81 RED +5V
- TP82 BLACK GND
- TP83 RED +5V
- TP84 BLACK GND
- TP85 RED +5V
- TP86 BLACK GND
- TP87 RED +5V
- TP88 BLACK GND
- TP89 RED +5V
- TP90 BLACK GND
- TP91 RED +5V
- TP92 BLACK GND
- TP93 RED +5V
- TP94 BLACK GND
- TP95 RED +5V
- TP96 BLACK GND
- TP97 RED +5V
- TP98 BLACK GND
- TP99 RED +5V
- TP100 BLACK GND



This figure contains two circuit diagrams for the SI53258-D02-AM 40-PIN QFN CUSTOMER EVB.

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- IMP_SEL

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EVB Power

The bottom diagram shows the EVB power supply circuit. It includes a +5V Input Select (J21) and a USB FOR POWER ONLY (J22) section. The circuit is powered by a 5V USB source (TP23) and a 5V USB source (TP24). The output is connected to the device's VDD_CORE pin (TP25).

Key components and values:

- J21: +5V Input Select
- J22: USB Type B
- TP23: RED +5V
- TP24: BLACK GND
- TP25: GND
- R85: 8.06K
- R86: 0.050
- C69: 1uF
- C70: 0.1uF
- C71: 1uF
- D2: Blue +5V
- D3: SP0503BAHT
- FB1: 22 Ohm

This figure contains two circuit diagrams for the SI53258-D02-AM 40-PIN QFN CUSTOMER EVB.

Device Function Selects

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- IMP_SEL

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EVB Power

The bottom diagram shows the EVB power supply circuit. It includes a +5V Input Select (J21) and a USB FOR POWER ONLY (J22) section. The circuit is powered by a 5V USB source (TP23) and a 5V USB source (TP24). The output is connected to the device's VDD_CORE pin (TP25).

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- FB1: 22 Ohm

