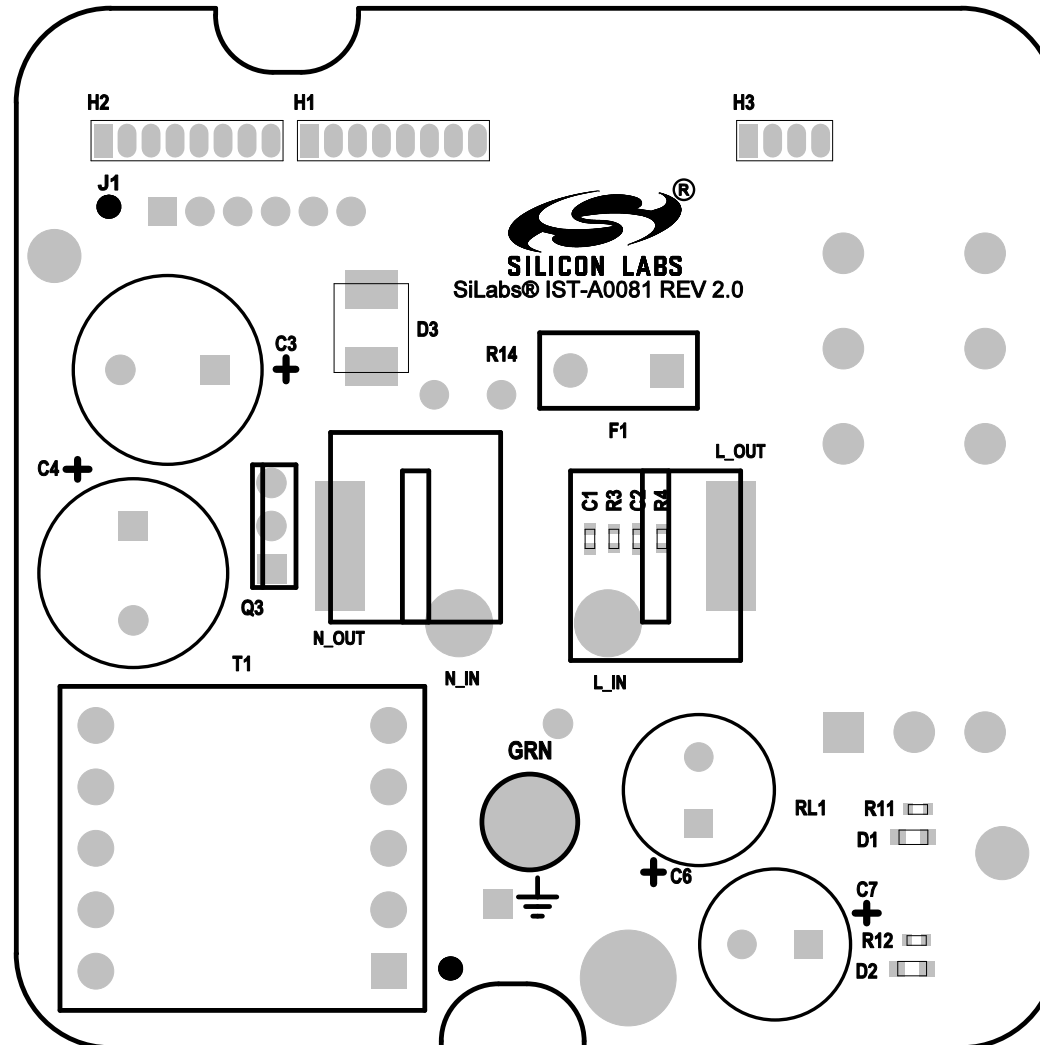
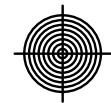
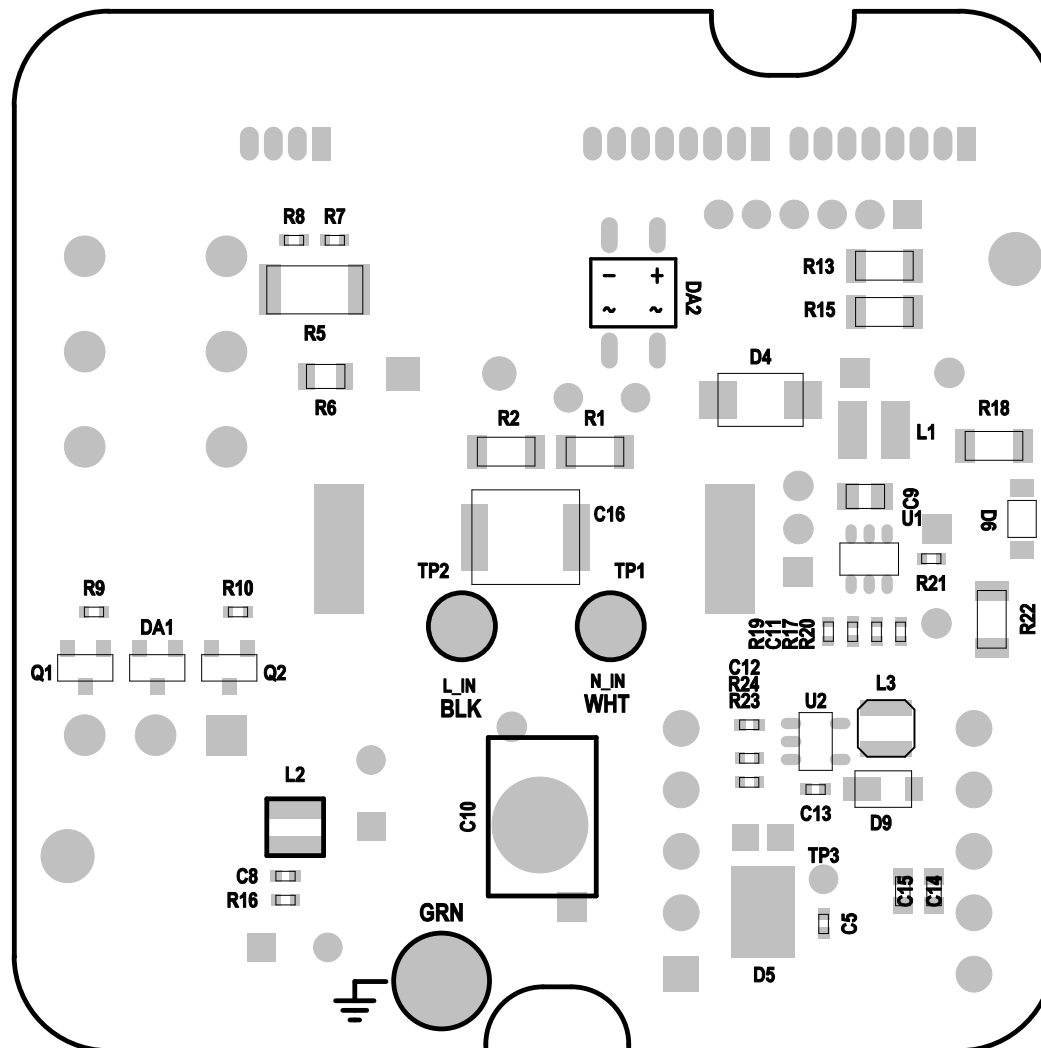
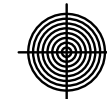


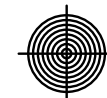


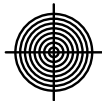
PRIMARY SILKSCREEN



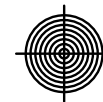
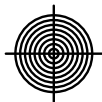
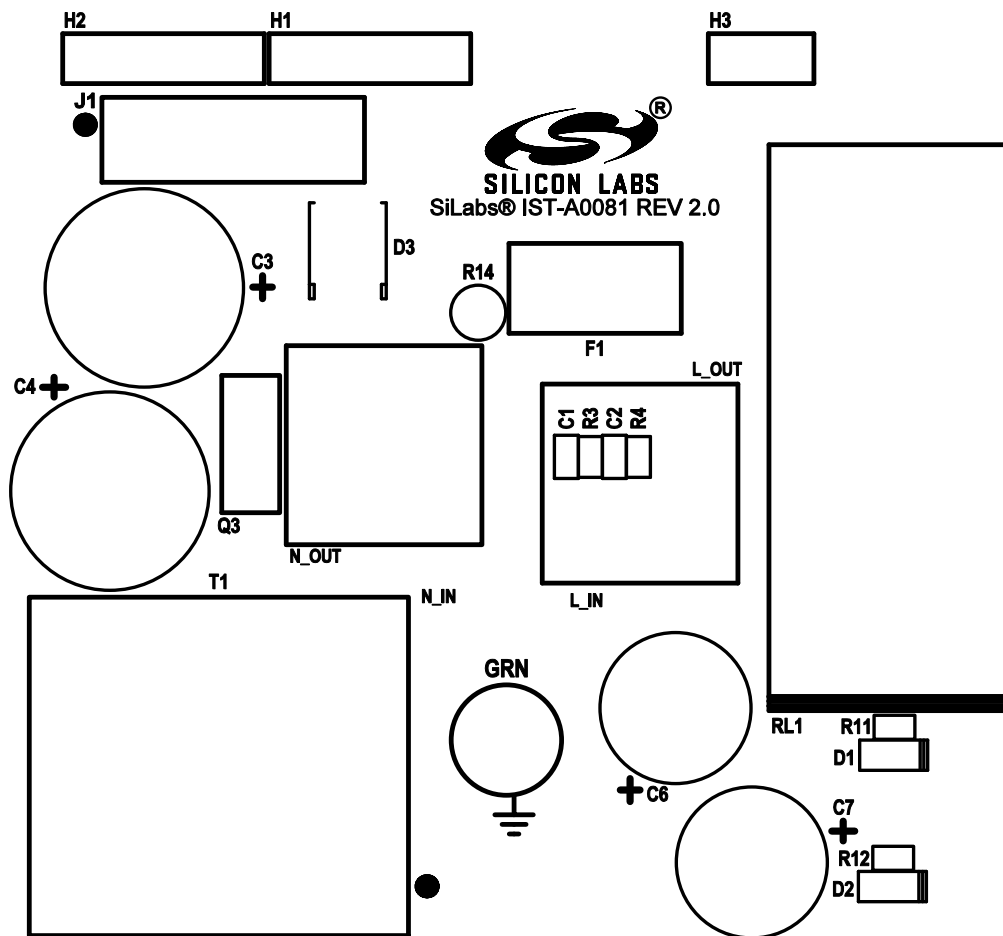


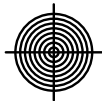
SECONDARY SILKSCREEN



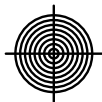
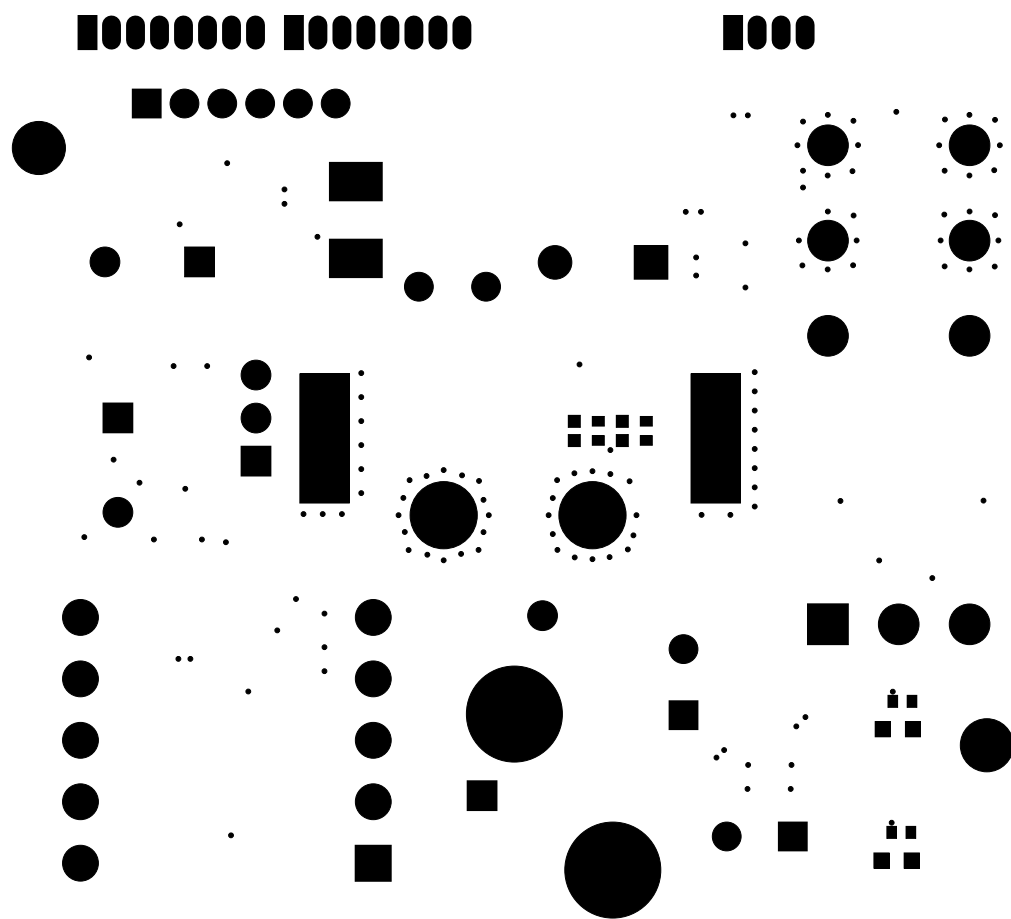


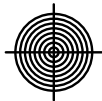
PRIMARY SILKSCREEN



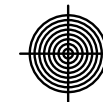
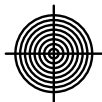
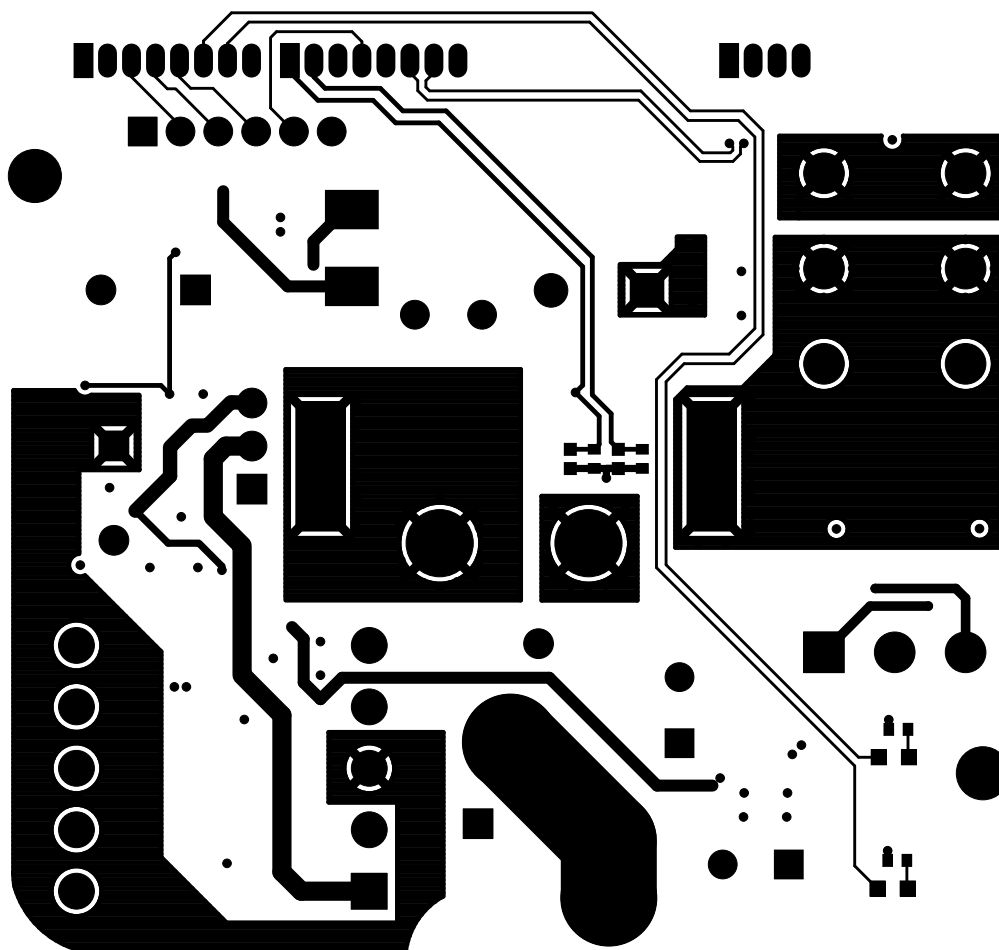


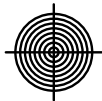
PRIMARY SOLDER MASK



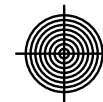
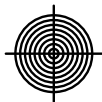
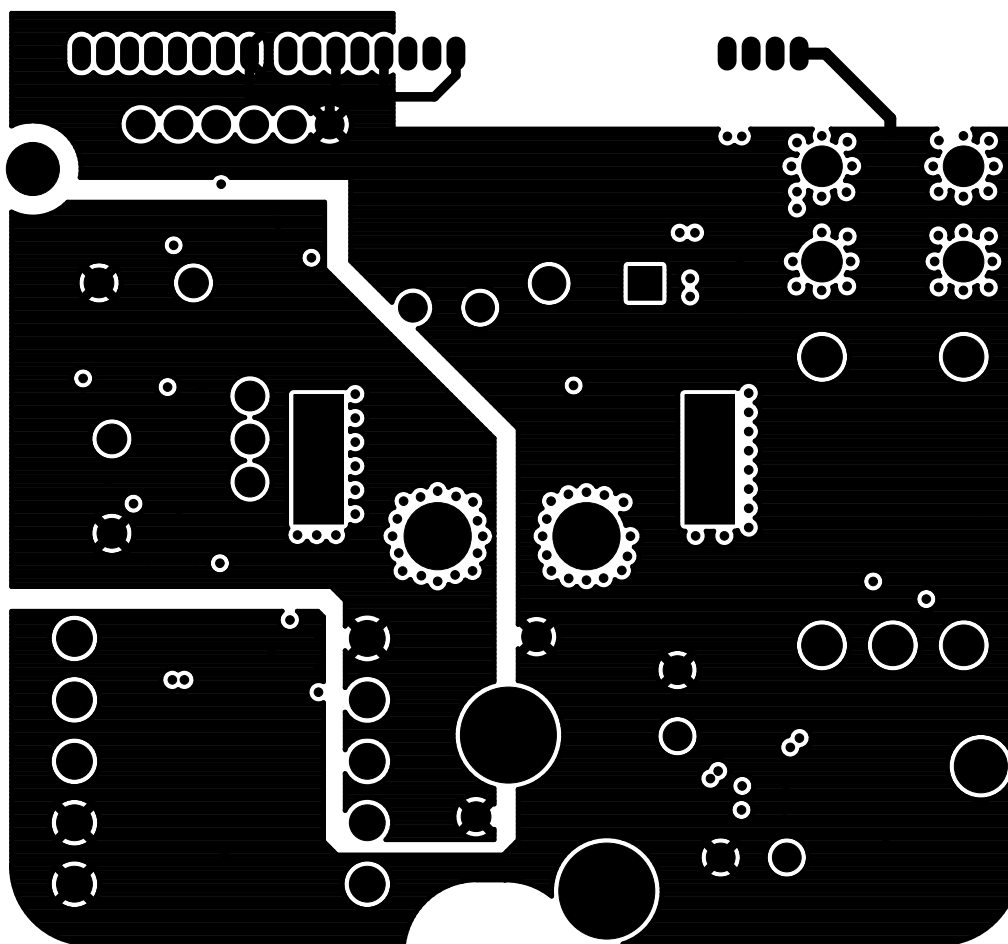


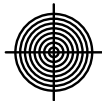
PRIMARY SIDE



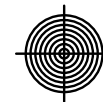
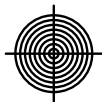
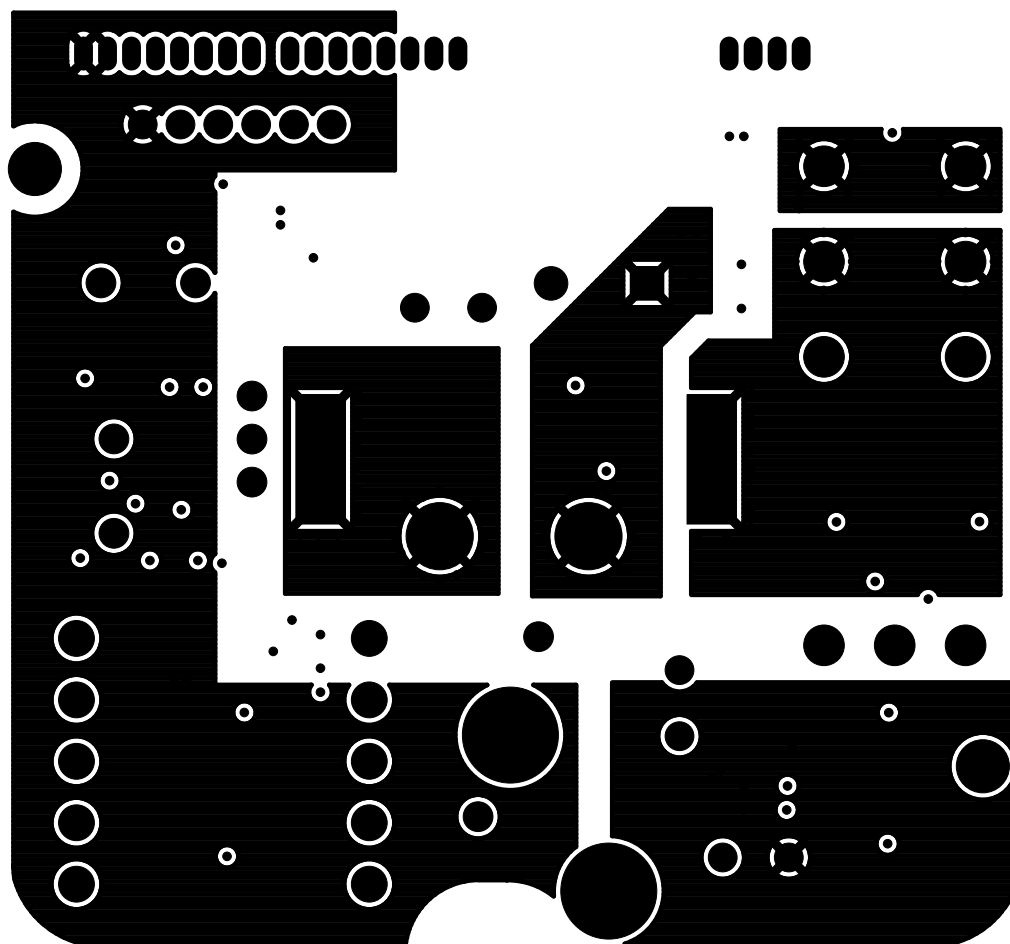


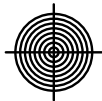
GROUND PLANE



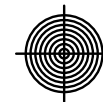
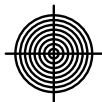
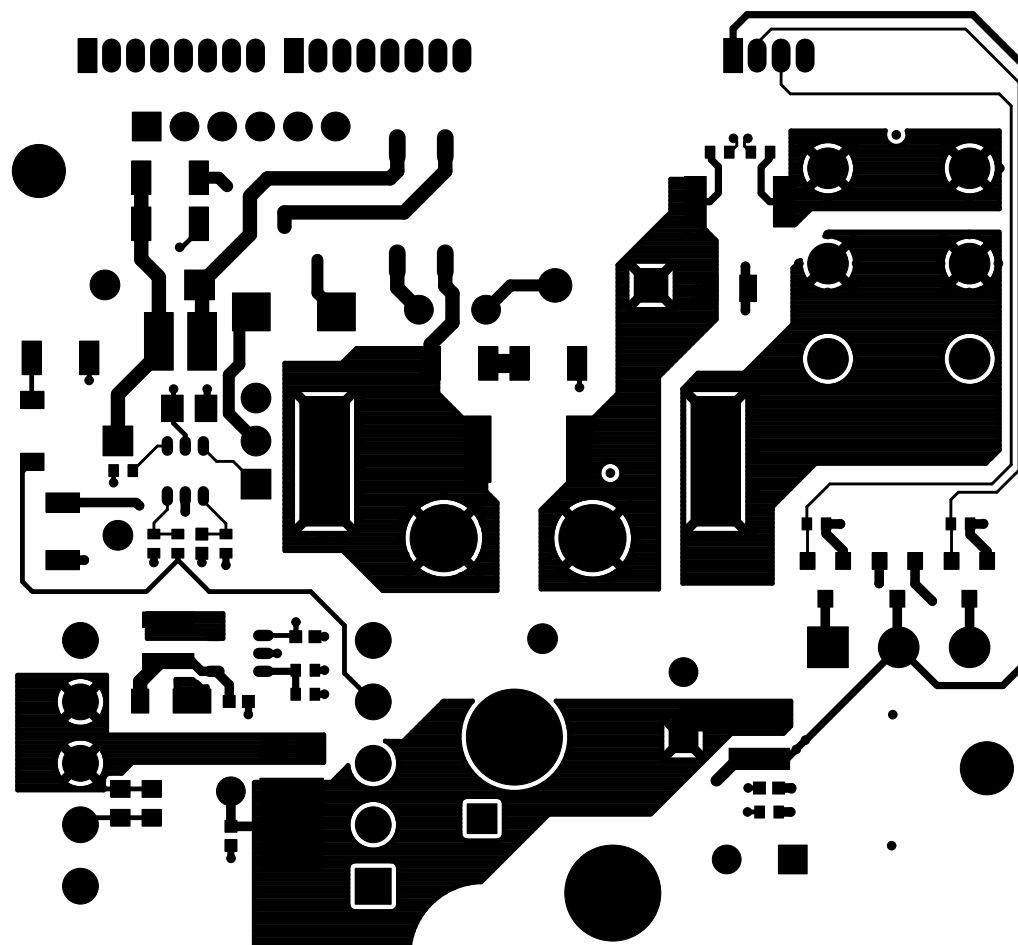


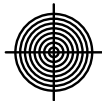
POWER PLANE



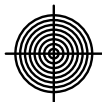
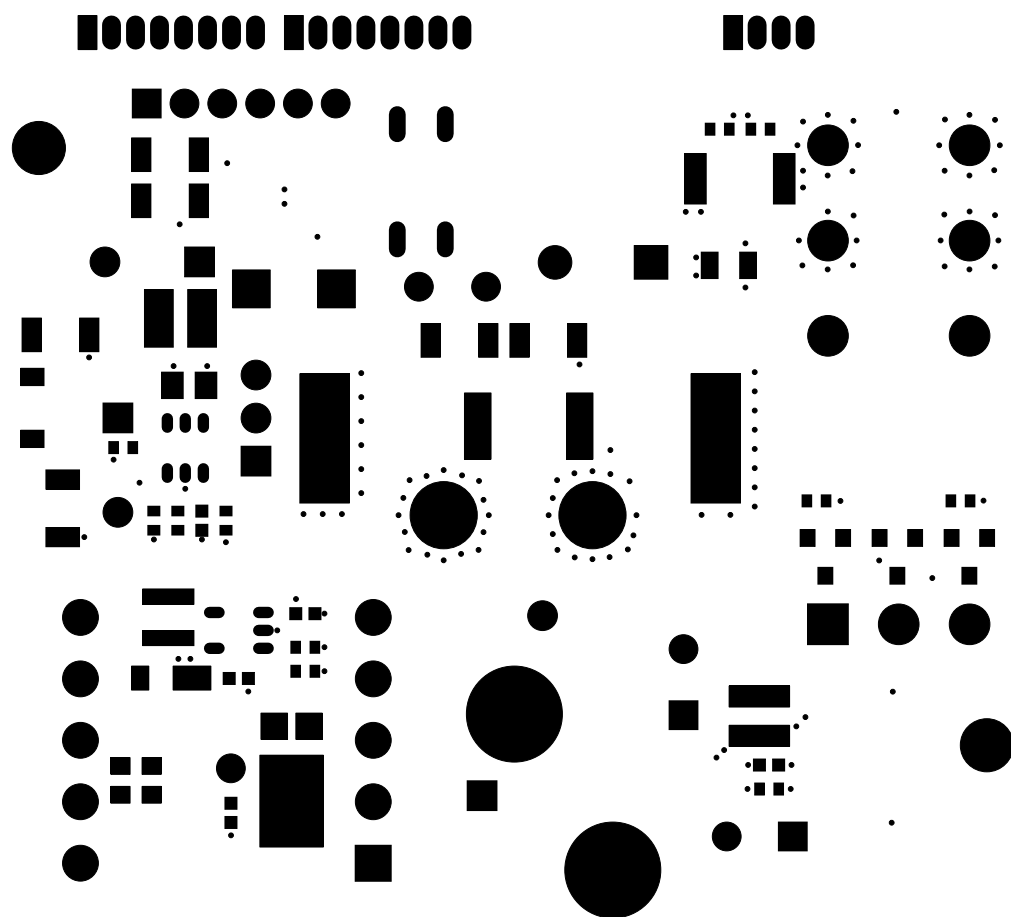


SECONDARY SIDE



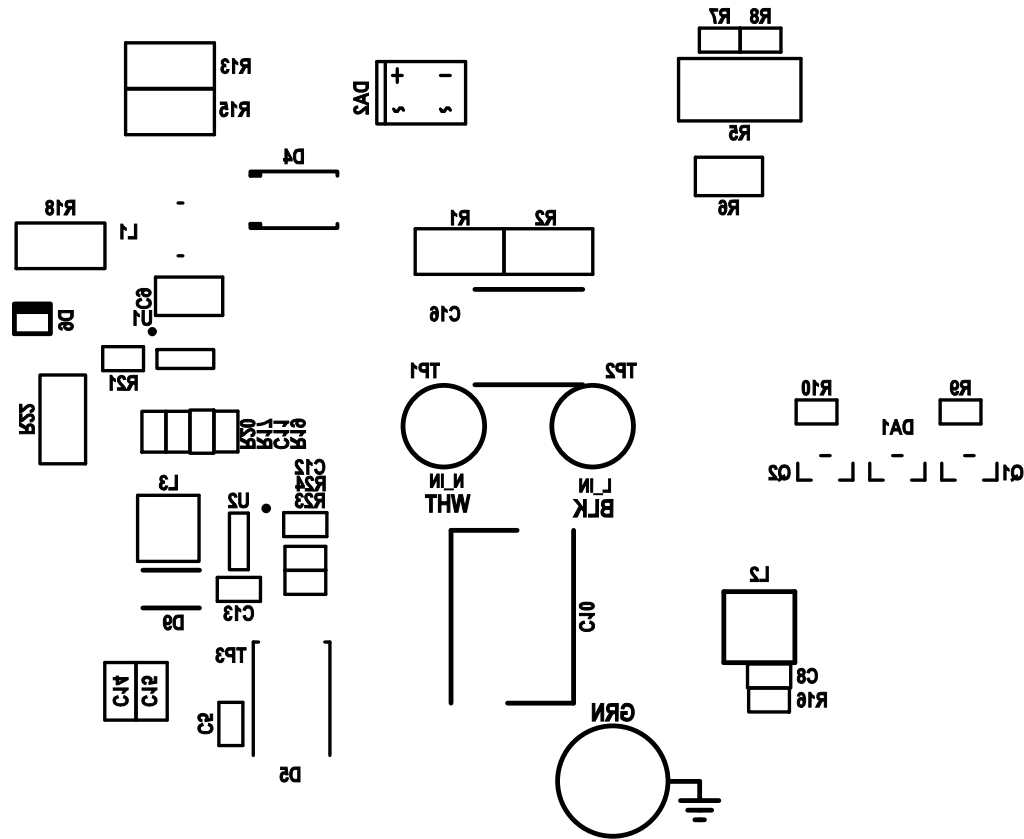


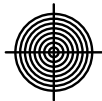
SECONDARY SOLDER MASK



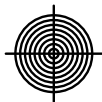


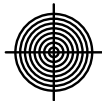
SECONDARY SILKSCREEN



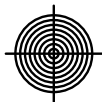
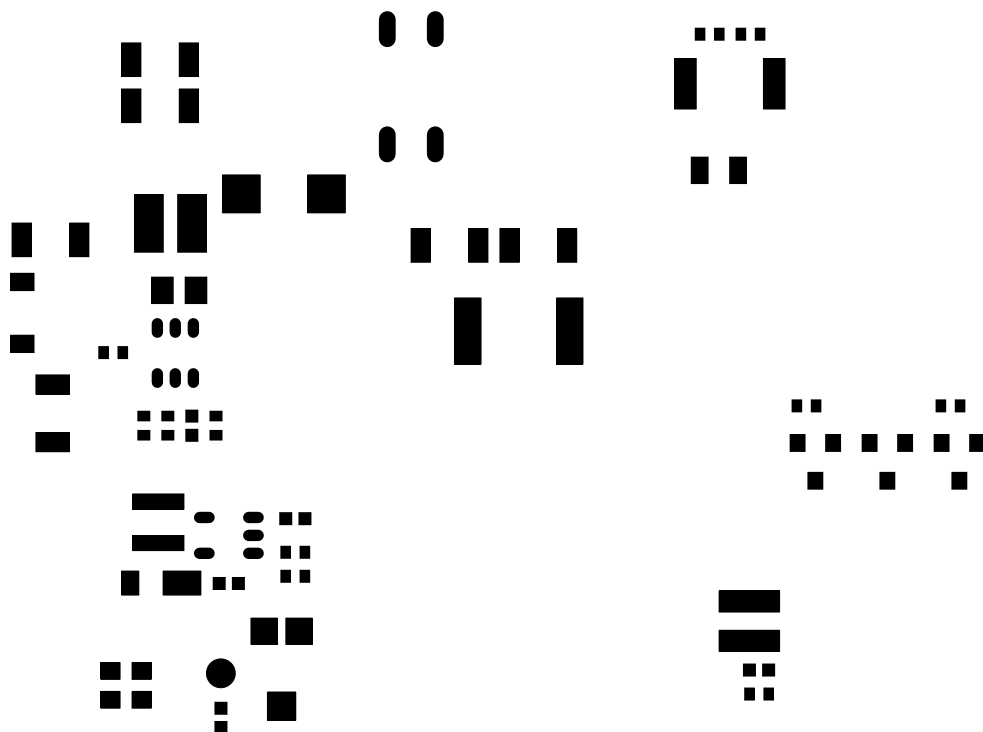


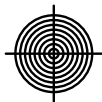
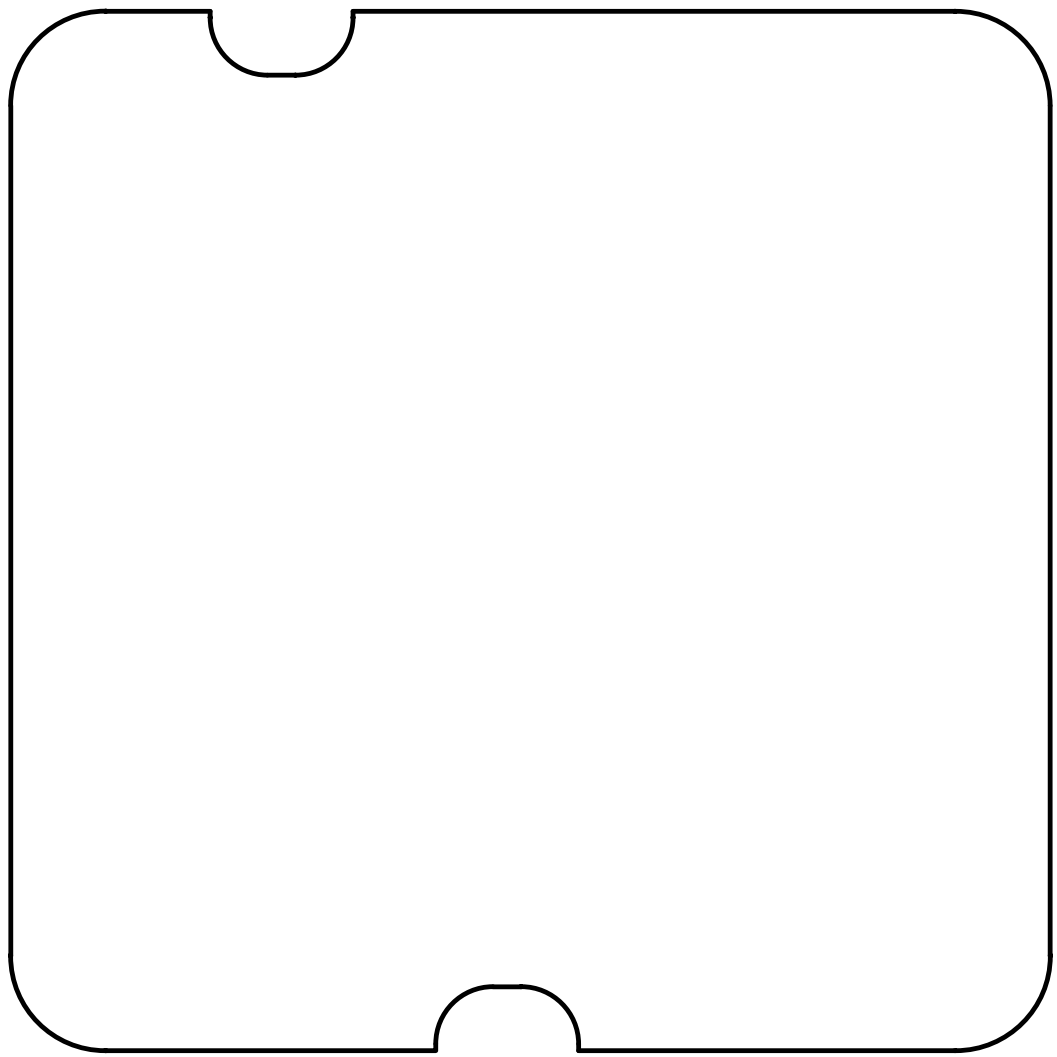
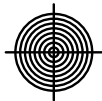
PRIMARY SOLDER PASTE

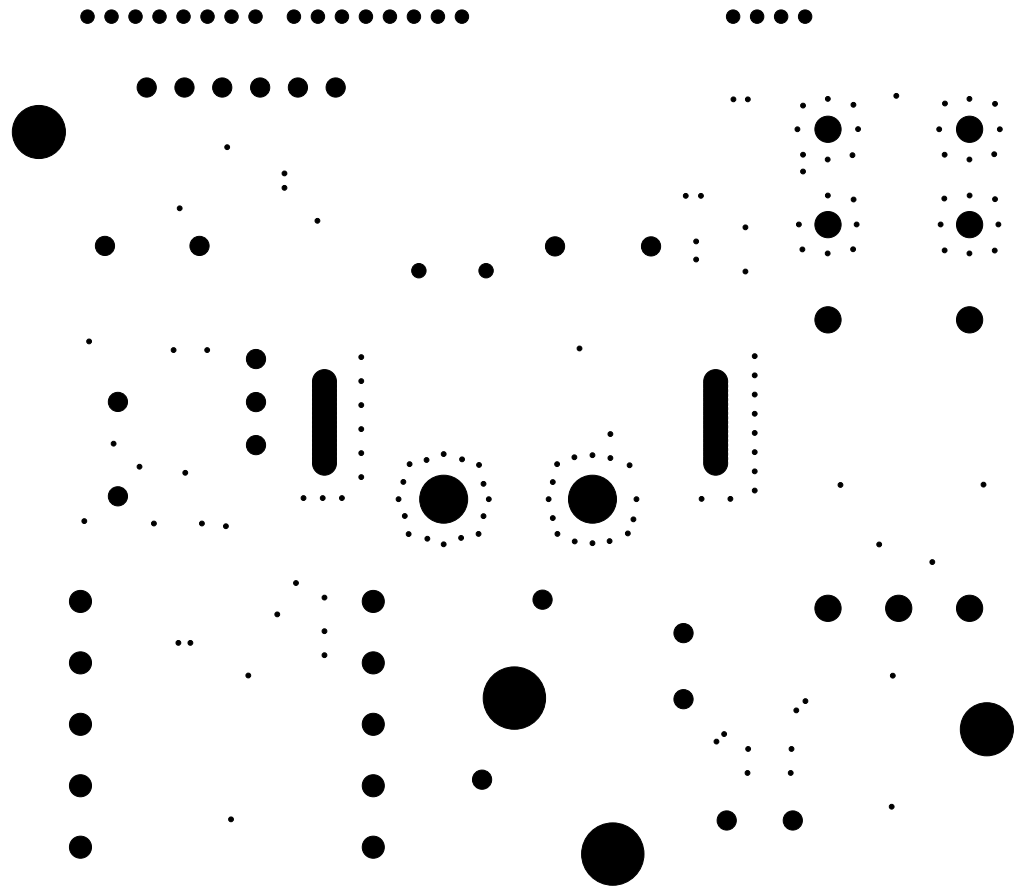


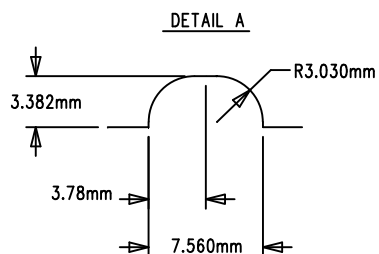
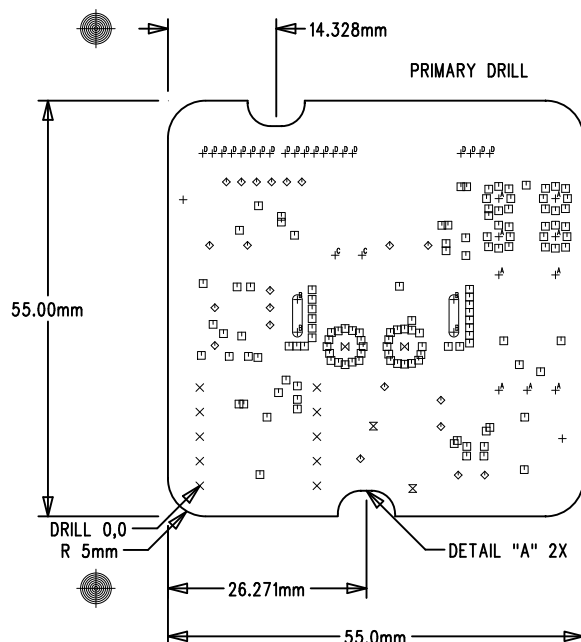


SECONDARY SOLDER PASTE









NOTES : UNLESS OTHERWISE SPECIFIED

- MANUFACTURE IN ACCORDANCE WITH IPC-6012, TYPE 3, CLASS 2.
- END PRODUCT FEATURES SHALL NOT VARY MORE THAN 20% FROM ARTWORK ORIGINALS.
- LAMINATE AND PREPREG SHALL BE AS PER IPC-4101/26,83,98 WITH A DECOMPOSITION TEMPERATURE $\geq 345^{\circ}\text{C}$, COLOR, NATURAL.
- COPPER WEIGHT SHALL BE 1.0 OZ./SQ. FT. BEFORE PLATING.
- ALL PLATED THROUGH HOLES SHALL HAVE A MINIMUM OF 0.001" COPPER.
- DRILL HOLE TOLERANCE AFTER PLATING SHALL BE ± 0.003 ".
- MINIMUM ANNULAR RING SHALL BE 0.001".
- MINIMUM ANNULAR RING AT EMERGENT CONDUCTORS SHALL BE 0.003".
- FINAL PCB THICKNESS SHALL BE 0.062" $\pm 10\%$.
- WARP/TWIST SHALL NOT EXCEED 1.0%.
- FINISH SHALL BE LPI, BLACK SMOBC, ENIG BOTH SIDES.
- SILKSCREEN WITH NONCONDUCTIVE WHITE EPOXY INK.
- REFERENCE ADDITIONAL FAB NOTES IN FILE README.TXT

LAYER STACKUP

FILE NAMES

PRIMARY SILKSCREEN	A81_PSS.PHO
PRIMARY SOLDERMASK	A81_PSM.PHO
PRIMARY SIDE	A81_PRI.PHO
GROUND PLANE	A81_L02.PHO
POWER PLANE	A81_L03.PHO
SECONDARY SIDE	A81_SEC.PHO
SECONDARY SOLDERMASK	A81_SSM.PHO
SECONDARY SILKSCREEN	A81_SSS.PHO

SCALE: NONE

SIZE	QTY	SYM	PLATED	TOL
0.111	2	+	NO	+/-0.003
0.046	10	X	YES	+/-0.003
0.01	131	□	YES	+/-0.003
0.04	21	◇	YES	+/-0.003
0.13	2	⊗	YES	+/-0.003
0.1	2	⊗	YES	+/-0.003
0.055	9	+ ^A	YES	+/-0.003
0.05 x 0.22	2	+ ^B	YES	+/-0.003
0.03	2	+ ^C	YES	+/-0.003
0.028	20	+ ^D	YES	+/-0.003

UNLESS OTHERWISE SPECIFIED			THIS DOCUMENT CONTAINS PROPRIETARY INFORMATION AND SHALL NOT BE DUPLICATED OR USED FOR ANY PURPOSE OTHER THAN THAT FOR WHICH PROVIDED OR DISCLOSED IN WHOLE OR IN PART, WITHOUT THE WRITTEN CONSENT OF SILICON LABORATORIES, INC..			COMPANY:		 400 W Cesar Chavez AUSTIN, TX 78701 (512)416-8500 www.silabs.com		
DIMENSIONS ARE IN INCHES AND APPLY AFTER FINISH										
INTERPRET DRAWING PER MIL-D-1000										
TOLERANCES										
HOLE TOLERANCES PER 78027			NAME: IST-A0081						REV 2.0	
DECIMALS .XX +/- .XXX +/-	ANGLES +/-	SURFACES MICRINCHES ✓								
PART TO BE FREE OF BURRS			DESIGN	YAS	01SEP2016		SIZE A	PART NUMBER:		
LAYOUT	CT	12SEP2016		SCALE 1:1	FABRICATION DRAWING			SHEET 1 OF 1		
BREAK EDGES MAX			BEND RADIUS MAX			BEND RELIEF MAX			DO NOT SCALE DRAWING	

REV : 2.0