



PS1012 – Security Vulnerability Disclosure Policy

Revision B

Revision Control Instructions

Post Hardcopy Revisions: Yes

Items for Hardcopy Release: All

Posted Locations: CRISIS006_Product Security Incident Response Plan (PSIRP) Dropbox Folder
External Website (<https://www.silabs.com/security>)

Table of Contents:

1. Purpose	3
2. Scope	3
3. Abbreviations/Definitions	3
4. Responsibilities	3
5. Reporting Vulnerability	3
5.1 Vulnerability Communication	3
5.2 PSIRT Process	3
6. Disclosure Statement	4
7. Document Revision History	5

1. Purpose

The purpose of this document is to describe the expectations and limitations regarding addressing and disclosing of security incidents related to Silicon Lab products.

2. Scope

This document applies to the following scenarios:

- Newly discovered security vulnerabilities that occur with Silicon Labs products and are not already covered in published documents/forums
- Silicon Lab collateral (documents or products) that are being used or accessed in an unexpected manner

3. Abbreviations/Definitions

- PCN – Product Change Notification
- PSIRT – Product Security Incident Response Team
- RFI – Request for Information

4. Responsibilities

- Customers, Researchers - report suspected vulnerabilities to Silicon Labs
- Sales, FAEs – gather information and related vulnerability details from the customer
- Applications – assess and confirm reproducibility related to security issue

5. Reporting Vulnerability

5.1 Vulnerability Communication

Vulnerabilities or suspicious functionality in products or software may be reported by customers, Silicon Labs employees, researchers, or other interested parties.

When a security vulnerability is suspected an email to be sent to product-security@silabs.com. An acknowledgement from the PSIRT will occur within 72 hours of receipt of the finding. Reporting content should include:

- The product(s) showing the vulnerability
- Product application/usage summary
- Steps and/or environment needed to reproduce/cause the issue

Further dialog on the issue will be pursued with the reporting entity by the PSIRT in a secure manner using a PGP/GPG key available at <https://www.silabs.com/security/product-security>.

5.2 PSIRT Process

The PSIRT works with other Silicon Labs groups including Applications, Developers, Sales and Marketing to assess reported vulnerabilities, perform technical analysis and determine an appropriate response. The key processes for addressing vulnerabilities include:

- Triage: This involves active dialog between the PSIRT and the reporting entity, Applications Support team, as well as the Engineering Design team, to determine what is needed to reproduce the vulnerability.
- Technical Analysis and Disposition: Includes the actual confirmation of the validity of the security vulnerability based on the issue's evaluation and/or reproduction. The scope and impact or severity of the vulnerability are confirmed as well as a resolution or disposition decision. This may include a fix, work-around or acceptance of the identified vulnerability.



- Output: The level of disclosure beyond the reporting entity will depend on the severity and scope of the vulnerability.

6. Disclosure Statement

Silicon Labs intends to provide customers with the latest and accurate documentation about security related concerns associated with our products. There are multiple methods for disclosing security related updates including but not limited to:

- PCNs – Product Change Notifications
- Release Notes – Documents provided with the release of software
- Direct Customer Communication – Communication through Sales or Field Application Engineers
- Security Advisories – Technical summaries about a security issue and the recommended action for addressing it

The above notification types may be signed up for at www.silabs.com under [the customer profile settings](#).

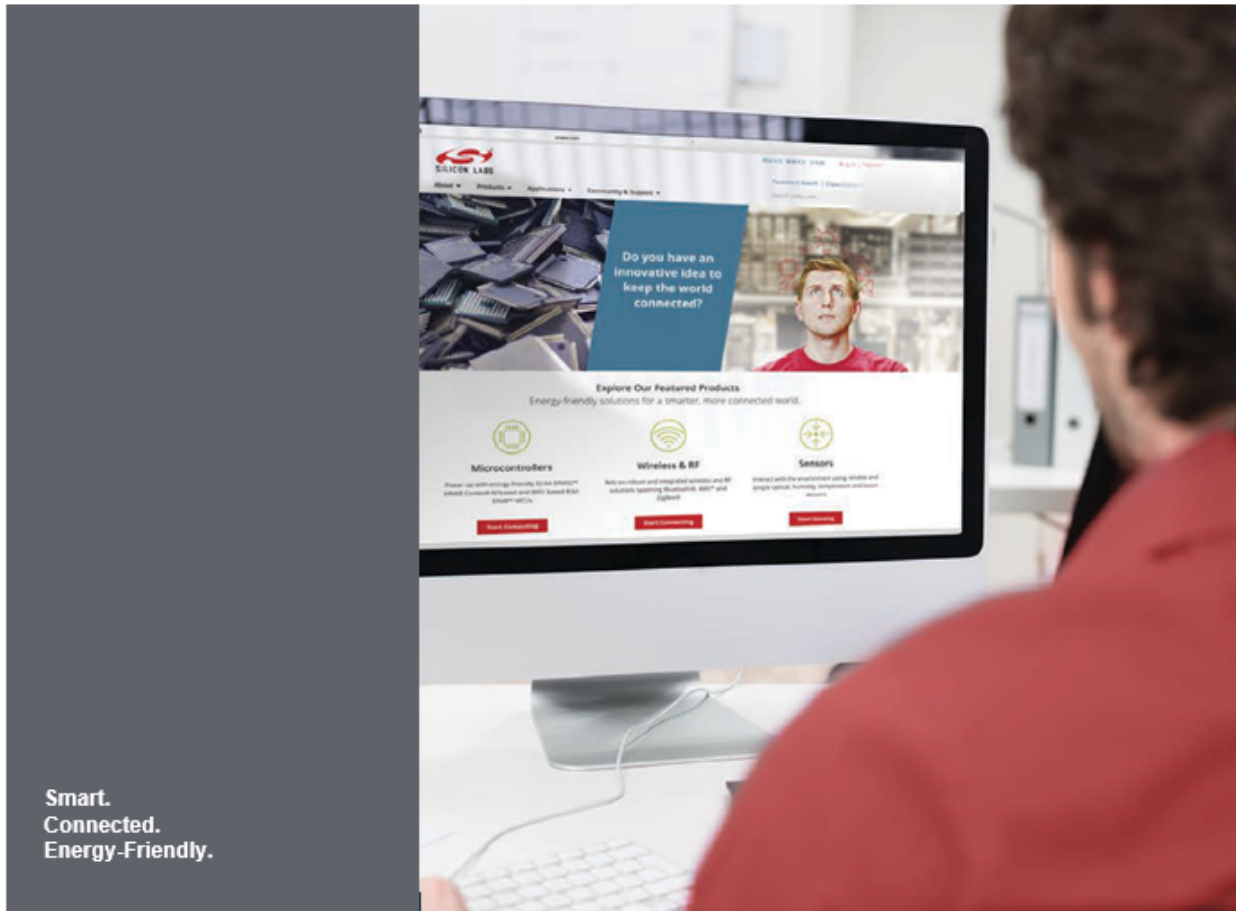
Use of products, by customers, must follow the provided specifications for operation to ensure proper functionality. In the event of a reported security concern Silicon Labs will analyze the details to assess the impact on Silicon Labs products or software, determine the associated technical cause, and provide an appropriate resolution and/or disclosure.

Silicon Labs reserves the right to adjust the (software/hardware) product if necessary for security or reliability reasons. Information sharing on vulnerabilities may take the form of release notes, PCNs, Incident Reports, Application Notes, and/or FAQs.

For details on the Terms & Conditions or product specific disclaimer content, please visit www.silabs.com. Requests for product related content not readily available at www.silabs.com may be requested through our [authorized sales channel](#).

7. Document Revision History

Rev.	Date	Initiator	Description of Changes
A	21-Feb-2019	Esther Alexander	Initial release
B	25-Feb-2020	Esther Alexander	Added some clarifications on methods to receive security disclosures in Section 6. Disclosure Statement Added disclaimer statement on final page



Smart.
Connected.
Energy-Friendly.



Products
www.silabs.com/products



Quality
www.silabs.com/quality



Support and Community
community.silabs.com

Disclaimer

Silicon Labs intends to provide customers with the latest, accurate, and in-depth documentation of all peripherals and modules available for system and software implementers using or intending to use the Silicon Labs products. Characterization data, available modules and peripherals, memory sizes and memory addresses refer to each specific device, and "Typical" parameters provided can and do vary in different applications. Application examples described herein are for illustrative purposes only. Silicon Labs reserves the right to make changes without further notice to the product information, specifications, and descriptions herein, and does not give warranties as to the accuracy or completeness of the included information. Without prior notification, Silicon Labs may update product firmware during the manufacturing process for security or reliability reasons. Such changes will not alter the specifications or the performance of the product. Silicon Labs shall have no liability for the consequences of use of the information supplied in this document. This document does not imply or expressly grant any license to design or fabricate any integrated circuits. The products are not designed or authorized to be used within any FDA Class III devices, applications for which FDA premarket approval is required, or Life Support Systems without the specific written consent of Silicon Labs. A "Life Support System" is any product or system intended to support or sustain life and/or health, which, if it fails, can be reasonably expected to result in significant personal injury or death. Silicon Labs products are not designed or authorized for military applications. Silicon Labs products shall under no circumstances be used in weapons of mass destruction including (but not limited to) nuclear, biological or chemical weapons, or missiles capable of delivering such weapons. Silicon Labs disclaims all express and implied warranties and shall not be responsible or liable for any injuries or damages related to use of a Silicon Labs product in such unauthorized applications.

Trademark Information

Silicon Laboratories Inc.®, Silicon Laboratories®, Silicon Labs®, SiLabs® and the Silicon Labs logo®, Bluegiga®, Bluegiga Logo®, ClockBuilder®, CMEMS®, DSPLL®, EFM®, EFM32®, EFR®, Ember®, Energy Micro, Energy Micro logo and combinations thereof, "the world's most energy friendly microcontrollers", Ember®, EZLink®, EZRadio®, EZRadioPRO®, Gecko®, Gecko OS, Gecko OS Studio, ISOmodem®, Precision32®, ProSLIC®, Simplicity Studio®, SiPHY®, Telegesis, the Telegesis Logo®, USBXpress®, Zentri, the Zentri logo and Zentri DMS, Z-Wave®, and others are trademarks or registered trademarks of Silicon Labs. ARM, CORTEX, Cortex-M3 and THUMB are trademarks or registered trademarks of ARM Holdings. Keil is a registered trademark of ARM Limited. Wi-Fi is a registered trademark of the Wi-Fi Alliance. All other products or brand names mentioned herein are trademarks of their respective holders.



Silicon Laboratories Inc.
400 West César Chavez
Austin, TX 78701
USA

<http://www.silabs.com>